

PULSE GENERATOR  
PERFORMANCE CHECK

Model: AV-1002-C

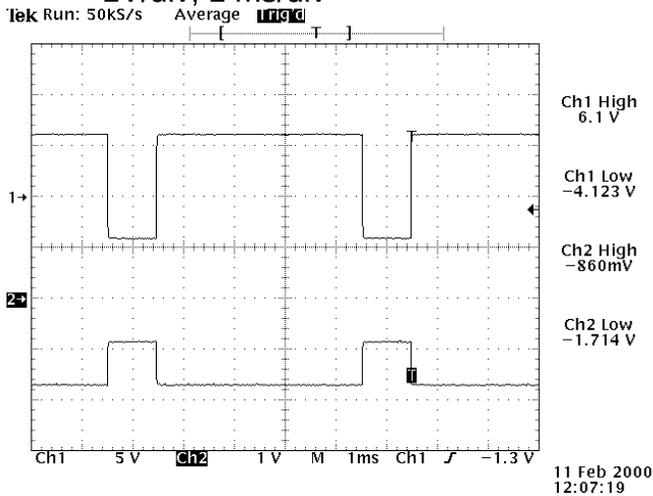
S.N.: 9146

Date: February 11, 2000

Wide Pulse Test:

Top: OUT, -10V with +6V offset, 1 ms PW, into 50  $\Omega$   
5V/div, 1 ms/div

Bot: LOGIC output into 50  $\Omega$  load, set to ECL mode  
1V/div, 1 ms/div



Rise Time Test:

Top: +5V into 50 $\Omega$ , at minimum pulse width and maximum pulse repetition frequency. 2V/ div, 20 ns/div  
Bottom: -5V into 50 $\Omega$ , similar conditions

a) Output Signal Amplitude: 0 to  $\pm 10V$ , plus TTL and ECL outputs.

b) DC Offset: 0 to  $\pm 10V$

c) Pulse Width: 50 ns - 1 ms

d) Rise Time: 5 ns

e) Fall Time: 5 ns

f) PRF: 1 Hz - 10 MHz

g) Jitter, Stability: OK

h) Prime Power: 120/240V AC, 50-60 Hz,