

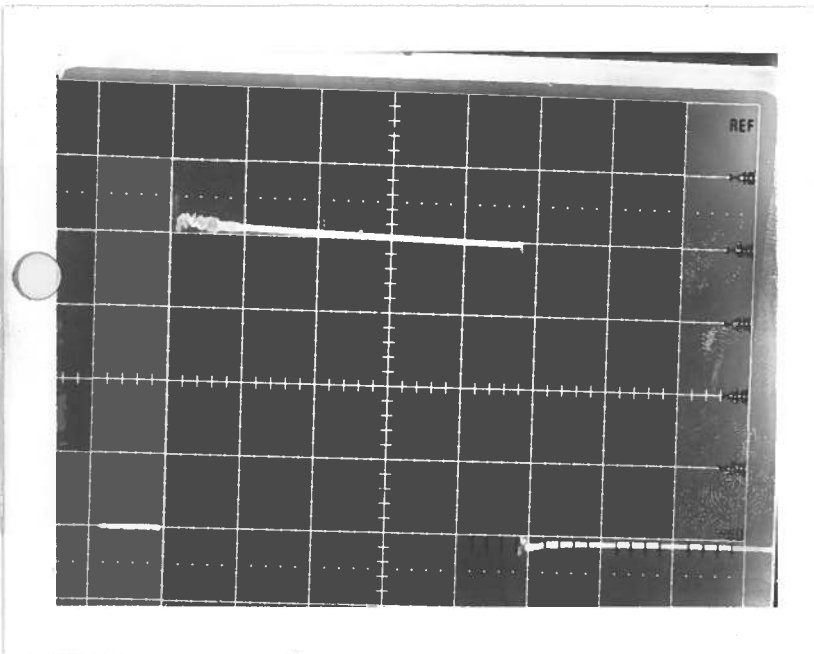
PULSE GENERATOR

PERFORMANCE CHECK

Model: AVR-A1-EA-PS-P

S.N.: 1750

Date: MAY 14 1984



- a) Output signal amplitude:
0 TO 1200 VOLTS
TO 50 OHMS
- b) Pulse width:
50 NSEC TO 1.0 USEC
- c) Rise time:
≤ 10 NSEC
- d) Fall time:
≤ 10 NSEC
- e) PRF:
0 TO 100 KHZ
0.5% MAX DUTY CYCLE
- f) Jitter, stability:
OK
- g) Prime power:

50 VOLTS / DIV

200 NSEC / DIV

4.0 KHZ

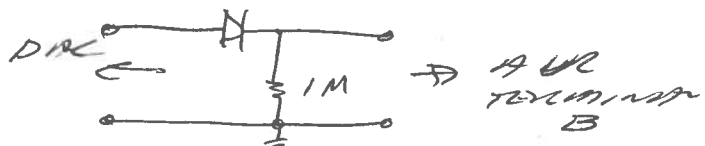
$R_L = 50 \Omega$

110/220V

50 - 60 HZ

NOTE

IF CONTROLLER OUTPUT AMPLITUDE USING DAC WITH ±10 VOLT OUTPUT, USE FOLLOWING CIRCUIT TO PROTECT AVR INPUT:

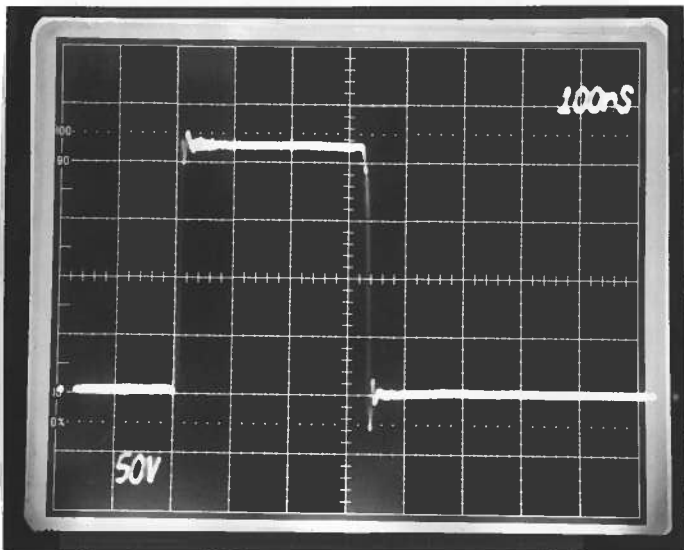


PULSE GENERATOR
PERFORMANCE CHECK

Model: *AVR-A-1-CA-P5-P*

S.N.: *1750 (MOD)*

Date: *MAY 30 1996*



- a) Output signal amplitude: *0 TO +200 V & 0 V*
- b) Pulse width: *($R_L = 50 \Omega$)
50 NS TO 1.0 μ S*
- c) Rise time: *(0.5% MAX DUTY CYCLE)
 ≤ 10 NS*
- d) Fall time: *≤ 10 NS*
- e) PRF: *0 TO 100 KHz
(0.5% MAX DUTY)*
- f) Jitter, stability: *CYCLE*
OK
- g) Prime power: *120 / 240 V
50 - 60 Hz*

*$R_L = 50 \Omega$
PRF = 10 KHz*