

PERFORMANCE CHECK

Model:

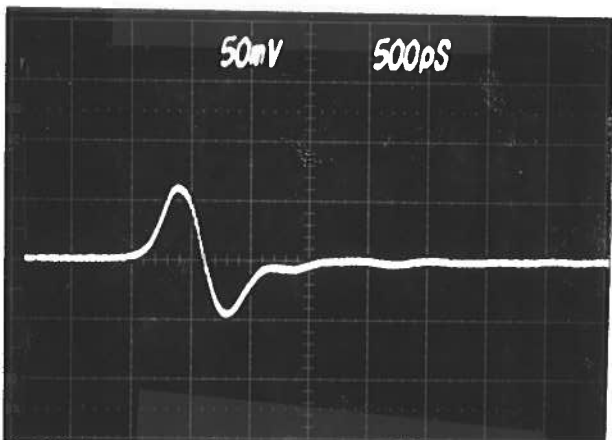
M/D-WH

S.N.:

3880

Date:

SEPT 24 87



a) Output signal amplitude, V_{pp} :

≈ 10 VOLTS

b) Spurious signals WRT peak:

≤ 26 db

c) Waveforms:

1.0 GHz

d) Prime power:

$+15 \pm 0.5$ V 250 mA
(max)

e) Tuning range:

NA

f) Symmetry:

OK

g) Stability:

OK

40 db ATTEN

5 VOLTS / DIV

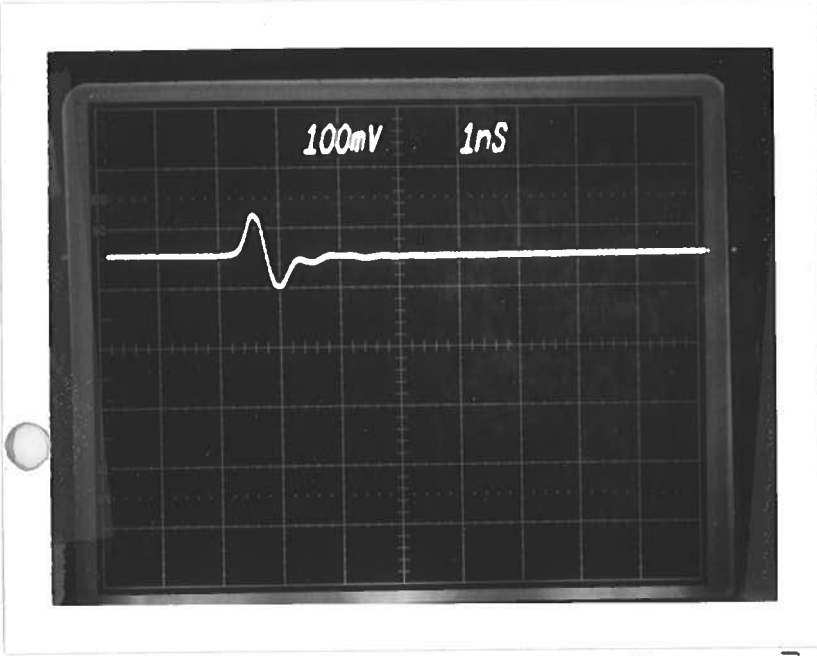
5.0 MHz (PRF)

PERFORMANCE CHECK

Model: *AN-441*

S.N.: *3880 REPAIRD FEB 19 88*

Date: *FEB 19 88*



a) Output signal amplitude, V_{pp} :

≥ 10 VOLTS

b) Spurious signals WRT peak:

c) Waveforms:

d) Prime power:

$+15 \pm 0.5$ VOLTS

e) Tuning range:

NA

f) Symmetry:

OK

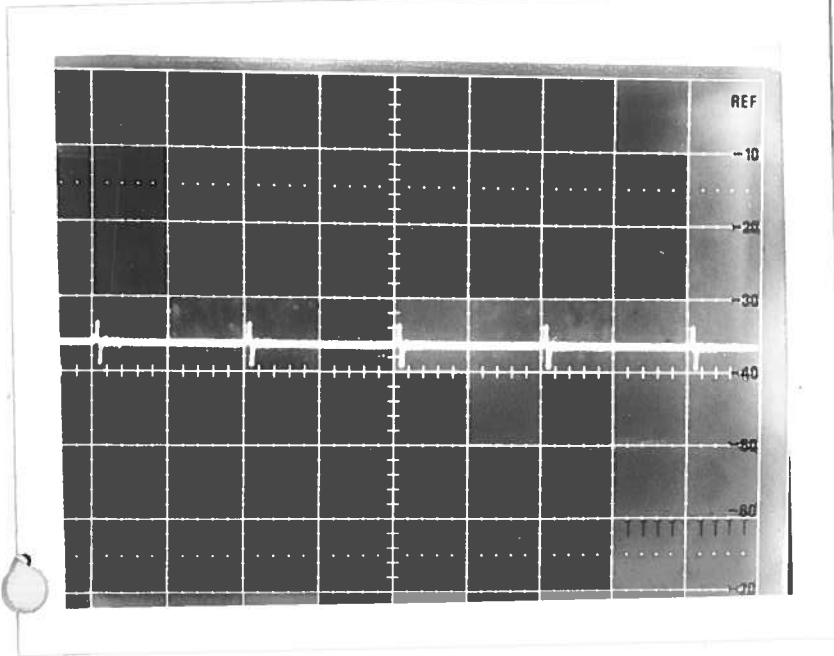
g) Stability:

OK

h) TEMP RANGE:

$20^{\circ}C \pm 10^{\circ}C$

(A) 10 VOLTS/DIV 5 MHz PRIF



(B) RISE TIME SLOPE DISPLAY PRIF = 5 MHz. 100 nSEC/DIV.