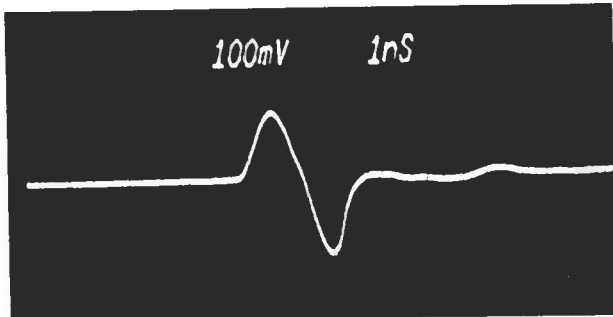


PERFORMANCE CHECK

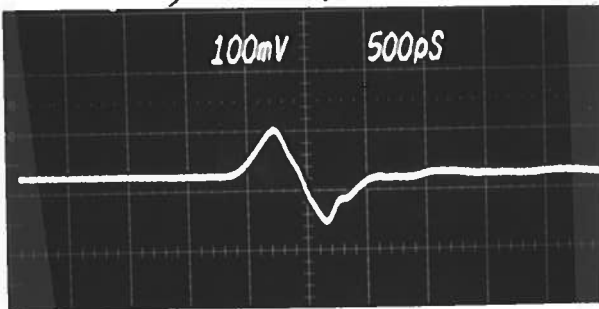
Model: *AND-655A/C*

S.N.: *6058*

Date: *JAN 14 1992*



(A) 50 dB ATTEN
∴ ≈ 32 VOLTS/DIV
PRF ≈ 50 KHz
f ≈ 500 MHz



(B) AS (A) BUT
f ≈ 10 GHz.

a) Output signal amplitude, V_{pp} :

≥ 50 VOLTS

b) Spurious signals WRT peak:

≤ 26 dB

c) Waveforms:

500 - 1000 MHz.

d) Prime power:

+12 VOLTS, 120 mA

-12 VOLTS, 60 mA

e) Tuning range:

500 TO 1000 MHz.

f) Symmetry:

OK

g) Stability:

OK

h) PRF:

0 TO 50 KHz

(C) INPUT TRIGGER:

(i) *+4 TO +30 VOLTS.*

PW ≥ 10 NS

INPUT CIRCUIT INCLUDE

250 V SERIES DC

BLOCKING CAPACITOR