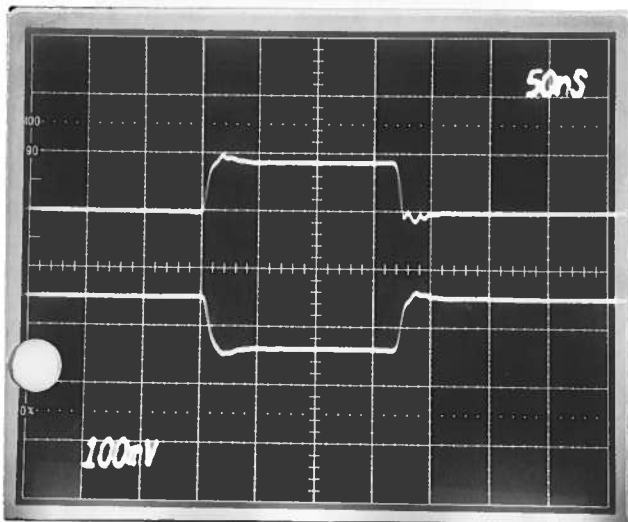


PULSE GENERATOR
PERFORMANCE CHECK

Model: *MV-155A-C-PN-CA-EO-M-ORTEL 2*

S.N.: *7278*

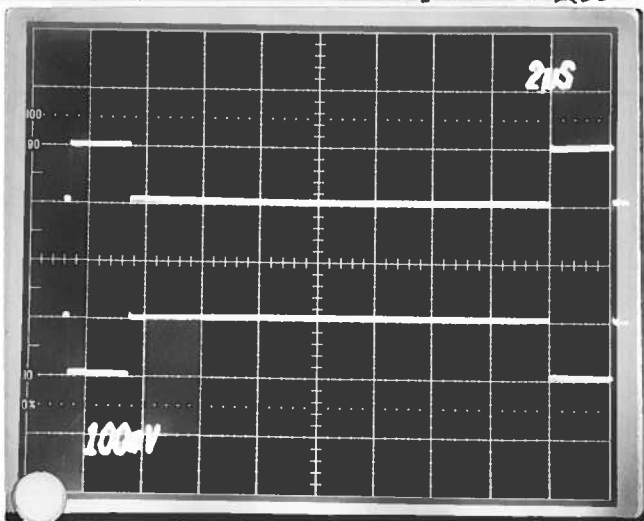
Date: *11 MAY 23 1995*



- a) Output signal amplitude:
PULSE: 0 TO ± 200 mA
- b) Pulse width:
OFFSET: 0 TO ± 200 mA
20 NS TO 2.00 S
- c) Rise time:
≤ 10 NS
- d) Fall time:
≤ 10 NS
- e) PRF: *0 TO 10 MHz.*

Ⓐ INT MODE, PRF ≈ 1 MHz
OUTPUT VOLTAGE ACROSS $R_L = 5.0 \Omega$
TOP: POS OUT (1 V OUT / DIV)
BOT: NEG OUT (200 mA / DIV)

- f) Jitter, stability:
OK
- g) Prime power:
120 / 240 V
50 - 60 Hz.



[Handwritten signature]

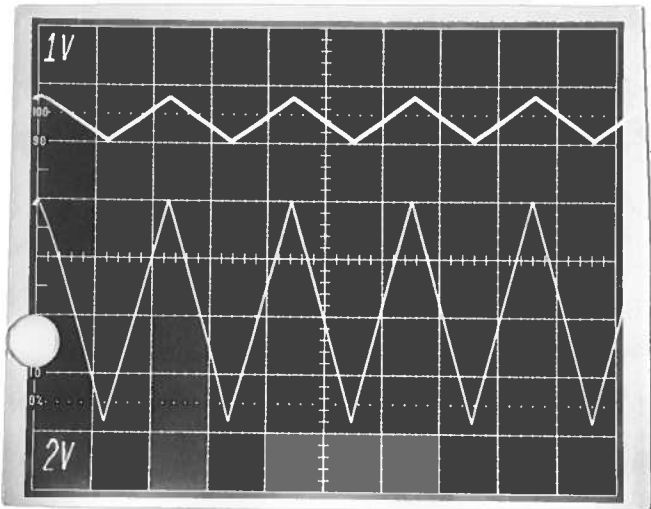
Ⓑ 105 MHz BUT PRF 250 KHz
& WIDE PULSE & AMPLITUDE
ADJUSTED SLIGHTLY.

PULSE GENERATOR
PERFORMANCE CHECK

Model:

S.N.: 7278 CONT.

Date:



POTENTIAL
ACROSS $R_L = 5\Omega$

a) Output signal amplitude:

b) Pulse width:

c) Rise time:

d) Fall time:

e) PRF:

f) Jitter, stability:

g) Prime power:

© BOTTOM: 8 V PP 1 MHz
INPUT TO ED TERMINAL
A.

TOP CORRESPONDING OUTPUT
VOLTAGE ACROSS
RESISTIVE LOAD
OF 5.0Ω .

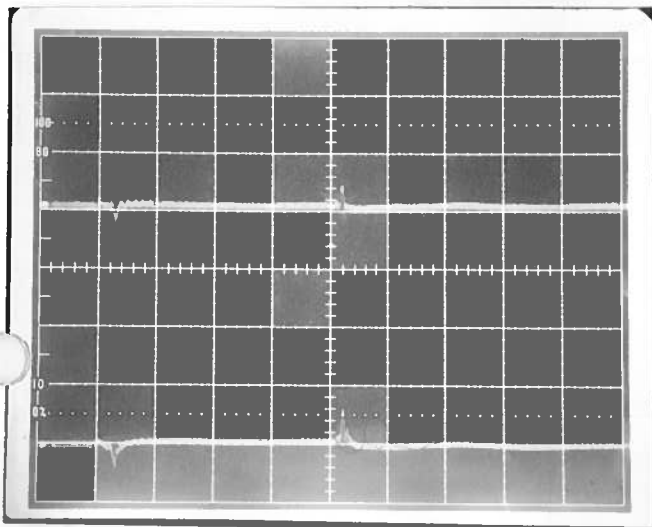
(500 NS/DIV)

PULSE GENERATOR
PERFORMANCE CHECK

Model: *AV-155A-C -PW-EA-ED-MA - ORTEL2*

S.N.: *7278 (MODIFIED)*

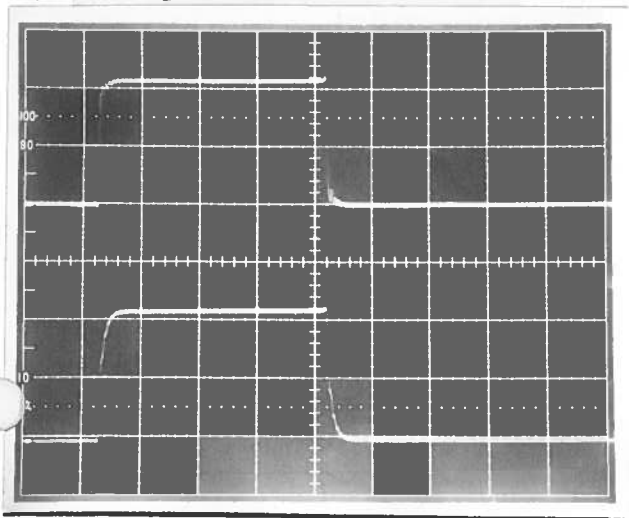
Date: *JUNE 8 1995*



- a) Output signal amplitude:
Pulse: 0 TO ±200 mA
PRF: 0 TO ±200 MHz
- b) Pulse width:
20 ns TO 2.0 μs
- c) Rise time:
≤ 10 ns
- d) Fall time:
≤ 10 ns
- e) PRF:
0 TO 10 MHz
- f) Jitter, stability:
OK.

Ⓐ *200 ns / DIV*
R_L = 10 Ω

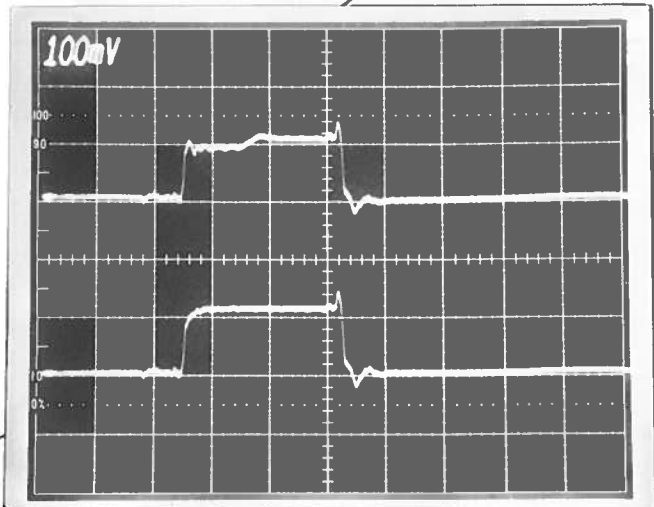
AMP + DC BIAS SET TO ZERO



Ⓑ *200 ns / DIV*
R_L = 10 Ω

g) Prime power: *120 / 240 V 50-60 Hz*

M_{OUT}
500 mV / DIV
R_L = 50 Ω



V_{LOAD}
1.0 V / DIV

Ⓒ *IN 459A DIODE AS TEST LOAD*
M_{OUT}, 20 mA / DIV
TOP: DC BIAS SET TO ZERO
BOTTOM: 1 mA TO BIAS ADDED