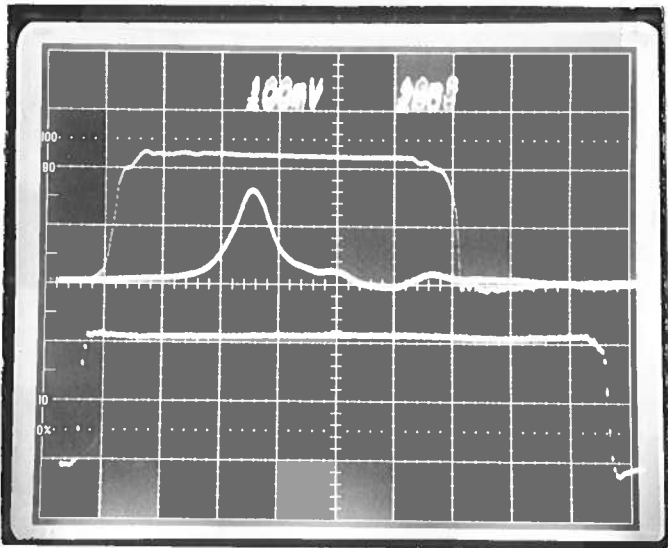


PULSE GENERATOR
PERFORMANCE CHECK

Model: *AVR-A-1-51-B-P-NRL2*

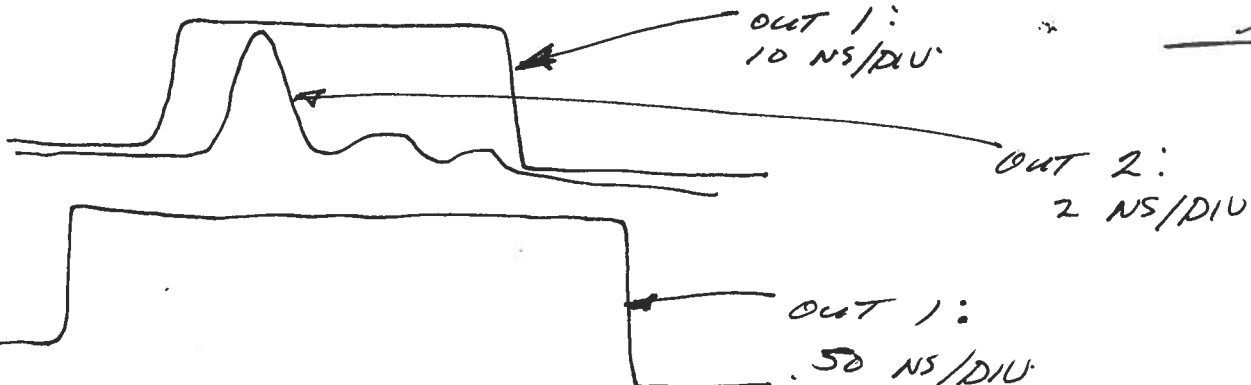
S.N.: *8832*

Date: *APRIL 29 1999*



- a) Output signal amplitude:
OUT 1: 0 TO +200V (TO 50Ω)
- b) Pulse width:
OUT 1: 50 NS TO 500 NS (0.5% MAX DUTY CYC)
- c) Rise time:
OUT 2: ≤ 2 NS FWHM (FIXED)
OUT 1: ≤ 2 NS
- d) Fall time:
OUT 2: ≤ 1 NS
OUT 1: ≤ 2 NS
- e) PRF:
0 TO 100KHz, 0.5% MAX DUTY CYCLE.
- f) Jitter, stability:
OK
- g) Prime power: *120/240V*
50-60 Hz

60 db ATTEN - .100V/DIV



[Signature]



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 U.S.A. 13669-0265
 TEL: (315) 472-5270
 FAX: (613) 226-2802

TEL: 1-800-265-6681
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P.O. BOX 5120 STN. F
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 CANADA K2C 3H4
 TEL: (613) 226-5772
 FAX: (613) 226-2802

"-B" Functional Test & Calibration Certificate

Date of test:	June 23, 1999				Tester:	MJC
Programmed model name:	AVR-A-1-S1-B-P-NRL2 (Repair)					
Programmed serial number:	8832					
Firmware revision:	1.95					
Internal trigger checked at:	1 Hz	100 Hz	10 kHz	100 kHz		
Actual measured output ¹ :	1.000 Hz	100.1 Hz	9.99 kHz	99.8 kHz		
External trigger checked:	Yes					
Trigger load resistor present:	200 Ohms					
Manual trigger checked:	Yes	No spurious trigger in manual mode for output on/off/on:				Yes
Pulse compression checked:	Yes					
Pulse width checked at:	50 ns	100 ns	200 ns	500 ns		
Actual measured output ² :	50.5 ns	97.5 ns	205 ns	510 ns		
DC mode checked:	N/A					
PWin = PWout mode checked:	N/A					
Duty Cycle Limit:	0.5%					
Delay nulled:	Yes (TTL trigger)					
Delay checked at:	0	100 ns	1 us	CH1 only. Approx 40ns propagation delay.		
Actual measured output ¹ :	42 ns	136 ns	1.036 us			
Double pulse checked:	N/A					
Gate checked:	Yes					
Invert mode checked:	N/A					
ECL/TTL modes checked:	N/A					
Zout switch checked:	N/A					
Amplitude checked at:	50V	100V	150V	200V	CH1 only	
Actual measured output ² :	50.4V	100.8V	149.6V	202V		
Amplitude polarity:	Positive					
Zout calibration:	N/A					
Electronic amplitude control:	N/A					
External amplify mode:	N/A					
Monitor V/I Ratio:	N/A	Monitor offset nulled:				
LCD Monitor calibrated:	N/A	Monitor offset nulled:				
Mon. Single Pulse/Min PW OK:	N/A	SHA Cap:				
Offset checked at:	N/A					
Actual measured output ² :	N/A					
Offset nulled (output on):	N/A					
Offset nulled (output off):	N/A					
RS-232 checked:	Yes					
Sync pulse width checked:	200 ns					
Circuit Boards:	PS:	93	Main:	86		
Overload Trigger Resistance:	Trips at:		Installed:	6.2k		
DC fuses:	Positive:	1A	Negative:	N/A		
AC Current at 115 VAC:	Quiescent:	0.48A	Max. Load:	0.62A		
Photographed:	Previously					

¹ Checked with: HP5370A Universal Time Interval Counter

² Checked with: Tektronix TDS360 digital oscilloscope for PW ≥ 5 ns,
 Tektronix 7704A/7S11/7T11/S4 sampling oscilloscope system for PW < 5 ns.