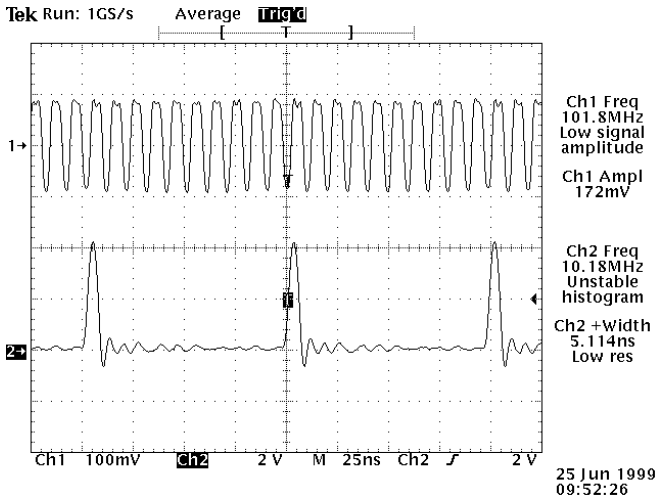


PULSE GENERATOR  
PERFORMANCE CHECK

Model: AVX-FD2-PS-IP-BNLA

S.N.: 8904

Date: July 5, 1999



Top: small signal input, 102 MHz,  
50Ω  $R_{IN}$  mode. 100mV/div, 25 ns/div  
Bottom: TTL output,  $\div 10$ , 5 ns pulse width, 50Ω load.  
2V/div, 25 ns/div.

a) Output Signal Amplitude: TTL,  $R_L \geq 50\Omega$

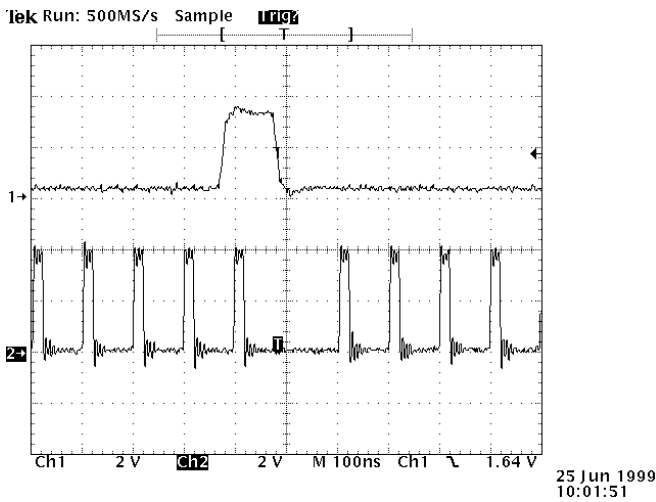
b) Pulse Width: 5 ns - 5  $\mu$ s

c) Division Factor: 2 to 255

d) PRF: 0 - 100 MHz

e) Jitter, Stability: OK

f) Prime Power: 120/240V AC, 50-60 Hz,



Top: RESET input.  
Bottom: TTL output,  $\div 10$ ,  $f_{IN}=100$ MHz, 50Ω load.  
Both: 2V/div, 100 ns/div.