

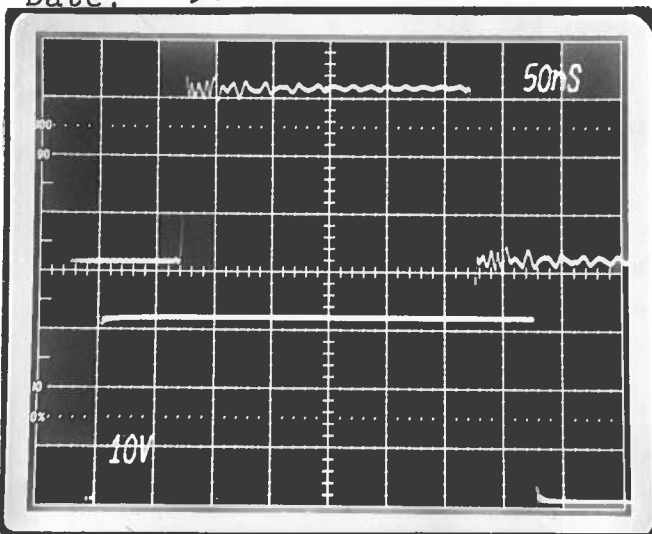
PULSE GENERATOR

PERFORMANCE CHECK

Model: *AUR-122-B*

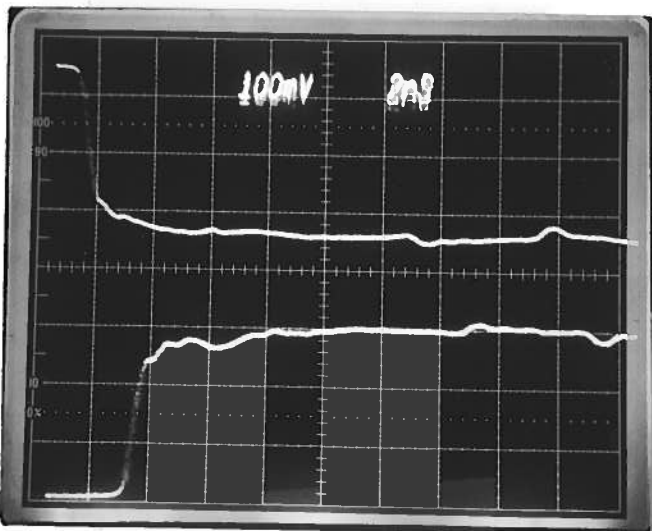
S.N.: *9028*

Date: *AUG 26 1999*



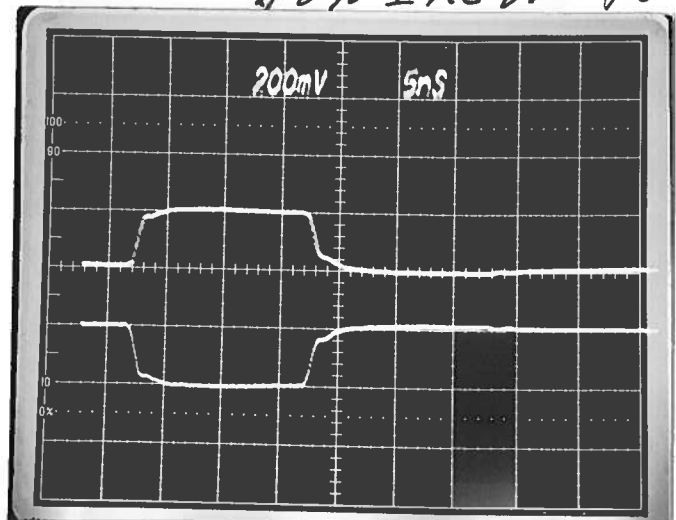
- a) Output signal amplitude:
 - 1) $1070 \pm 30V$ ($R_L = 50\Omega$)
 - 2) $\pm 2V$ ($R_L = 50\Omega$)
- b) Pulse width:
 - 1) $20NS$ TO $20\mu S$
 - 2) $15NS$
- c) Rise time:
 - $\leq 1NS$
- d) Fall time:
 - $\leq 1NS$
- e) PRF: 0 TO 50 KHz.
(10% MAX DUTY CYCLE)
- f) Jitter, stability: *OK*

Ⓐ 1 OUT POS, $R_L = 50\Omega$
TOP: $50NS/DIV$, BOT = $2\mu S/DIV$.



- g) Prime power: $170/240V, 50-60Hz$
- h) DC OFFSET: 1) 0 TO $\pm 15VDC$
2) 0 TO $\pm 1.0VDC$.

Ⓑ 1 OUT 40 dB ATTEN $2NS/DIV$
TOP: NEG OUT, RISE TIME
BOT: POS OUT, RISE TIME



Ⓒ 2 OUT POS + NEG $5NS/DIV$
 $2VOUT/DIV$ ($5NS/DIV$)



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"-B" Functional Test & Calibration Certificate

Date of test:		August 19, 1999				Tester:		MJC	
Programmed model name:		AVR-D2-B							
Programmed serial number:		9028							
Firmware revision:		1.97							
Internal trigger checked at:		5 Hz	50 Hz	500 Hz	5 kHz	50 kHz			
Actual measured output ¹ :		5.01 Hz	50.2 Hz	501.6 Hz	4.99 kHz	49.9 kHz			
External trigger checked:		Yes							
Trigger load resistor present:		Yes							
Manual trigger checked:		Yes	No spurious trigger in manual mode for output on/off/on:				N/A		
Pulse compression checked:		Yes							
Pulse width checked at:		CH1: 200 ns	CH1: 2 us	CH1: 20 us	CH2: (fixed)	At max.			
Actual measured output ² :		201 ns	2.01 us	20.2 us	15.0 ns	amplitude			
DC mode checked:		N/A							
PWin = PWout mode checked:		N/A							
Duty Cycle Limit:		10% (CH1)							
Delay nulled:		Yes (sync to CH2)							
Delay checked at:		100 ns	1 us	10 us	Sync to CH2				
Actual measured output ¹ :		100 ns	0.998 us	9.98 us					
Double pulse checked:		N/A							
Gate checked:		Yes							
Invert mode checked:		N/A							
ECL/TTL modes checked:		N/A							
Zout switch checked:		N/A							
Amplitude checked at:		CH1: +1V	CH1: -15V	CH1: +30V	CH2: +/- 2V	CH1: at 20 us pulse width			
Actual measured output ² :		+0.96V	-15.0V	+30.0V	+/- 2.0V				
Amplitude polarity:		+/-							
Zout calibration:		N/A							
Electronic amplitude control:		N/A							
External amplify mode:		N/A							
Monitor V/I Ratio:		N/A				Monitor offset nulled:			
LCD Monitor calibrated:		N/A				Monitor offset nulled:			
Mon. Single Pulse/Min PW OK:		N/A				SHA Cap:			
Offset checked at:		CH1: -15V	CH1: +0.0V	CH1: +15V	CH2: -1V	CH2: +1V			
Actual measured output ² :		-15.08 V	-8 mV	+15.07V	-0.992V	+0.990			
Offset nulled (output on):		N/A							
Offset nulled (output off):		N/A							
RS-232 checked:		Yes							
Sync pulse width checked:		200 ns							
Circuit Boards:		PS:	93	Main:	108				
Overload Trigger Resistance:		Trips at:		Installed:	6.2 k				
DC fuses:		Positive:	1.0 A	Negative:	N/A				
AC Current at 115 VAC:		Quiescent:	0.52 A	Max. Load:	0.75 A				
Photographed:		No							

¹ Checked with: HP5370A Universal Time Interval Counter

² Checked with: Tektronix TDS360 digital oscilloscope for PW ≥ 5 ns,
 Tektronix 7704A/7S11/7T11/S4 sampling oscilloscope system for PW < 5 ns.