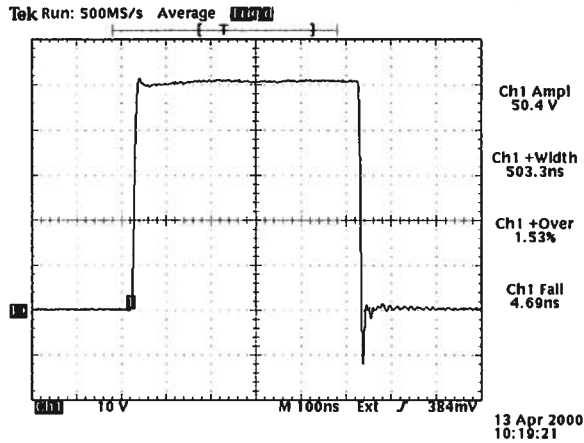


PULSE GENERATOR
PERFORMANCE CHECK

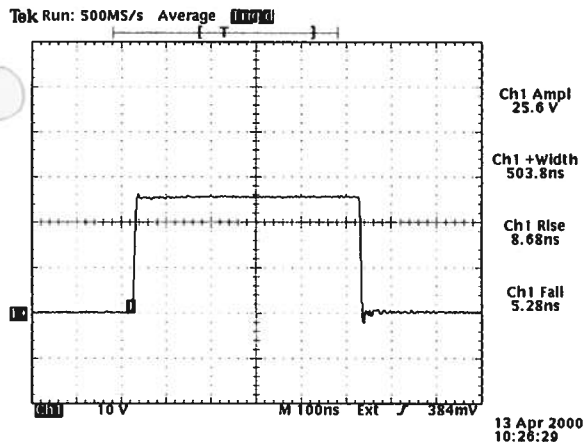
Model: AV-1015-B

S.N.: 9255

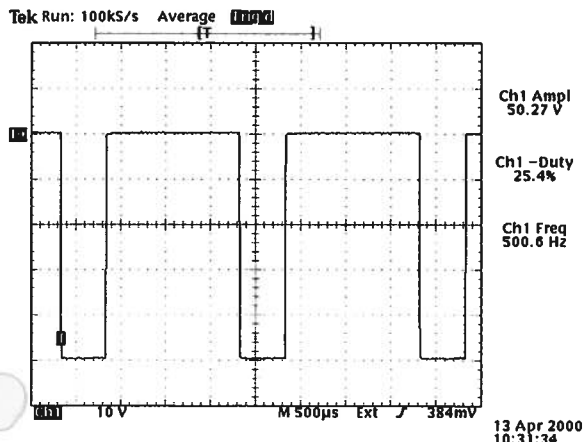
Date: April 13, 2000



+50V amplitude, 50Ω load, $Z_{OUT} = 2\Omega$, PW = 500 ns



+50V amplitude, 50Ω load, $Z_{OUT} = 50\Omega$, PW = 500 ns



-50V into 50Ω load, $Z_{OUT} = 2\Omega$,
500 us PW, 500 Hz PRF, 10V/div, 500 us/div

a) Output Signal Amplitude: 0 to ±50V ✓

b) Pulse Width: 20 ns - 10 ms,
25% duty cycle ✓

c) Rise Time: ≤ 10 ns ✓

d) Fall Time: ≤ 10 ns ✓

e) PRF: 0.1Hz - 10 MHz ✓

f) Jitter, Stability: OK ✓

g) Prime Power: 120/240V AC, 50-60 Hz, ✓



AVTECH ELECTROSYSTEMS LTD.

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SINCE 1975

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"-B" Functional Test & Calibration Certificate

Date of test:	April 12, 2000				Tester:	MJC
Programmed model name:	AV-1015-B					
Programmed serial number:	9255					
Firmware revision:	2.11					
Internal trigger checked at:	0.1 Hz	100 Hz	10 kHz	10 MHz		
Actual measured output ¹ :	0.1005 Hz	100.5 Hz	10.04 kHz	10.05 MHz		
External trigger checked:	yes			Gate checked:	yes	
Trigger load resistor present:	yes					
Manual trigger checked:	yes					
Pulse compression checked:	yes					
Pulse width checked at:	20 ns	2 us	200 us	10 ms	10 Hz, +50V, into 50 Ohms	
Actual measured output ² :	18.3 ns	2.01 us	201.6 us	10.04 ms		
PWin = PWout mode checked:	yes			DC mode checked:	N/A	
Duty Cycle Limit:	25% for 50 Ohm loads, 80% for high impedance loads					
Delay nulled:	yes					
Delay checked at:	100 ns	1 us	100 us	10 ms	10 Hz, +50V into 50 Ohms	
Actual measured output ¹ :	101.3 ns	1.002 us	99.3 us	10.04 ms		
Double pulse checked:	yes					
Invert mode checked:	N/A					
ECL/TTL modes checked:	N/A					
Zout switch checked:	yes					
Amplitude checked at:	-10V	+10V	-50V	+50V	10 kHz, 10us, to 50 Ohms	
Actual measured output ² :	-10.0V	+10.0V	-50.4V	+50.4V		
Amplitude polarity:	+/-					
Zout calibration:	N/A					
Electronic amplitude control:	OK					
External amplify mode:	N/A					
Ultravolt flux removed:	OK					
Monitor V/I Ratio:	N/A			Monitor offset nulled:		
LCD Monitor calibrated:	N/A			Monitor offset nulled:		
Mon. Single Pulse/Min PW OK:	N/A			SHA Cap:		
Offset checked at:	N/A					
Actual measured output ² :	N/A					
Offset nulled (output on):	N/A					
Offset nulled (output off):	N/A					
RS-232 checked:	yes					
Sync pulse width checked:	50 ns					
Circuit Boards:	PS:	93	Main:	108		
Overload Trigger Resistance:	Trips at:	N/A	Installed:	6k		
DC fuses:	Positive:	2A	Negative:	N/A		
AC Current at 115 VAC:	Quiescent:	0.41A	Max. Load:	0.70A		
AC fuse:	1A					
Photographed:	yes					

¹ Checked with: HP5370A Universal Time Interval Counter

² Checked with: Tektronix TDS360 digital oscilloscope for PW ≥ 5 ns,
Tektronix 7704A/7S11/7T11/S4 sampling oscilloscope system for PW < 5 ns.