

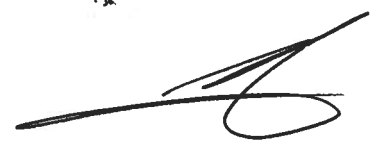
PULSE GENERATOR
PERFORMANCE CHECK

Model: AN-1011-B

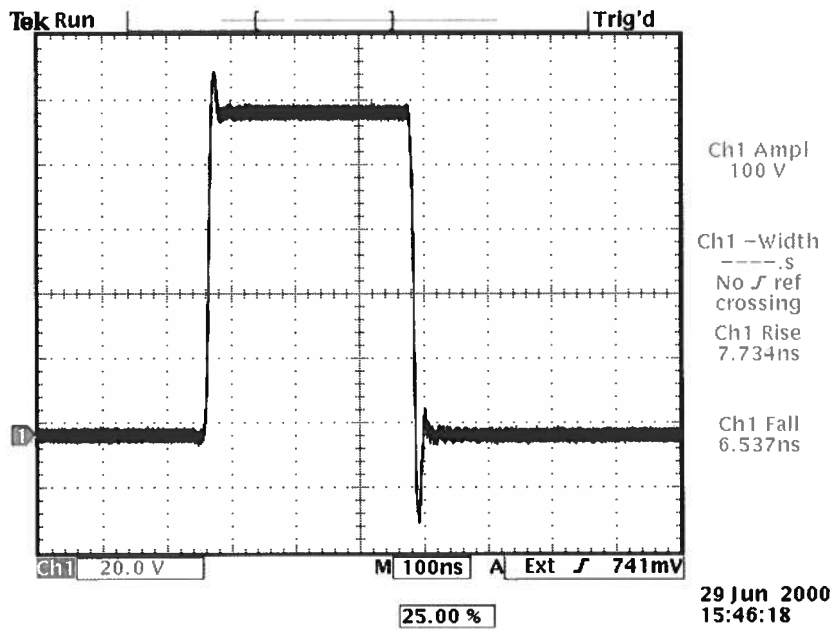
S.N.: 9414

Date: JUNE 24 2000

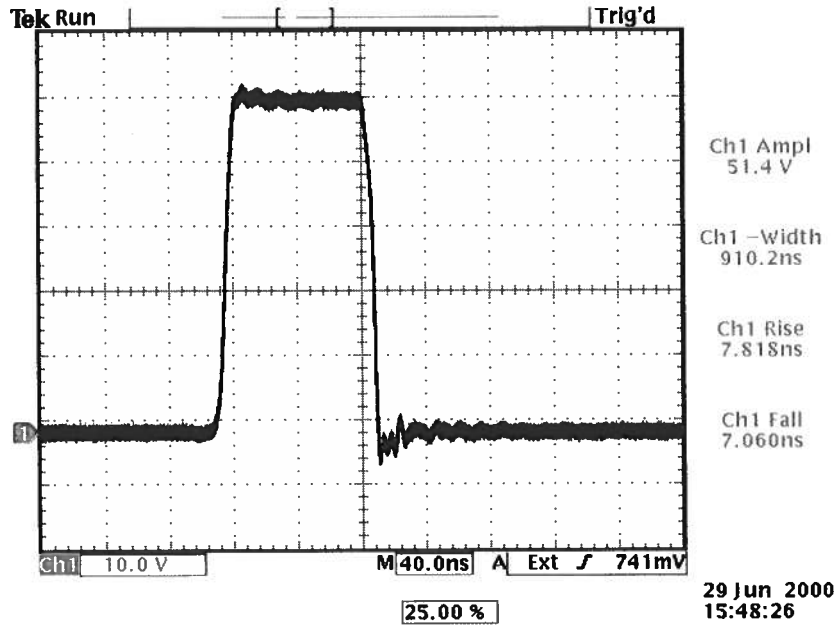
- a) Output signal amplitude:
0 TO $\pm 100V$ ($R_L \geq 50 \Omega$)
- b) Pulse width:
0.1 μ S TO 1 MS
- c) Rise time:
(10% MAX DUTY CYCLE)
 $\leq 10 NS$
- d) Fall time:
 $\leq 10 NS$
- e) PRF:
0 TO 1 MHz
(10% MAX DUTY CYCLE)
- f) Jitter, stability:
OK.
- g) Prime power:
120/240V
50-60 Hz.



9414
PRTF = 100 KHz
 $Z_{out} = 2 \Omega$
 $R_c = 50 \Omega$
POS OUT



9414
PRF = 1.0 MHz.
 $Z_{out} = 50 \Omega$
 $R_L = 50 \Omega$
Pos out.



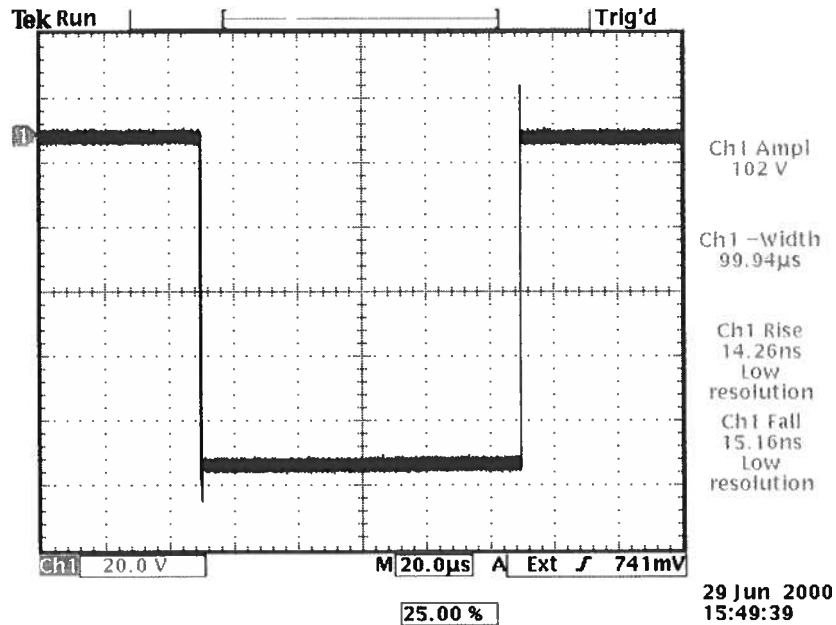
9414

PRF = 100 Hz.

$Z_{out} = 2 \Omega$

$R_c = 50 \Omega$

NEG out.





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"-B" Functional Test & Calibration Certificate

| | | | | | | |
|---------------------------------------|---------------|----------|------------|------------------------------------|----------------------------|-----|
| Date of test: | June 30, 2000 | | | | Tester: | MJC |
| Programmed model name: | AV-1011-B | | | | | |
| Programmed serial number: | 9414 | | | | | |
| Firmware revision: | 2.18 | | | | | |
| Internal trigger checked at: | 0.1 Hz | 10 Hz | 10 kHz | 1 MHz | | |
| Actual measured output ¹ : | 0.099 Hz | 9.96 Hz | 9.98 kHz | 1.003 MHz | | |
| External trigger checked: | yes | | | Gate checked: | yes | |
| Trigger load resistor present: | yes | | | | | |
| Manual trigger checked: | yes | | | | | |
| Pulse compression checked: | yes | | | | | |
| Pulse width checked at: | 100 ns | 1 us | 10 us | 1 ms | 100 Hz, +100V into 50 Ohms | |
| Actual measured output ² : | 99.8 ns | 0.996 us | 9.97 us | 1.000 ms | | |
| PWin = PWout mode checked: | yes | | | DC mode checked: | N/A | |
| Duty Cycle Limit: | 10% | | | | | |
| Delay nulled: | yes | | | | | |
| Delay checked at: | 1 us | 10 us | 100 us | 1 ms | 100 Hz, +100V into 50 Ohms | |
| Actual measured output ¹ : | 1.000 us | 9.94 us | 100.0 us | 1.000 ms | | |
| Double pulse checked: | yes | | | | | |
| Invert mode checked: | N/A | | | | | |
| ECL/TTL modes checked: | N/A | | | | | |
| Zout switch checked: | yes | | | | | |
| PRF, PW, delay min-to-max check: | yes | | | | | |
| Amplitude checked at: | -10V | +20V | -50V | +100V | 100 Hz, 10us, into 50 Ohms | |
| Actual measured output ² : | -10.0V | +20.0V | -50.0V | +100.0V | | |
| Amplitude polarity: | +/- | | | | | |
| Zout calibration: | N/A | | | | | |
| Electronic amplitude control: | OK | | | | | |
| External amplify mode: | N/A | | | | | |
| Ultravolt flux removed: | N/A | | | | | |
| Monitor V/I Ratio: | N/A | | | Monitor offset nulled: | | |
| LCD Monitor calibrated: | N/A | | | Monitor offset nulled: | | |
| Mon. Single Pulse/Min PW OK: | N/A | | | SHA Cap: | | |
| Offset checked at: | N/A | | | | | |
| Actual measured output ² : | N/A | | | | | |
| Offset nulled (output on): | N/A | | | Amplitude-dependent offset nulled: | | |
| Offset nulled (output off): | N/A | | | | | |
| RS-232 checked: | OK | | | | | |
| Sync pulse width checked: | 50 ns | | | | | |
| Circuit Boards: | PS: | 93 | Main: | 108 | | |
| Overload Trigger Resistance: | Trips at: | N/A | Installed: | 4k | | |
| DC fuses: | Positive: | 2A | Negative: | N/A | | |
| AC Current at 115 VAC: | Quiescent: | 0.40A | Max. Load: | 0.85A | | |
| AC fuse: | 1A | | | | | |
| Photographed: | yes | | | | | |

¹ Checked with: HP5370A Universal Time Interval Counter

² Checked with: Tektronix TDS360 digital oscilloscope for PW ≥ 5 ns,
Tektronix 7704A/7S11/7T11/S4 sampling oscilloscope system for PW < 5 ns.