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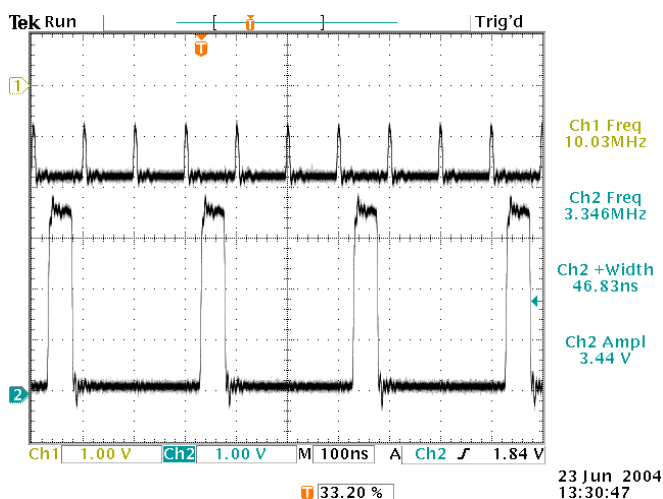
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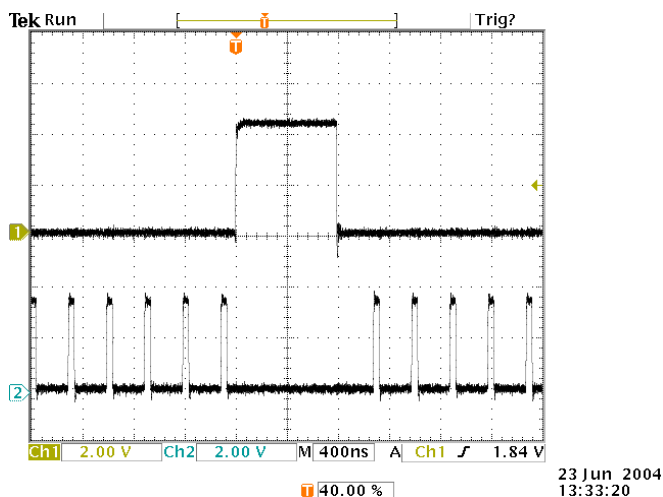
PERFORMANCE CHECKSHEET

Model: AVX-FD1-PS-XN-IP-SLA
S.N.: 10939
Date: June 23, 2004



Top: ECL input, 10 MHz.
Bottom: TTL output, $\div 3$, 50 ns pulse width, 50 Ω load.
Both: 1V/div, 100 ns/div.

- a) Output Signal Amplitude: TTL,
 $R_L \geq 50\Omega$
- b) Pulse Width: 50 ns - 50 us
- c) Division Factor: 0 to 65535
- d) PRF: 0 - 10 MHz
- e) Jitter, Stability: OK
- f) Prime Power: 100-240V AC, 50-60 Hz.



Top: RESET input.
Bottom: TTL output, $\div 3$, $f_{IN}=10\text{MHz}$.
Both: 2V/div, 400 ns/div.