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NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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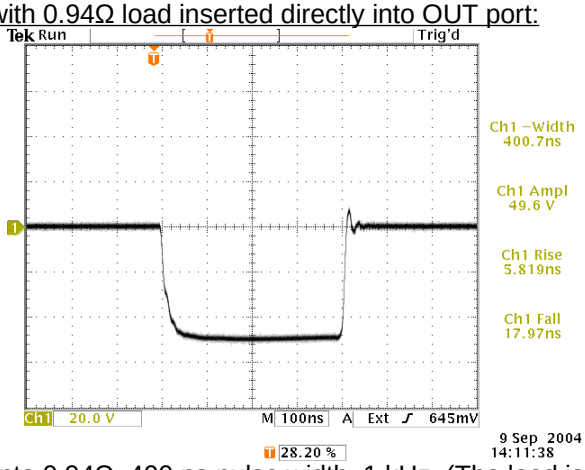
PERFORMANCE CHECKSHEET

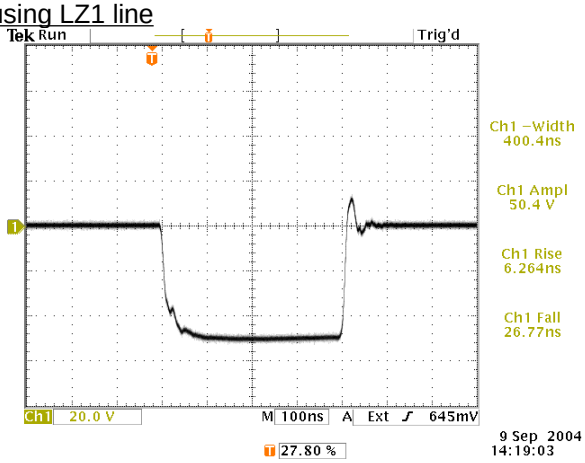
Model: AVOZ-A2-B-N
S.N.: 10975
Date: September 9, 2004

Test with 0.94Ω load inserted directly into OUT port:



-50V into 0.94Ω, 400 ns pulse width, 1 kHz. (The load is soldered onto a circuit board, which is inserted directly into the “OUT” connector. No LZ line is used.) 20 V/div, 100 ns/div.

Test using LZ1 line



-50V into 0.94Ω, 400 ns pulse width, 1 kHz. (The load is soldered onto the circuit board at the end of the 60 cm length of LZ1 line.) 20 V/div, 100 ns/div.

The voltage spike on the trailing edge is caused by parasitic inductance (V = L dI/dt ≈ L × 50A / 6.2 ns ≈ 8.1 V per nH).

References levels: 20%, 80%.

a) Output Signal Amplitude: 0 to +50 V to ≥ 1.0 Ohms (i.e., 0 to +50 Amps)

b) Pulse Width: 40 ns - 1 us (maximum duty cycle 0.4%)

c) Rise Time: ≤ 30 ns (20%-80%)

d) Fall Time: ≤ 10 ns (80%-20%)

e) PRF: 0 - 20 kHz (maximum duty cycle 0.4%)

f) Jitter, Stability: OK

g) Prime Power: 100-240V AC, 50-60 Hz.