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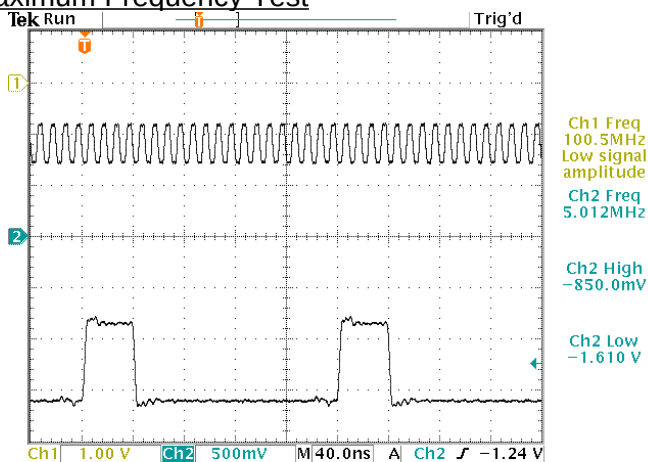
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PERFORMANCE CHECKSHEET

Model: AVX-FD2-PS-DECL-EP
S.N.: 11153
Date: March 22, 2005

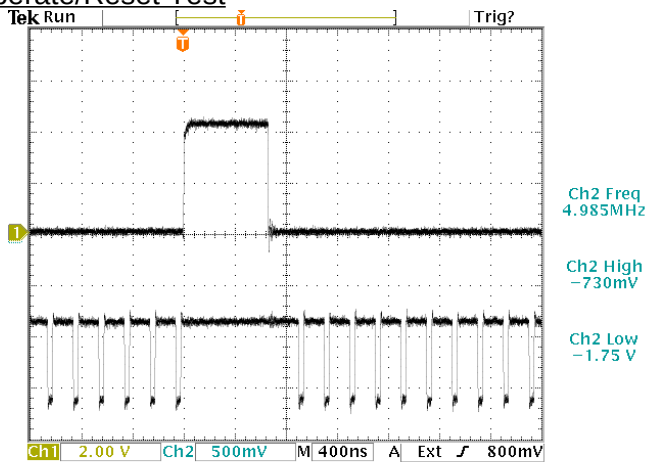
Maximum Frequency Test



- a) Output Signal Amplitude: ECL,
 $R_L \geq 50\Omega$
- b) Pulse Width: 5 ns - 5 us
- c) Division Factor: 0 to 255
- d) PRF: 0 - 100 MHz
- e) Jitter, Stability: OK

Top: ECL in (ECL mode), 100 MHz. 1 V/div, 40 ns/div.
Bottom: OUT, ÷ 20, 50Ω load. 0.5 V/div, 40 ns/div.

Operate/Reset Test



Top: RESET input. 2V/div, 400 ns/div.
Bottom: NOT OUT, ÷ 20, $f_{IN} = 100\text{MHz}$, 0.5V/div.