AVTECH ELECTROSYSTEMS LTD.
NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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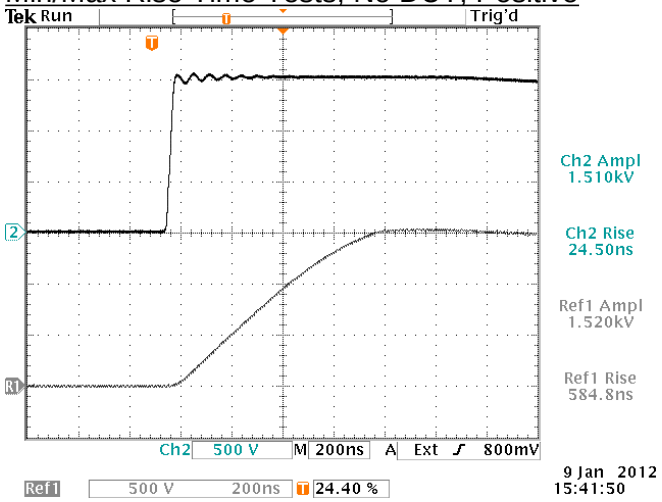
BOX 5120, LCD MERIVALE
OTTAWA, ONTARIO
CANADA K2C 3H4

PERFORMANCE CHECKSHEET

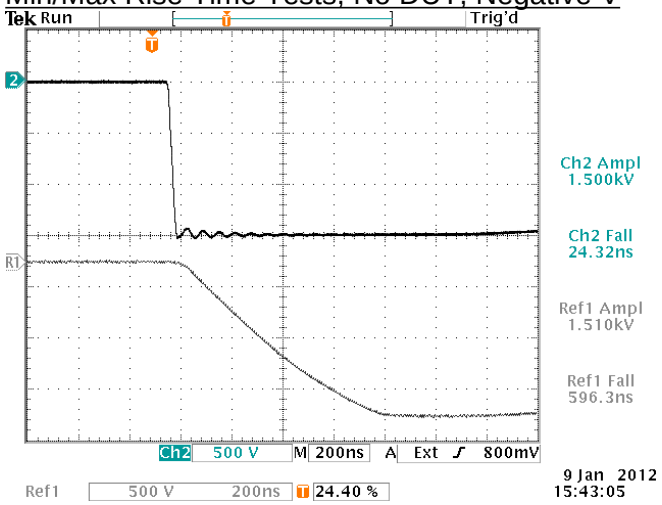
Model: AVRQ-4-B
Type: Common Mode Transient Immunity (CMTI) Test for Opto-Couplers
S.N.: 12740
Date: January 11, 2012

Min/Max Rise Time Tests, No DUT, Positive



Top: minimum rise time setting, +1.5 kV
Bottom: maximum rise time setting, +1.5 kV

Min/Max Rise Time Tests, No DUT, Negative V



Top: minimum rise time setting, -1.5 kV
Bottom: maximum rise time setting, -1.5 kV

a) Output Signal Amplitude: ±1 kV, ±1.5 kV

b) Rise Time (10%-90%): 25 ns - 250 ns

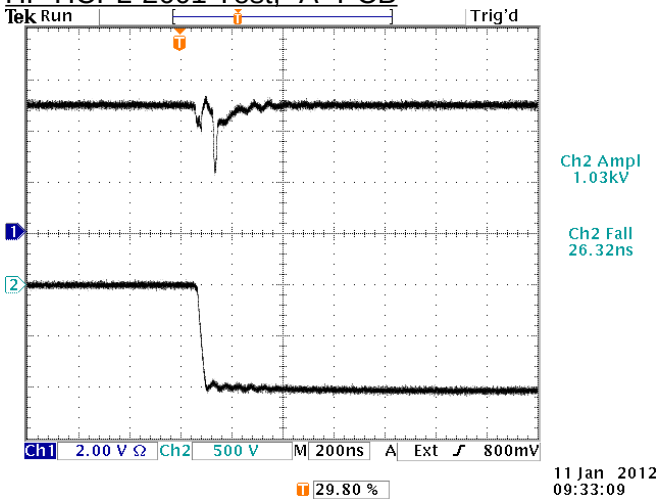
c) PRF: 1 Hz - 10 Hz

d) Jitter, Stability: OK

e) Prime Power: 100-240V AC, 50-60 Hz.

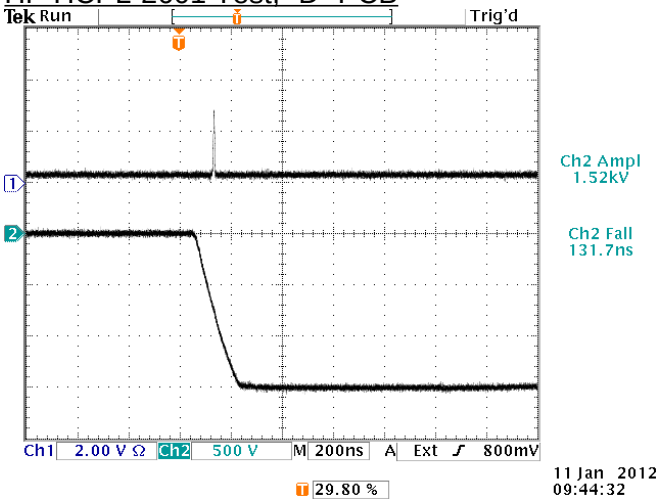
References levels: 10%, 90%.

HP HCPL-2601 Test, “A” PCB



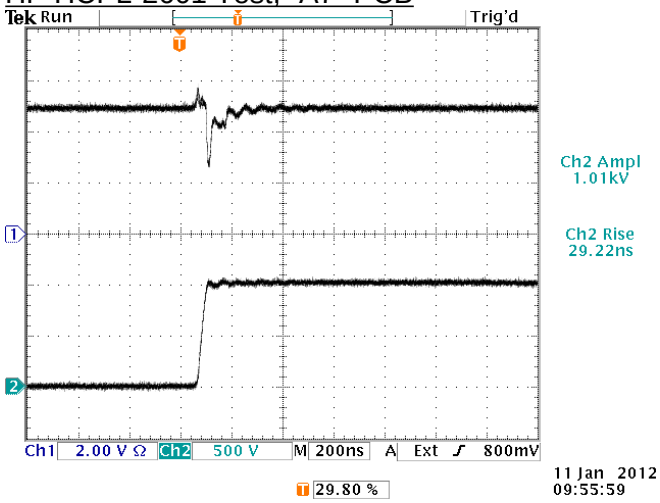
-1kV, +5V, 0 mA, 348Ω (“A” PCB). 50% glitch starts to occur at dV/dt of 1 kV × (90% - 10%) / 26.32 ns = 30.4 kV/us.

HP HCPL-2601 Test, “D” PCB



-1.5kV, +5V, 7.5 mA, 348Ω (“D” PCB). 50% glitch starts to occur at dV/dt of 1.5 kV × (90% - 10%) / 131.7 ns = 9.1 kV/us.

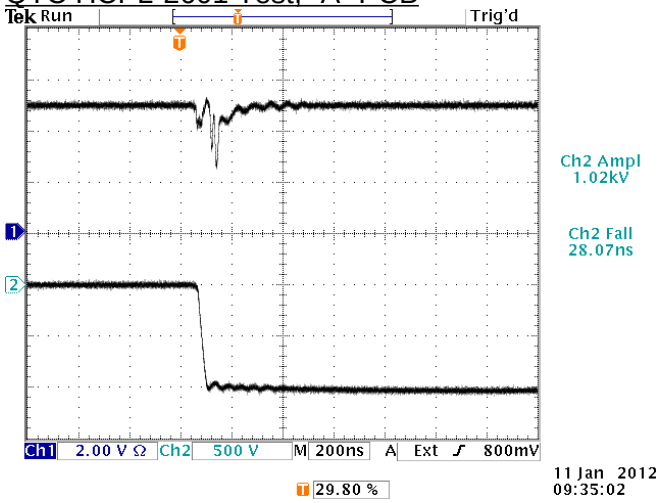
HP HCPL-2601 Test, “A7” PCB



+1kV, +5V, 0 mA, 348Ω (“A7” PCB). 50% glitch starts to occur at dV/dt of 1 kV × (90% - 10%) / 29.22 ns = 27.4 kV/us.

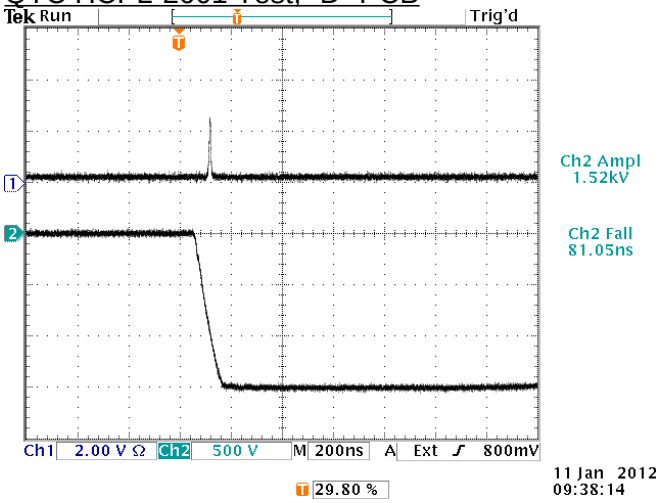
The DUT used for the waveforms on this page was a Hewlett-Packard HCPL-2601, date code 9205.

QTC HCPL-2601 Test, “A” PCB



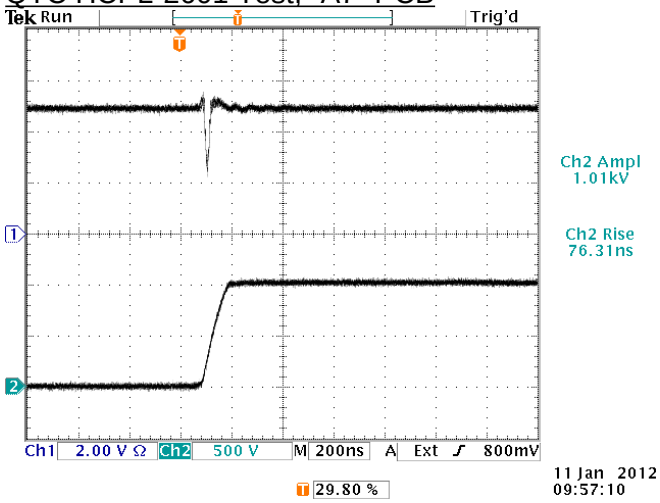
-1kV, +5V, 0 mA, 348Ω (“A” PCB). 50% glitch starts to occur at dV/dt of 1 kV × (90% - 10%) / 28.07 ns = 28.5 kV/us.

QTC HCPL-2601 Test, “D” PCB



-1.5kV, +5V, 7.5 mA, 348Ω (“D” PCB). 50% glitch starts to occur at dV/dt of 1.5 kV × (90% - 10%) / 81.05 ns = 14.8 kV/us.

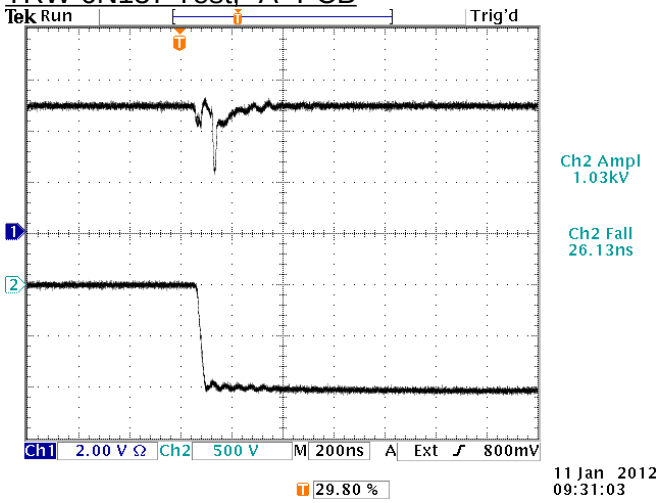
QTC HCPL-2601 Test, “A7” PCB



+1kV, +5V, 0 mA, 348Ω (“A7” PCB). 50% glitch starts to occur at dV/dt of 1 kV × (90% - 10%) / 76.31 ns = 10.5 kV/us.

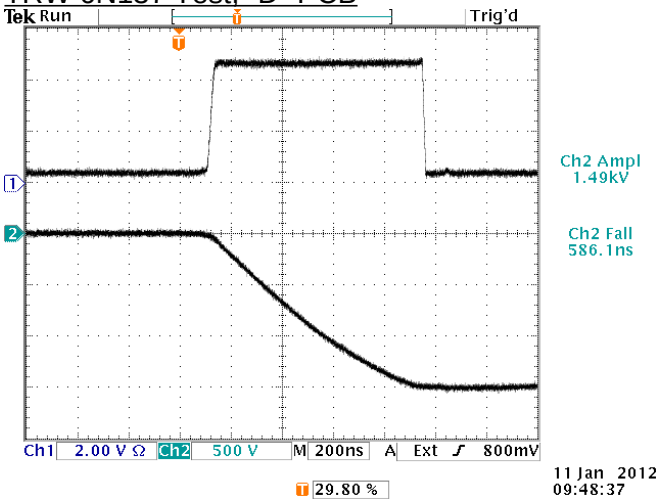
The DUT used for the waveforms on this page was a QTC HCPL-2601, date code 9522.

TRW 6N137 Test, “A” PCB



-1kV, +5V, 0 mA, 348Ω (“A” PCB). 50% glitch starts to occur at dV/dt of 1 kV × (90% - 10%) / 26.13 ns = 30.6 kV/us.

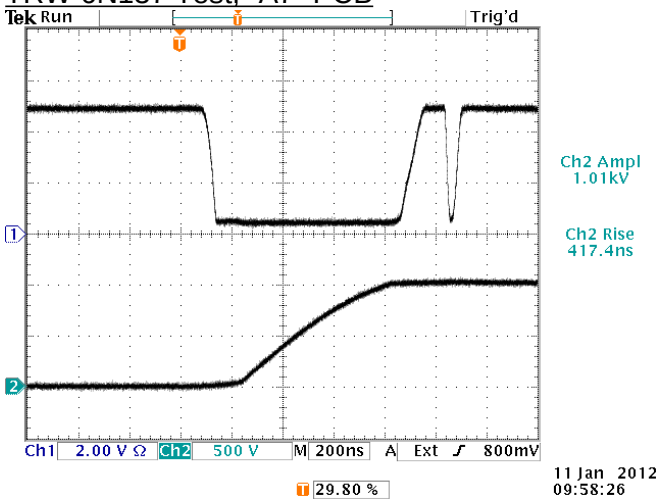
TRW 6N137 Test, “D” PCB



-1.5kV, +5V, 7.5 mA, 348Ω (“D” PCB).

Glitches are observed at the slowest rise time setting, with a dV/dt of 1.5 kV × (90% - 10%) / 586.1 ns = 2.0 kV/us.

TRW 6N137 Test, “A7” PCB



+1kV, +5V, 0 mA, 348Ω (“A7” PCB).

Glitches are observed at the slowest rise time setting, with a dV/dt of 1 kV × (90% - 10%) / 417.4 ns = 1.9 kV/us.

The DUT used for the waveforms on this page was a TRW 6N137, date code 8642.