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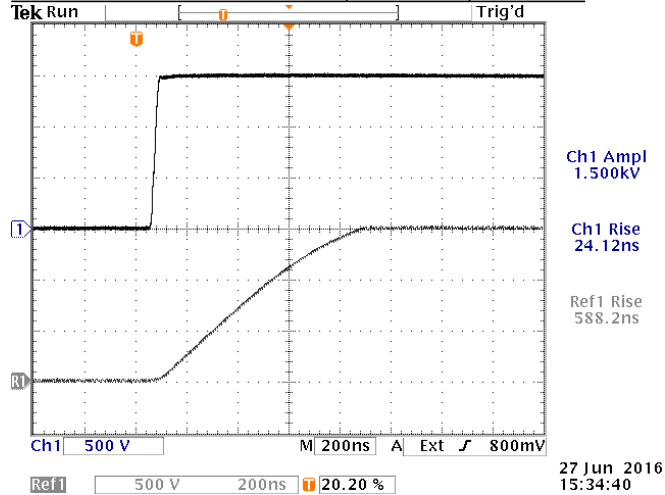
BOX 5120, LCD MERIVALE
OTTAWA, ONTARIO
CANADA K2C 3H5

info@avtechpulse.com - http://www.avtechpulse.com/

PERFORMANCE CHECKSHEET

Model: AVRQ-4-B
Type: Common Mode Transient Immunity (CMTI) Test for Opto-Couplers
S.N.: 13472
Date: June 27, 2016

Min/Max Rise Time Tests, No DUT, Positive



a) Output Signal Amplitude: ±1 kV, ±1.5 kV

b) Rise Time (10%-90%): 25 ns - 250 ns

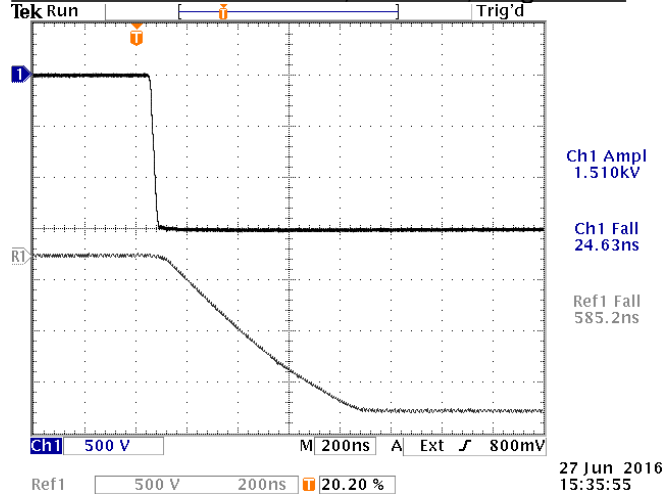
c) PRF: 1 Hz - 10 Hz

d) Jitter, Stability: OK

e) Prime Power: 100-240V AC, 50-60 Hz.

Top: minimum rise time setting, +1.5 kV
Bottom: maximum rise time setting, +1.5 kV

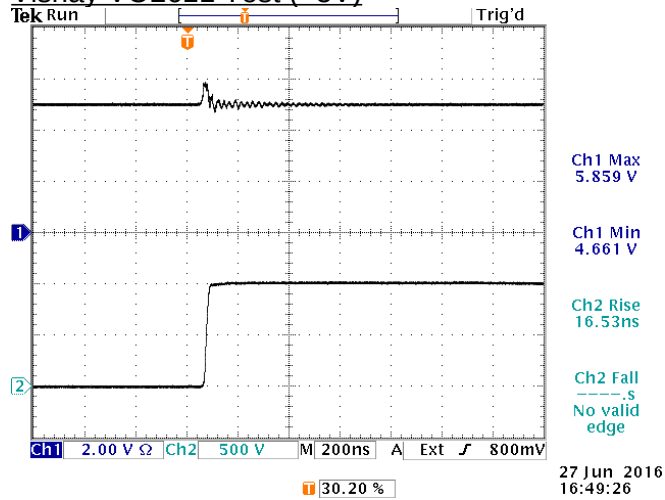
Min/Max Rise Time Tests, No DUT, Negative V



Top: minimum rise time setting, -1.5 kV
Bottom: maximum rise time setting, -1.5 kV

References levels: 10%, 90%.

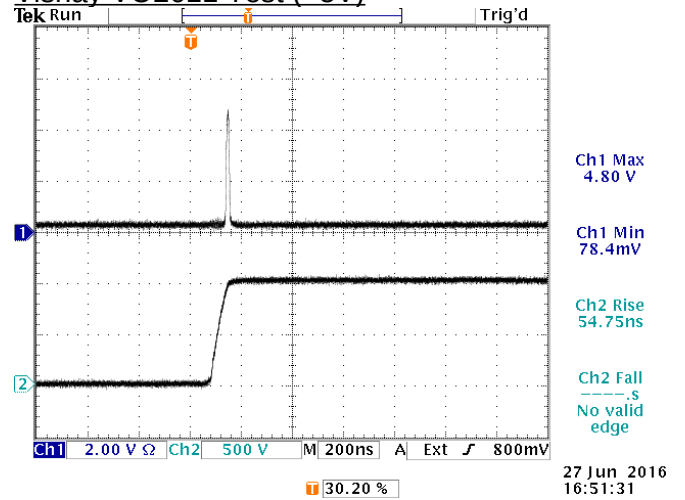
Vishay VO2611 Test (+5V)



+1 kV, +5V, 0 mA, 348Ω load ("A" PCB).

No glitches at minimum risetime, so the CMTI exceeds $(1\text{kV} \times (90\% - 10\%) / 16.53 \text{ ns}) = 48.4 \text{ kV/us}$.

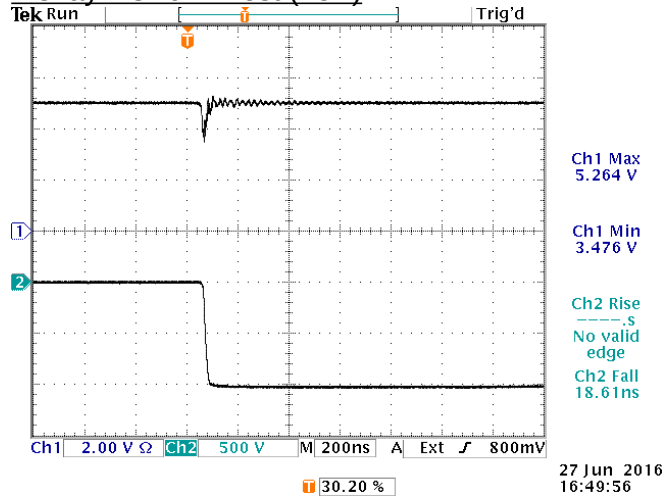
Vishay VO2611 Test (+5V)



+1 kV, +5V, 7.5 mA, 348Ω load ("D7" PCB).

A ~50% glitch starts to occur at $1 \text{ kV} \times (90\% - 10\%) / 54.75 \text{ ns} = 14.6 \text{ kV/us}$.

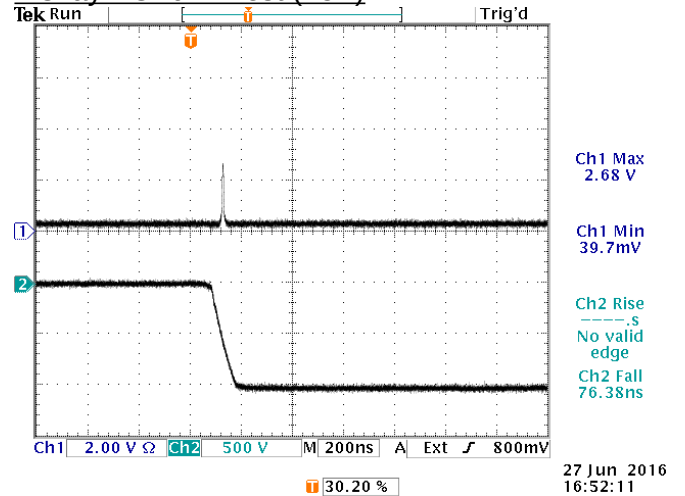
Vishay VO2611 Test (+5V)



-1 kV, +5V, 0 mA, 348Ω load ("A" PCB).

The glitch at minimum risetime does not dip below 50%, so the CMTI exceeds $(1\text{kV} \times (90\% - 10\%) / 18.61 \text{ ns}) = 43.0 \text{ kV/us}$.

Vishay VO2611 Test (+5V)



-1 kV, +5V, 7.5 mA, 348Ω load ("D7" PCB).

A ~50% glitch starts to occur at $1 \text{ kV} \times (90\% - 10\%) / 76.38 \text{ ns} = 10.5 \text{ kV/us}$.