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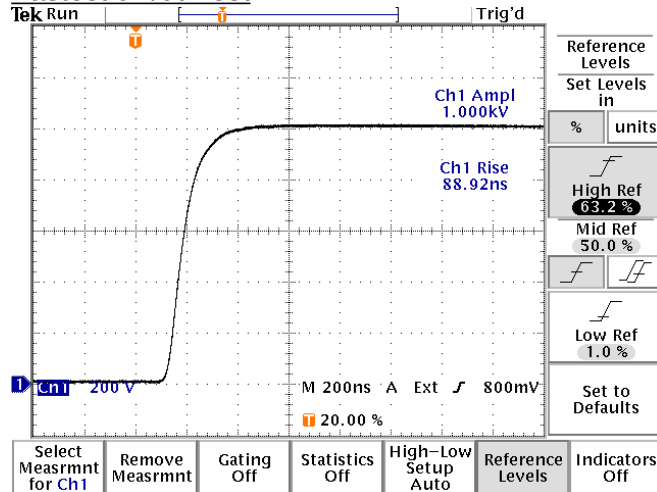
BOX 5120, LCD MERIVALE
OTTAWA, ONTARIO
CANADA K2C 3H5

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PERFORMANCE CHECKSHEET

Model: AVR-DV1-B
Type: High-Voltage Phototriac dV/dt Test System
S.N.: 13531
Date: December 19, 2016

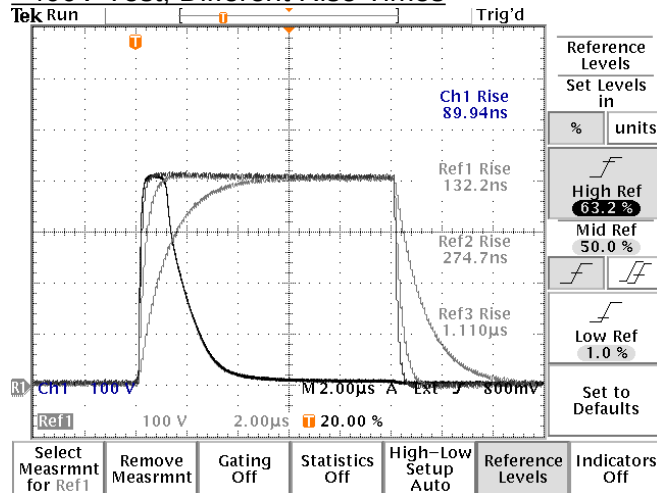
Fastest dV/dt Test



+1 kV pulse, with "T" control at minimum. 200 V/div, 200 ns/div. 10 Hz, 10 us PW. No DUT.

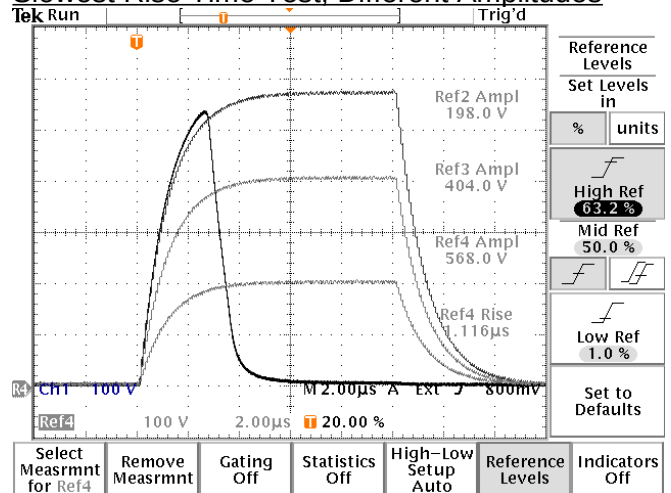
- a) Output Signal Amplitude: 0 to ±1000V
- b) Pulse Width (FWHM): 200 ns - 200 us
- c) Rise Time (20%-80%): < 100 ns at mainframe OUT
- d) Fall Time (80%-20%): < 100 ns at mainframe OUT
- e) PRF: 1 Hz – 1 kHz
- f) Jitter, Stability: OK
- g) Prime Power: 100-240V AC, 50-60 Hz.

+400V Test, Different Rise Times



DUT = Fairchild MOC3052M
Fastest dV/dt without voltage collapse at 400V is
 $0.632 \times 400V / 132.2 \text{ ns} = 1912 \text{ V/us}$.

Slowest Rise Time Test, Different Amplitudes



DUT = Fairchild MOC3052M
The highest amplitude at the slowest T setting without voltage collapse is 568V, giving
 $0.632 \times 568V / 1.116 \text{ us} = 321.7 \text{ V/us}$.