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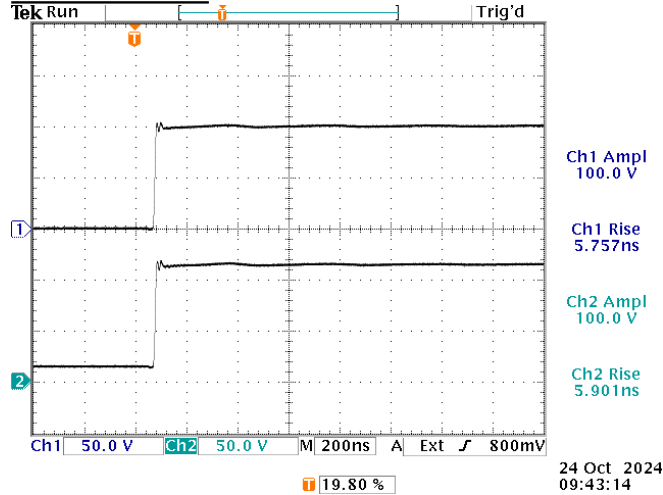
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BOX 5120, LCD MERIVALE
OTTAWA, CANADA K2C3H5

PERFORMANCE CHECKSHEET

Model: AVX-TD
Type: High-Speed Bias Tee
S.N.: 14490
Date: October 24, 2024

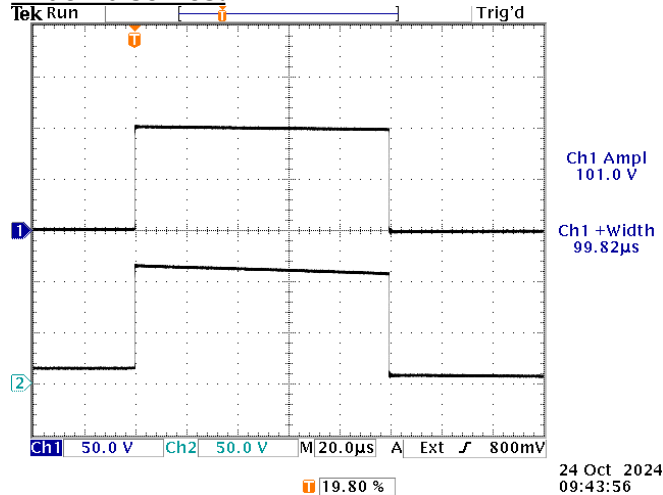
Rise Time Test



- a) Pulse/DC voltage limit: 100V
- b) DC current limit: 0.3A
- c) Pulse width range: 40 ns – 100 us
- d) Rise time (20%-80%): < 5 ns

Top: +100V, 100 us pulse from an AV-1010-B.
Bottom: Output of AVX-TD, with +0.3A offset.

Wide Pulse Test



Top: +100V, 100 us pulse from an AV-1010-B.
Bottom: Output of AVX-TD, with +0.3A offset.

References levels: 20%, 80%.