## INSTRUCTIONS

MODEL AV-1000-C PULSE GENERATOR
S.N.:

## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty is either expressed or implied.

## INTRODUCTION

The Avtech AV-1000 is a versatile, general-purpose, low-cost, 50 MHz laboratory pulse generator, useful everywhere from undergraduate university classrooms to the most advanced research and development laboratories. This pulse generator features variable pulse repetition frequency (PRF), delay, pulse width, rise and fall times, amplitude, and baseline. Additionally, the generator can be triggered either internally or externally, as well as by the manual "Single Pulse" pushbutton. All trigger sources can be gated by a TTL-type pulse. PRF is continuously variable from 1 kHz to 50 MHz , delay to 1 ms , pulse width to 1 ms , and amplitude and baseline offset to $\pm 10 \mathrm{~V}$. Rise and fall times are independently variable (within a given range) from less than 5 ns to 1 ms . Three outputs are supplied, the first being the Main output, which has all of its characteristics variable, and is designed to drive $50 \Omega$ loads. The other two outputs are "logic" outputs, which can provide either TTL or ECL-type pulses and their complements into $50 \Omega$. A synchronizing trigger output is supplied when operating off of the internal trigger ( +2 V into $50 \Omega,+4 \mathrm{~V}$ into $1 \mathrm{M} \Omega$.)

## SPECIFICATIONS

## PULSE REPETITION FREQUENCY

The PRF is continuously variable from 1 kHz to 50 MHz in 10 ranges, each range providing a ratio of approximately 3 between its highest and lowest frequency.

## DELAY

The delay between the SYNC output or the external trigger is variable from 10ns to 1 ms in five ranges. Delay is variable over $75 \%$ of the pulse period up to 1 MHz , decreasing to $40 \%$ at 20 MHz , and $10 \%$ at 50 MHz .

## PULSE WIDTH (AND DUTY CYCLE)

Pulse width is measured at the $50 \%$ amplitude point, and is continuously variable from 10 ns to 1 ms . Duty cycle may range up to $80 \%$ at $1 \mathrm{MHz}, 60 \%$ at 40 MHz , and $40 \%$ at 50 MHz . Higher duty cycles may often be obtained by reversing the Polarity switch, and adjusting the baseline to obtain an inverted pulse.

## RISE/FALL TIMES

The rise and fall times are measured from the $10 \%$ to $90 \%$ amplitude levels with the output terminated into $50 \Omega$. Each is independently variable within the same range (i.e. the maximum ratio between them is just over 10:1). The rise and fall times are continuously variable between 5 ns and 1 ms , in 5 ranges.

## BASELINE

The baseline, or offset, of the main output pulse is determined by a one-turned control. When the amplitude is set on the 10 V range, the baseline is continuously variable between +10 V and -10 V . When in the 5 V and 1 V ranges, the baseline is continuously variable between +5 V and -5 V . (This is because in the 5 V and 1 V ranges, a $50 \Omega$ resistor is placed in series with the output and the load. This reduces the maximum output level, but provides backmatching which tends to reduce reflections and other waveform distortions.) Note that the sum of the baseline offset and the pulse amplitude can not exceed $\pm 10 \mathrm{~V}$, and that all of these values are valid only for a $50 \Omega$ load.

## AMPLITUDE

The amplitude of the main output is continuously variable between zero and ten volts, with the polarity controlled by the polarity switch. The amplitude can be varied in three ranges, from 0 to $1 \mathrm{~V}, 0$ to 5 V , and 0 to 10 V .
The lower two ranges switch in a $50 \Omega$ backmatching resistor onto the output, for improved waveform quality. The 10 V range does not have any backmatching. Note that
the sum of the pulse amplitude and the baseline offset can not exceed $\pm 10 \mathrm{~V}$, and that all of these values are valid only for a $50 \Omega$ load.

## LOGIC OUTPUTS

The logic outputs provide either a TTL-type signal and its logic complement, or an ECL-type pulse and its logic complement, depending on the setting of the "TTL/ECL" switch. The outputs have the same PRF, delay, and pulse width as the main output, but do not have variable rise/fall times, or any amplitude control. They are designed to drive $50 \Omega$ loads, but will drive any load greater than $50 \Omega$ with the penalty of increased waveform distortion.

## SYNC OUT

When triggering off of the internal clock, the SYNC OUT/TRIG IN connector is used as a SYNC output, allowing the user to synchronize other equipment to the instrument (e.g. oscilloscopes). This output provides approximately +2 V into a $50 \Omega$ load, or +4 V into a $1 \mathrm{M} \Omega$ load. This pulse leads the other outputs by a duration set by the "DELAY" controls, and has a pulse width of approximately 10 ns . A sync signal is not provided in the external mode.

## EXTERNAL TRIGGER

When the "INT/EXT" switch is in the EXT position, the instrument triggers off of an external signal, which must be supplied by either a TTL type signal (i.e. 0 to +5 V ) on the "SYNC OUT/TRIG IN" connector or by pressing the "SINGLE PULSE" pushbutton. The external trigger must be at least 4 ns wide. This input has a high input impedance (greater than $1 \mathrm{k} \Omega$ ).

## SINGLE PULSE

Pressing the "SINGLE PULSE" pushbutton with the "INT/EXT" switch in the "EXT" position will generate a single output pulse on the Main and Logic outputs. Pressing the "SINGLE PULSE" pushbutton with the switch in the "INT" position has no effect.

## GATE IN

The "GATE IN" input is a high impedance input that can be used to suppress the triggering of the instrument. Leaving this input unconnected, or applying a TTL high level (e.g. +2.8 V to 5 V ) will permit normal triggering. Taking the input low (to ground, or less than +0.8 V ) will inhibit any sort of triggering.

## JITTER

Repetition rate, delay, and pulse width jitter are less than $\pm 15$ ps or $\pm 0.01 \%$, whichever is greater.

## WAVEFORM ABERRATIONS

Overshoot, undershoot, ringing, and top slope aberration are less than $\pm 3 \%$ at amplitudes of 300 mV and higher with outputs terminated in $50 \Omega$.

## OUTPUT PROTECTION

The instrument will not be damaged by any combination of front panel setting, or open or short circuits.

## OPERATING TEMPERATURE

The instrument is rated for operation in ambient temperatures of $+15^{\circ} \mathrm{C}$ to $+40^{\circ} \mathrm{C}$.

## POWER REQUIRED

A maximum of 30 W is required. The instrument can operate on 120 V AC or 240 V AC, selectable on the back panel, at 50 to 60 Hz .

## PHYSICAL CHARACTERISTICS

The instrument is contained in a $4^{\prime \prime} \times 16^{\prime \prime} \times 12$ " anodized aluminum chassis with handles, with a mass of 10 kg . Signal connectors are all BNC type.

## ACCESSORIES

One instruction manual and one power cord are supplied with the instrument. An optional 19" rack mounting kit is available (Avtech Part No. -R4)

## INSTALLATION

## VISUAL CHECK

After unpacking the instrument, examine to ensure that it has not been damaged in shipment. Visually inspect all connectors, knobs, and the handles. Confirm that a power cord and instruction manual are with the instrument. (If the instrument has been damaged in shipment, file a claim immediately with the company that transported the instrument.)

## PLUGGING IN THE INSTRUMENT

Examine the rear of the instrument. There will be a male power receptacle, a fuse holder, and the edge of the power selector card will be visible. Confirm that the power selector is in the correct orientation - it should be marked either 120 or 240 , indicating whether it expects 120 V AC or 240 V AC. If it is not set for the proper voltage, remove the fuse, then grasp the card with a pair of pliers and remove it. Rotate horizontally through 180 degrees. Reinstall the card and the correct fuse. In the 120 V setting, a $1 / 2 \mathrm{~A}$ slow blow fuse is required. In the 240 V setting, a $1 / 4 \mathrm{~A}$ slow blow fuse is required.

## OPERATIONAL CHECK

This check is to confirm that the instrument is fully functional. In all tests, use $50 \Omega$ cable with female BNC connectors on each end. Set the controls to the following values:

INT/EXT Switch: INT position
REPETITION RATE RANGE Switch: 1 MHz
REPETITION RATE Vernier: MAX
DELAY RANGE Switch: 100ns
DELAY Vernier: MIN
PULSE WIDTH RANGE Switch: 100ns
PULSE WIDTH Vernier: MAX
RISE/FALL RANGE Switch: 100ns
RISE Vernier: MIN
FALL Vernier: MIN
GND/VAR Switch: GND

POLARITY Switch: +
AMPLITUDE RANGE Switch: 10V

AMPLITUDE Vernier: MAX
LOGIC Switch: TTL
Connect a cable from the SYNC OUT/TRIG IN connector to the TRIG input of an oscilloscope (preferably one rated for at least 20 MHz .) A second cable from the main output should be connected to a male arm of a BNC T-connector. On a second arm of the T-connector, a $2 \mathrm{~W} 50 \Omega$ termination should be installed. The third arm is then connected to the oscilloscope input. Alternatively, a BNC $50 \Omega$ feedthru adapter may be placed between the output cable and the oscilloscope input. Or, a $50 \Omega$ resistor may be placed across a BNC-banana plug adapter at the output, and a scope probe can then be clipped onto the resistor. Any of the three methods for terminating the output in $50 \Omega$ can be used.

Set the oscilloscope to trigger externally. Then follow the instructions on the next page, and compare what is seen on the oscilloscope to what is described. Only approximate values are needed to confirm operation.

| STEP | CONTROL | OPERATION | RESULTS |
| :---: | :---: | :---: | :---: |
| 1 | POWER | Push in (ON) | +10 V pulses at the main output, with period $1 \mu \mathrm{~s}$, pulse width $100 \mathrm{~ns},<10 \mathrm{~ns}$ rise and fall times. |
| 2 | REPETITION RATE VERNIER | Rotate to MIN, then to MAX | Period rises to about $4 \mu \mathrm{~s}$, then falls to about $1 \mu \mathrm{~s}$. |
| 3 | DELAY VERNIER | Rotate to MAX, then to MIN | Pulses shift to the right on the oscilloscope by 100ns, then back. |
| 4 | PULSE WIDTH VERNIER | Rotate to MIN, then to MAX | Pulses become very narrow (about 10ns wide), then return to 100 ns pulse width. |
| 5 | RISE VERNIER | Rotate to MAX, then to MIN | Rise time increases to 100 ns , then decreases. |
| 6 | FALL VERNIER | Rotate to MAX, then to MIN | Fall time increases to 100ns, then decreases. |
| 7 | POLARITY SWITCH | Switch to -, then to + | Pulses swing between 0 and -10V, then swing between 0 and +10 V . |
| 8 | BASELINE SWITCH | Switch to VAR | Pulses may shift up or down. |
| 9 | BASELINE VERNIER | Rotate to -10V | Pulses swing between -10 V and 0 V . |
| 10 | BASELINE SWITCH | Switch to GND | Pulses swing between 0 and +10 V . |
| 11 | AMPLITUDE RANGE | Switch to 5 V , then 1 V , then back to 10 V | Amplitude falls to +5 V , then +1 V , then rises back up to +10 V . |
| 12 | OUT | Remove cable, place on LOGIC connector | Oscilloscope shows pulses swinging between 0 V and +3 V , with period 1 us , and pulse width 100ns. |
| 13 | TTL/ECL SWITCH | Switch to ECL | Oscilloscope shows pulses swinging between -0.8 V and -1.6 V , with period $1 \mu \mathrm{~s}$, and pulse width 100 ns . |
| 14 | TTL/ECL SWITCH | Switch to TTL | Oscilloscope trace is the same as in step 11. |
| 15 | LOGIC | Remove cable, place on LOGIC connector | Oscilloscope shows inverted pulses swinging between 3 V and 0 V , with period $1 \mu \mathrm{~s}$, and low time 100 ns . |

## OPERATING INSTRUCTIONS

## POWER Switch

The POWER pushbutton switch applies AC prime power the primaries of the transformer, turning the instrument on. The pushbutton lamp (\#382 type) is connected to the +15 V DC supply.

## INT/EXT Switch

In the "INT" position the instrument is internally triggered and the "SYNC OUT/TRIG IN" connector provides a SYNC output, which allows one to trigger other instruments, such as oscilloscopes. In the "EXT" position the instrument is triggered by a TTL level input pulse on the "SYNC OUT/TRIG IN" connector, or by pressing the "SINGLE PULSE" pushbutton.

## SINGLE PULSE Pushbutton

The "SINGLE PULSE" pushbutton will trigger the instrument manually for one cycle of output, when the "INT/EXT" switch is in the "EXT" position. Otherwise, the pushbutton has no effect.

## SYNC OUT/TRIG IN Connector

When in the "INT/EXT" switch is in the "INT" position, this connector supplies a SYNC output, that can be used to trigger other equipment, particularly oscilloscopes. This signal leads the main output by a duration set by the "DELAY" controls, and has an approximate amplitude of +2 V in $50 \Omega$, or +4 V into $1 \mathrm{M} \Omega$, with a pulse width of about 10ns. When the switch is in the "EXT" position, the external trigger is applied to this connector. This input presents a high impedance (greater than $1 \mathrm{k} \Omega$ ). Should an input impedance of $50 \Omega$ be required, it must be added manually at the input.

## GATE Input

The GATE input will suppress the triggering of the instrument if grounded, or taken to a TTL LOW level (i.e. 0 to 0.8 V ). If it is left open, or taken to a TTL HIGH level (i.e. +2.4V to 5.0 V ), normal triggering will occur. This connector has a high input impedance (greater than $1 \mathrm{k} \Omega$.)

## REPETITION RATE Controls

The rotary switch marked "RANGE" selects the pulse repetition rate for the internally triggered mode.
The vernier (labelled "MIN - MAX" provides continuously variable control of each range. There are ten ranges and the instrument is set to the rate indicated on the front panel when the vernier is in the "MAX" position.

## DELAY Controls

The rotary switch selects one of five ranges and the vernier provides continuously variable control of each range. The instrument is set to the delay indicated on the front panel when the vernier is in the "MAX" position.

## PULSE WIDTH Controls

The rotary switch selects one of five ranges and the vernier provides continuously variable control of each range. The instrument is set to the pulse width indicated on the front panel when the vernier is in the "MAX" position.

## RISEIFALL RANGE Switch

The rotary switch selects one of five transition time ranges for both the rising and falling edge of the main output pulses.

## RISE and FALL Controls

The RISE and FALL verniers provide continuously variable control of the rising and falling times for each range. The instrument is set to the transition time indicated by the RISE/FALL RANGE switch when its respective vernier is in the "MAX" position.

## GND/VAR Baseline Switch

The GND/VAR switch allows the baseline offset of the main output pulse to be either fixed at ground potential (GND, zero Volts) or to be continuously variable between +10 V and -10 V (or +5 V and -5 V , depending on the amplitude range selected. See descriptions below.)

## BASELINE Control

The BASELINE Control varies the baseline offset of the main output pulse when the GND/VAR switch is in the "VAR" position. If the amplitude range switch is in the 10V range, the baseline may be varied between +10 V and -10 V , into a $50 \Omega$ load. If the range switch is in the 5 V or 1 V ranges, the baseline may be varied between +5 V and -5 V . This is due to the fact that in the 1 V and 5 V ranges, the instrument switches in a $50 \Omega$ backmatching resistor in series with the output. This, is effect, forms a resistive divider, limiting the baseline to 5 V , but the backmatching provides a more electrically ideal situation.

## POLARITY Switch

If the polarity switch is in the " + " position, the main output pulse will pulse upwards (i.e. to a more positive level.) If it is in the "-" position, the output will pulse downwards, to a more negative level. This switch does not affect the BASELINE controls.

## AMPLITUDE RANGE Switch

When in the 1 V range, the main output is between variable in amplitude from 0 to $\pm 1 \mathrm{~V}$, peak to peak. Similarly, in the 5 V and 10 V ranges, the amplitude is variable from 0 to $\pm 5 \mathrm{~V}$ and $\pm 10 \mathrm{~V}$ respectively. The 1 V and 5 V have $50 \Omega$ backmatching on the output, as mentioned in the baseline descriptions, while the 10 V range does not.

## AMPLITUDE Controls

The amplitude vernier provide continuously variable control of the peak to peak amplitude of the main output, from zero Volts to the maximum set by the range switch.

## TTL/ECL Switch

The two logic outputs will provide a TTL pulse (approx. 0 V to +3.5 V ) and its complement, or an ECL pulse (approx. -0.8 V to -1.6 V ) and its complement, depending upon the position of the switch. These outputs will drive $50 \Omega$ loads.

## AC POWER INPUT

A three-pronged recessed male connector is provided on the back panel for AC power connection to the instrument. Also contained in this assembly is a $1 / 2 \mathrm{~A}$ slow-blow fuse, and a removable card, that can be removed and repositioned to switch between 120 V AC in and 240 V AC in.

## TOP AND BOTTOM COVER REMOVAL

The interior of the instrument may be accessed by removing the four Phillips screws on the rear panel. With the four screws removed, the top cover may be slid back (and off). In addition, the bottom cover may also be slid back (and off).

## ELECTROMAGNETIC INTERFERENCE

To prevent interference with other equipment, all used outputs should be connected to shielded $50 \Omega$ loads using shielded $50 \Omega$ coaxial cables. Unused outputs should be terminated with shielded $50 \Omega$ BNC terminators or with shielded BNC dust caps, to prevent unintentional electromagnetic radiation. All cords and cables should be less than 3 meters in length.

## THEORY OF OPERATION AND BASIC CIRCUITS

The pulse generator circuits of the AV-1000 family are based largely on low-jitter ECL integrated circuit and state-of-the-art buffers and operational amplifiers (op amps). This allows an unprecedented level of integration for pulse generators, and lessens the reliance on discrete components. Discrete components (i.e. transistors) have been used only in circuits where no satisfactory substitute was available.

## CIRCUIT DESCRIPTION

## TRIGGER SOURCES

As shown in the simplified circuit diagram, all of the trigger sources are fed into a multiplexer. The internal trigger is generated by the 1658 ECL oscillator (IC2), whose output frequency is controlled by the capacitance across pins 11 and 14, and the voltage on pin 2. The REPETITION RATE RANGE switch changes the capacitor, while the vernier varies the voltage on pin 2 between 0 V and -2 V . The output of the 1658 oscillator is fed into the 10174 four-into-one multiplexer (IC3). When the INT/EXT switch is in the INT position, the multiplexer selects the 1658 as its output. When the INT/EXT switch is in the EXT position, the multiplexer will select either the input that is held at a logic "high", or the external trigger input. If the SINGLE PULSE pushbutton is not depressed, the external trigger will be selected as the multiplexers output. When the user presses the SINGLE PULSE pushbutton, the logic "high" input is selected, and the output goes high, creating a short pulse. The output of this multiplexer then feeds the next stage of circuitry.

## SYNC OUT CIRCUIT

The output of the 10174 multiplexer (IC3) triggers a 10198 one-shot (i.e. a monostable multivibrator, IC6), on the rising edge of the input pulse. The one-shot then pulses high for 10ns. The narrow pulse is then translated from ECL to TTL logic levels by a 10H125 level translator (IC7), which feeds a double emitter-follower buffer. This buffer drives a $50 \Omega$ resistor in series with the SYNC OUT output. Thus, the SYNC OUT output will have TTL levels ( $0 V$ to approx. 3.5 V ) when driving a high impedance load, and an amplitude of exactly half that when driving $50 \Omega$ loads. (The series $50 \Omega$ resistor provides short circuit protection.) The SYNC OUT output is available on the SYNC OUT/TRIG IN connector only when the INT/EXT switch is in the INT position.

## DELAY CIRCUIT

The output of the multiplexer also feeds another 10198 one-shot (IC5). This one-shot, however, outputs a pulse of variable width when triggered by the rising edge of the multiplexer output.
The pulse width is controlled by the capacitor between pin 4 and ground, and the resistance between pin 6 and pin 8 (the -5V power supply). The DELAY RANGE switch switches in different capacitors onto pin 4, and the vernier is in fact a potentiometer
between pins 6 and 8 , plus a minimum resistance, which is switched in by the range switch. The output of the 10198 is an inverted ECL pulse, whose width is equal to the delay between the SYNC OUT pulse and the main output (plus various other propagation delays.) This output drives the next stage.

## PULSE WIDTH CIRCUIT

The first part of the pulse width circuit is almost identical to the delay circuit. The rising edge of the delay circuit output triggers the 10198 (IC8) to generate a non-inverted ECL pulse of variable duration. The duration sets the instrument's output pulse width. The range and vernier controls operate as in the delay section. The output of the 10198 is then transmitted through an AND gate (IC9), and the output of this gate in ANDed with its input. This serves to shave several nanoseconds off of the pulse, by taking advantage of the first AND gate's propagation delay. The two outputs of the second AND gate (it has both inverting and non-inverting outputs) drive the following sections.

## LOGIC OUTPUTS

The two ECL outputs of the pulse width section are buffered by two CLC110 fast integrated circuit buffers (IC11 and IC13), so as to be able to drive $50 \Omega$ loads. Also, the two PW section outputs are translated to TTL levels, and these TTL pulses are buffered to drive $50 \Omega$ by two CLC404 op amps (IC12 and IC14). This gives four signals: a TTL pulse, and its complement, and an ECL pulse and its complement. To feed the two LOGIC outputs on the front panel, an M-2-5V-C93401 double-pole double-throw relay (RLY1) is used to select between these four outputs. One relay switch is used to select between the non-inverted TTL and ECL signals. When the TTL/ECL switch is in the TTL position, the relay is activated, and selects the TTL signal as output for the LOGIC connector. When in the ECL position, the relay disengages, and selects the ECL signal. The other relay switch selects between the inverting outputs in a similar fashion, for transmission to the other LOGIC output.

## RISEIFALL CIRCUIT

The non-inverted ECL ouput of the pulse width circuit is fed into IC15, a 10 H 125 ECL to TTL converter. The level of this pulse is shifted by various transistor circuits, and split into two parts. One part pulses Q7, which acts as a pulsed constant current source. The other part of the input signal pulses Q8, out of phase with Q7. Q8 acts as a pulsed constant current sink. Together Q7 and Q8 drive the Rise/Fall time capacitors. Since $\mathrm{I}=\mathrm{C} d v / \mathrm{dt}$, the capacitor charges and discharges linearly, until clamped by diodes D11 and D14. It is this linear charging and discharging that governs the rise and fall times. The rise and fall times are varied within each capacitance range by varying the charging and discharged currents. This circuit yields an output that swings between 0 V and -2 V . (The -2 V level can be adjusted by R50)

## OUTPUT STAGE

The output stage takes advantages of several high speed buffers and op amps. The output from the rise and fall time section is buffered by a CLC110 buffer (IC16), to drive $50 \Omega$. The output of the buffer is fed into an MT-2-5V-C94301 relay (RLY2), which either sends the signal through an inverting buffer (the CLC404 op amp, IC17), or bypasses the buffer. If the POLARITY switch is set to "+", the op amp is bypassed, giving an output which swings between 0 and -2 V . If the POLARITY switch is in the "-" position, the relay switches revert to the other condition, sending the signal through the op amp to give an output that swings between 0 V and +2 V . This output is fed into a $50 \Omega$ potentiometer (R65), which serves as the AMPLITUDE vernier. A second relay then varies the gain-setting resistor of the SL50 output op amp (IC18). In the 5V and 10V amplitude ranges, the relay is closed, giving a total resistance of about $270 \Omega$ (R66). In the 1 V range, the relay is open, so there is a series resistance of about $1.5 \mathrm{k} \Omega$. (The second gain-setting resistor for the SL 50 op amp, a $1.5 \mathrm{k} \Omega$ resistor, is contained inside the op amp case.) The SL50 op amp is set up in an inverting op amp configuration, with offset. The offset is determined by a front-panel potentiometer voltage, which is buffered by a 741 op amp (IC19) with a gain of -0.67 . The baseline offset is fed to the op amp through a $1.5 \mathrm{k} \Omega$ resistor, giving unity gain for the offset. Lastly, the SL50 output is fed into a $50 \Omega$ backmatching resistor. Since the load is most likely $50 \Omega$, this produces a resistor divider effect at the main output. The op amp can supply a maximum of $\pm 11 \mathrm{~V}$ out, so this $50 \Omega$ backmatching resistor is shorted out in the 10 V range to provide for maximum voltage swing, with slightly increased reflections and distortion.

## POWER SUPPLIES

A dual secondary transformer (DMT-8-12) and seven linear regulator IC's are used to generate the following voltages:

| -2 | Volts | $(150$ | $\mathrm{mA})$ |
| ---: | ---: | ---: | ---: |
| -5.2 | Volts | $(600$ | $\mathrm{mA})$ |
| +15 | Volts | $(200$ | $\mathrm{mA})$ |
| -15 | Volts | $(100$ | $\mathrm{mA})$ |
| +5 | Volts | $(350$ | $\mathrm{mA})$ |
| +24 | Volts | $(20$ | $\mathrm{mA})$ |
| -24 | Volts | $(20$ | $\mathrm{mA})$ |

The voltage test points are provided on the power supply printed circuit.

## TEST POINT WAVEFORMS

Twelve key TP points (test points) are indicated on the circuit diagrams along with sample waveforms. In the event of an instrument failure or malfunction, the TP waveforms may be used to isolate and identify the offending circuit (or component). The twelve TP points are indicated in white lettering on the pulse generator printed circuit board.

## MAINTENANCE

No special maintenance is required to operate the AV-1000. INSPECTION. To examine the interior of the instrument, one must remove the four screws on the back of the unit two at the extreme left, and two at the extreme right. Remove the two bars that these hold in place, and the top and bottom cover will be free to slide off. Any accumulated dust inside the instrument should be blown or brushed off.

## PARTS REPLACEMENT

The WA01 output amplifier is socketed. If the output stage has been somehow damaged, and the WA01 has been identified as the damaged part by examining the test points, this part may be easily replaced, either by ordering the part from Avtech Electrosystems Ltd., or Apex Microtechnology Corp. If the instrument has any other defective circuitry, the unit should be sent back to Avtech for repair. However, to facilitate field maintenance outside the warranty period, this manual includes complete schematics and key test point data.

## SCHEMATICS AND TEST POINTS

## PARTS LIST

## INTEGRATED CIRCUITS

| PART NO. | SOURCE | DESCRIPTION | DEVICE |
| :--- | :--- | :--- | :--- |
|  | IC1 | VARIOUS |  |
| IC2 | MOTOROLA | NAND GATE | OSCILLATOR |
| IC3 | MOTOROLA | MULTIPLEXER | MC1658P |
| IC4 | MOTOROLA | TTL-ECL TRANSLATOR | MC10174P |
| IC5 | MOTOROLA | ONE-SHOT | MC10H124P |
| IC6 | MOTOROLA | ONE-SHOT | MC10198PP |
| IC7 | MOTOROLA | ECL-TTL TRANSLATOR | MC10H125P |
| IC8 | MOTOROLA | ONE-SHOT | MC10198P |
| IC9 | MOTOROLA | AND GATE | MC10104P |
| IC10 | MOTOROLA | ECL-TTL TRANSLATOR | MC10H125P |
| IC11 | COMLINEAR | BUFFER | CLC110AJP |
| IC12 | COMLINEAR | OP AMP | CLC404AJP |
| IC13 | COMLINEAR | BUFFER | CLC110AJP |
| IC14 | COMLINEAR | OP AMP | CLC404AJP |
| IC15 | MOTOROLA | ECL-TTL TRANSLATOR | MC10H125P |
| IC16 | COMLINEAR | BUFFER | CLC110AJP |
| IC17 | COMLINEAR | OP AMP | CLC404AJP |
| IC18 | AVTECH | OP AMP | SL50 |
| IC19 | VARIOUS | OP AMP |  |

## RELAYS

| PART NO. | SOURCE | DESCRIPTION |  |
| :--- | :--- | :--- | :--- |
|  |  |  | DEVICE |
| RLY1 | ITT | DPDT | MT-2-5V-C93401 |
| RLY2 | ITT | DPDT | MT-2-5V-C93401 |
| RLY3 | POTTER | SPST | JWD-107-5 |
|  | AND BRUMFIELD |  |  |
| RLY4 | POTTER | SPST | JWD-107-5 |

## DIODES

| PART NO. | SOURCE | DESCRIPTION | DEVICE |
| :--- | :--- | :--- | :--- |
| D1 |  |  | 1N4150 |
| D2 |  |  | 1N4150 |
| D3 |  |  | 1N4150 |
| D4 |  | 1N4150 |  |
| D5 |  | 1N4150 |  |
| D6 |  | 1N4150 |  |
| D7 |  | 1N4150 |  |
| D8 |  | 1N4150 |  |
| D9 |  |  | 1N4150 |
| D10 | HEWLETT- | 1N4150 |  |
| D11 |  | $5082-1001$ |  |
|  |  |  |  |
| D12 |  |  | 1N4150 |
| D13 |  | 1N4150 |  |
| D14 |  | $5082-1001$ |  |
| D15 |  |  | 1N4150 |
| D16 |  | 1N4150 | 1N5819 |

## CAPACITORS

PART NO
SOURCE
DESCRIPTION
DEVICE
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
C12
C13
C14
C15
C16

820pF<br>10000pF<br>0.47 uF<br>0.15uF<br>0.047uF<br>1000pF<br>5000pF<br>2400pF<br>2000pF<br>1000pF<br>300pF<br>470pF<br>120pF<br>33pF<br>8.2pF<br>100pF

## CAPACITORS (CONT'D.)

| PART NO. | SOURCE | DESCRIPTION | DEVICE |
| :---: | :---: | :---: | :---: |
| C17 |  | 1000pF |  |
| C18 |  | 300pF |  |
| C19 |  | 4700pF |  |
| C20 |  | 6800pF |  |
| C21 |  | 0.1uF |  |
| C22 |  | 0.022uF |  |
| C23 |  | 100pF |  |
| C24 |  | 1000pF |  |
| C25 |  | 300pF |  |
| C26 |  | 4700pF |  |
| C27 |  | 6800pF |  |
| C28 |  | 0.1uF |  |
| C29 |  | 0.022uF |  |
| C30 |  | 82pF |  |
| C31 |  | 15pF |  |
| C32 |  | 0.022uF |  |
| C33 |  | 47uF |  |
| C34 |  | 0.022uF |  |
| C35 |  | 47uF |  |
| C36 |  | 820pF |  |
| C37 |  | 0.022uF |  |
| C38 |  | 6.8uF |  |
| C39 |  | 6.8uF |  |
| C40 |  | 0.022uF |  |
| C41 |  | 820pF |  |
| C42 |  | 220pF |  |
| C43 |  | 2500pF |  |
| C44 |  | 0.033uF |  |
| C45 |  | 0.33uF |  |
| C46 |  | 2.2uF |  |
| C47 |  | 2.2uF |  |
| C48 |  | 120pF |  |

## RESISTORS AND POTENTIOMETERS

| PART NO. | SOURCE | DESCRIPTION | DEVICE |
| :---: | :---: | :---: | :---: |
| R1 |  | $4.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |  |
| R2 |  | $4.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |  |
| R3 |  | $4.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |  |
| R4 |  | $4.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |  |
| R5 |  | 25k $\Omega$ POTENTIOMETER |  |
| R6 |  | 51, , 1/4W |  |
| R7 |  | 51, , 1/4W |  |
| R8 |  | 51, , 1/4W |  |
| R9 |  | 51 $\Omega, 1 / 2 \mathrm{~W}$ |  |
| R10 |  | 51, , 1/4W |  |
| R11 |  | 51 $2,1 / 4 \mathrm{~W}$ |  |
| R12 |  | 51 $\Omega$, 1/4W |  |
| R13 |  | $1 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |  |
| R14 |  | 200, $1 / 4 \mathrm{~W}$ |  |
| R15 | BOURNS | $10 \mathrm{~K} \Omega$ POTENTIOMETER |  |
| R16 |  | 51, 1/4W |  |
| R17 |  | 200 $2,1 / 4 \mathrm{~W}$ |  |
| R18 | BOURNS | 10K $\Omega$ POTENTIOMETER |  |
| R19 |  | 51, 1/4W |  |
| R20 |  | $51 \Omega, 1 / 4 \mathrm{~W} \Omega$ |  |
| R21 |  | 100, $1 / 4 \mathrm{~W}$ |  |
| R22 |  | $470 \Omega, 1 / 4 \mathrm{~W}$ |  |
| R23 |  | 51 $\Omega$, 1/4W |  |
| R24 |  | 390, $1 / 4 \mathrm{~W}$ |  |
| R25 |  | $100 \Omega, 1 / 4 \mathrm{~W}$ |  |
| R26 |  | 390 ${ }^{\text {, }} 1 / 4 \mathrm{~W}$ |  |
| R27 |  | 100 , 1/4W |  |
| R28 |  | $100 \Omega, 1 / 4 \mathrm{~W}$ |  |
| R29 |  | $470 \Omega, 1 / 4 \mathrm{~W}$ |  |
| R30 |  | 51 $\Omega$, 1/4W |  |
| R31 |  | 51 $\Omega, 1 / 4 \mathrm{~W}$ |  |
| R32 |  | 51, 1/4W |  |
| R33 |  | 1.2k $\Omega, 1 / 4 \mathrm{~W}$ |  |
| R34 |  | 51, 1/2W |  |
| R35 |  | $560 \Omega, 1 / 4 \mathrm{~W}$ |  |
| R36 |  | $5.6 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |  |
| R37 |  | 22S, 1/4W |  |
| R38 |  | 918, 1/4W |  |
| R39 |  | 688, 1/4W |  |
| R40 |  | 39 k , 1/4W |  |


| PART NO. | SOURCE | DESCRIPTION |
| :---: | :---: | :---: |
| R41 |  | 33 2 , 1/2W |
| R42 |  | 10 $\Omega$, 1/4W |
| R43 |  | 33 2 , 1/4W |
| R44 |  | 25k P POTENTIOMETER |
| R45 |  | 1.2k $\Omega, 1 / 4 \mathrm{~W}$ |
| R46 |  | $1.2 \mathrm{~K} \Omega, 1 / 4 \mathrm{~W}$ |
| R47 |  | $25 \mathrm{k} \Omega$ POTENTIOMETER |
| R48 |  | $10 \mathrm{~K} \Omega, 1 / 4 \mathrm{~W}$ |
| R49 |  | $56 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R50 |  | 10k $\Omega, 1 / 4 \mathrm{~W}$ |
| R51 |  | $27 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R52 |  | $2.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R53 |  | 2.2k $\Omega, 1 / 4 \mathrm{~W}$ |
| R54 |  | $25 \mathrm{~K} \Omega$ POTENTIOMETER |
| R55 |  | $2.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R56 |  | $4.7 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R57 |  | 10k $\Omega, 1 / 4 \mathrm{~W}$ |
| R58 |  | $15 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R59 |  | $2.7 \Omega, 1 / 4 \mathrm{~W}$ |
| R60 |  | 1.2k 2 , 1/4W |
| R61 |  | $25 \mathrm{k} \Omega$ POTENTIOMETER |
| R62 |  | $470 \Omega, 1 / 4 \mathrm{~W}$ |
| R63 |  | 51, $1 / 4 \mathrm{~W}$ |
| R64 |  | 470 , 14W |
| R65 | BOURNS | $50 \Omega$ POTENTIOMETER |
| R66 |  | $270 \Omega, 1 / 4 \mathrm{~W}$ |
| R67 |  | $1.2 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R68 |  | $1.5 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R69 |  | $51 \mathrm{k} \Omega, 1 / 4 \mathrm{~W}$ |
| R70 |  | 75k $\Omega, 1 / 4 \mathrm{~W}$ |
| R71 |  | 5 k ¢ POTENTIOMETER |
| R72 | IRC | 51, 3W |

## SWITCHES

SW1
SW2
SW3
SW4
SW5
SW6
SW7
SW8
SW9
SW10

|  | EXT/INT SWITCH |
| :--- | :--- |
|  | SINGLE PULSE SWITCH |
|  | REP RATE RANGE |
|  | DELAY RANGE SWITCH |
|  | PW RANGE SWITCH |
|  | TTL/ECL SWITCH |
|  | RISE/FALL RANGE |
|  | POLARITY SWITCH |
| GUGATVAR SWITCH |  |
| GUGALCOSWITCH | POWER SWITCH |

TRANSISTORS

| PART NO. | SOURCE | DESCRIPTION | DEVICE |
| :---: | :---: | :---: | :---: |
| Q1 |  |  | 2N2222 |
| Q2 |  |  | 2N2907 |
| Q3 |  |  | 2N5836 |
| Q4 |  |  | 2N5836 |
| Q5 |  |  | 2N4209 |
| Q6 |  |  | 2N2369 |
| Q7 |  |  | 2N4209 |
| Q8 |  |  | 2N2369 |
| Q9 |  |  | 2N2222 |
| Q10 |  |  | 2N2907 |
| Q11 |  |  | 2N2222 |
| Q12 |  |  | 2N2907 |

## MISCELLANEOUS

PART NO

SOURCE

AAVID
H.H. SMITH
H.H. SMITH

AUGAT/ALCOSWITCH AUGAT/ALCOSWITCH AUGAT/ALCOSWITCH (generic)

DESCRIPTION

HEAT SINK
FLEXIBLE SHALFS
COUPLERS
KNOBS
PUSHBUTTON COVER PUSHBUTTON COVER LAMP

BNC CONNECTORS
\#382
DEVICE

