

INSTRUCTIONS

MODEL AVM-2-N-TERA2 PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

TECHNICAL SUPPORT

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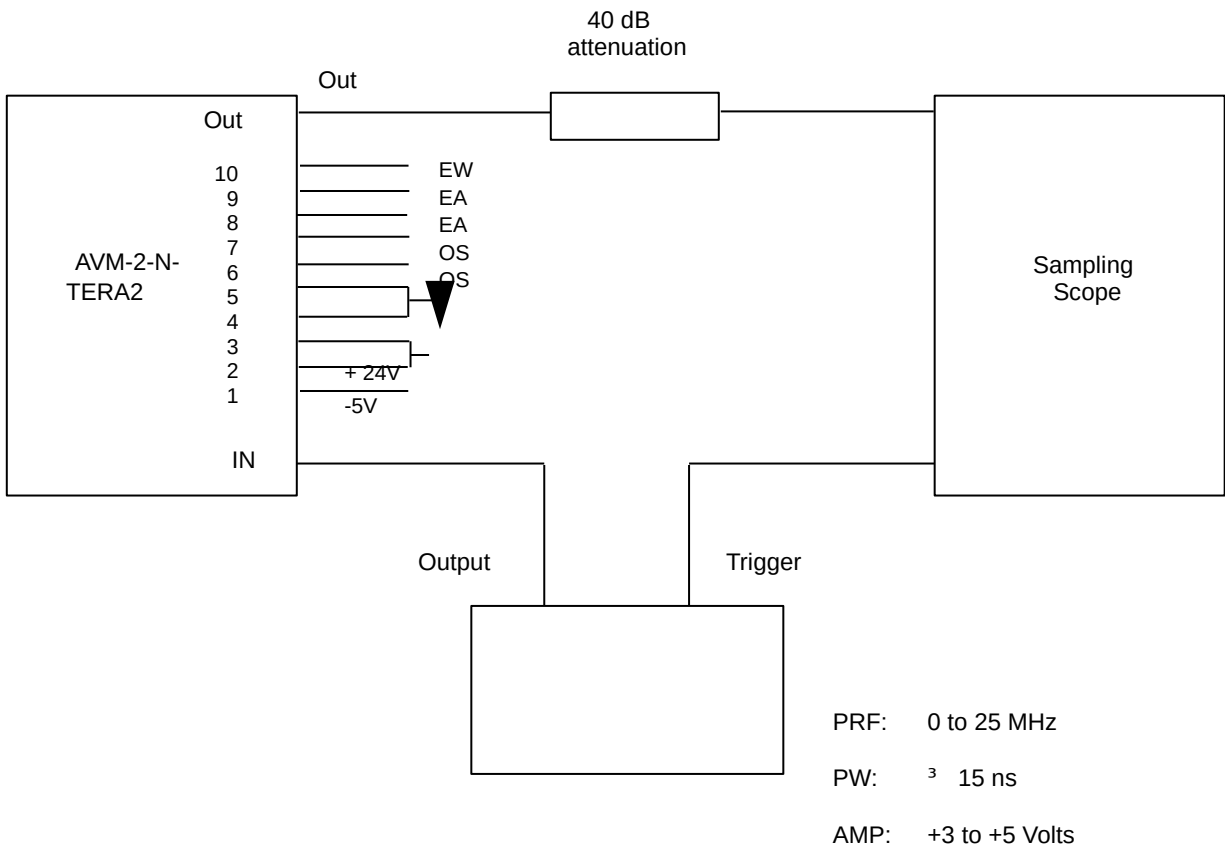
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Manual Reference: /fileserver1/officefiles/instructword/avm/older manuals/AVM-2-N-TERA2eda-fig.doc, created June 7, 2000

FIG. 1: PULSE GENERATOR TEST ARRANGEMENT



GENERAL OPERATING INSTRUCTIONS

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 40db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 us range.
- 4) When testing using a general purpose 50 ohm laboratory pulse generator as the input trigger signal source, the input signal should be applied via a 50 ohm feed-through load or alternatively, the input to the AVM unit should be shunted with a 50 ohm resistor. This will prevent reflection (and degradation of the input pulse waveform) caused by the high impedance at the IN port. However, when triggering from a TTL source, no 50 ohm feed-through load or resistor is necessary but lead length should be as short as possible. High-speed TTL Schottky logic is recommended for the driving circuitry.
- 5) The input trigger pulse width should be greater than 15 ns and less than one half of the pulse repetition frequency period. The unit triggers on the leading edge of the input trigger signal.
- 6) The DC control voltages and the DC prime power supplies are applied to the unit via the amp 1-640441-0 connector (which is supplied along with a 59803-1 installation tool). The connector mates to 24 AWG.
CAUTION: When connecting the connector to the chassis always insure that pin 1 on the connector aligns with and mates with pin 1 on the chassis. The unit may be damaged if this alignment is not achieved. Such damage is not covered by the warranty.
- 7) The DC prime power of +24VDC and -5V are respectively connected to pins 2,3 and to pin 1.
- 8) The output pulse width is controlled by means of 0 to +10 VDC applied to pin 10.
- 9) The output pulse amplitude is controlled by means of 0 to +10 VDC applied to pin 8 or 9. The pulse width may change by several nanoseconds as the output amplitude is reduced from maximum to minimum. Therefore it is convenient to first set the desired amplitude and then set the desired pulse width.

- 10) To DC offset the output pulse connect a DC power supply set to the required DC offset value pins 6 or 7. The maximum DC offset voltage is 50 volts (200 mA).
- 11) WARNING: Model AVM-2-N-TERA2 may fail if triggered at a PRF greater than 25.0 MHz.
- 12) The AVM unit requires a maximum prime input power of about 12 watts. It is therefore strongly recommended that the unit be heatsunk in order to maintain a moderate chassis temperature. The heat sink can be attached using the four 2-56 tapped holes on the bottom of the chassis. The four blind holes are tapped to at least 0.3 inches.
- 13) For additional assistance:

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PERFORMANCE CHECK SHEET