

INSTRUCTIONS

MODEL AVO-8C-PS PULSE GENERATOR

S.N.:

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

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FIG. 1: PULSE GENERATOR TEST ARRANGEMENT

## GENERAL OPERATING INSTRUCTIONS

- 1) The equipment should be connected in the general fashion shown above. Since the AVO unit provides an output pulse rise time as low as 0.1 us a fast oscilloscope (at least 50 MHz) should be used to display the waveform.
- 2) The user supplied lab power supply attaches to the -PG output module via the red and black SUPERCON connectors which are supplied. The positive terminal of the power supply is to be connected to the RED SUPERCON connector on the -PG module. The negative terminal on the lab power supply is to be connected to the BLACK SUPERCON connector on the -PG module. For a -P unit, the negative terminal on the lab power supply must be grounded while for a -N unit the positive terminal must be grounded.
- 3) The output module (-PG) must be connected to the mainframe via the RS232 style gray cable (which mates to the OUT connector on the mainframe rear panel).
- 4) The output terminals of the pulse generator module consists of a short length of microstrip transmission line protruding from the module chassis. The OUT terminal is the center conductor which is bounded on both sides by the ground plane (see below). Note that the "OUT" red banana terminal is in parallel with the microstrip center conductor and so may also be used as the output terminal.

The load should be connected between the OUT and GND terminals using very short leads ( $\leq 2.0$  cm).

Take care to insure that during soldering the OUT conductor is not shorted to the chassis. Also, use minimal heat when soldering.

- 5) When the PW MODE switch is in the INT position, the output pulse width is controlled by the 4 position range switch and one turn fine control. To voltage control the output pulse width within each range, set the rear panel switch in the EXT position and apply 0 to +10 volts between terminal A and ground ( $R_{IN} \geq 10K$ ). (option). (See (7) for PW control when the PW MODE switch is in the EXT position).
- 6) The output amplitude is controlled by the amplitude of the DC potential supplied to the SUPERCON connectors on the PG module.
- 7) An external clock may be used to control the output PRF of the unit by setting the front panel INT-EXT-DC switch in the INT position and applying a 50 ns (or wider) TTL level pulse to the TRIG BNC connector input. The external clock may also be used to control the output pulse width by setting the PW mode switch in the EXT position (in this case,  $PW_{OUT} = PW_{IN}$ ). With the switch set in the DC position, the output duty cycle is 100% (ie DC)
- 8) The AVO-8C is designed to supply up to 200 amperes peak to a maximum load voltage of 40 volts. Factory tests are conducted with a 0.2 ohm load capable of dissipating at least 4000 watts. Higher load resistance values may be used but the input voltage must be limited to 40 volts or less. Note that the unit may fail if the average output current exceeds 100 Amp.
- 9) Model AVO-8C will operate with a duty cycle at least as high as 85% when the pulse width is controlled by the front panel Pulse Width and Pulse Width Range controls. A duty cycle of 100% is obtained by setting the pulse width mode switch in the DC position. In this position the PW controls are no longer active. In this position the load voltage will be about 1.5 volts less than the applied DC voltage for a load current of 100 amperes. Note that the average output current must never exceed 100 Amp.
- 10) The unit can be converted from 120 to 240V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector cable connector assembly.
- 11) The rear panel monitor output BNC (M) provides an output voltage (to 50 ohms or higher) which is proportional to the current flowing through the microstrip output terminals ( $500\text{ mV} \approx 200\text{ Amps}$ ). Note that the monitor function is not connected to the banana output terminals (option).
- 12) Model AVO-8C-PS includes a high voltage protection circuit which sounds a loud audible alarm if the applied DC voltage exceeds  $\approx +45\text{ VDC}$ . The output stages of the unit will not trigger while the alarm is sounding. The unit also

includes a temperature limit circuit which sounds an audible alarm if the temperature of the output stage switching elements exceeds +35°C (this will occur if the average current exceeds 100 Amps).

13) If application assistance is required:

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Fig. 2: FRONT PANEL CONTROLS

## FRONT PANEL CONTROLS

- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PW Control. A one turn control and four position range switch which varies the output pulse width as follows (when the PW MODE switch is in the INT mode):

Range 1	2.0 us to 20 us
Range 2	20 us to 200 us
Range 3	200 us to 2 ms
Range 4	2 ms to 20 ms
- (3) PW MODE. With the MODE switch in the INT position the PW is controlled by the front panel controls. When the switch is in the EXT position, the PW is controlled by the TTL PW applied externally to the TRIG input. Model AVO-8C-C will operate with a duty cycle at least as high as 85% when the PW MODE switch is in the INT position and the pulse is controlled by the front panel Pulse Width and Pulse Width Range controls. A duty cycle of 100% is obtained by setting the INT-EXT -DC switch in the DC position. In this position the PW controls are no longer active. In this position the load voltage will be about 1.5 volts less than the applied DC voltage for a load current of 100 amperes.
- (4) TRIG IN. A TTL trigger pulse (PW >50 ns) is applied to this BNC input connector.

FIG 3: BACK PANEL CONTROLS

## BACK PANEL CONTROLS

- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.5 A SB).
- (2) M OUT. BNC connector provides output voltage (to  $R_L \geq 50$  Ohms) which is proportional to load current through the banana output terminals (500 mV  $\approx$  200 Amp). (option).
- (3) OUT. The gray RS232 style cable from the output module connects to this connector.

AVO-8-C BLOCK DIAGRAM

## SYSTEM BLOCK DIAGRAM

## START-UP CHECK LIST

- 1) The instruction manual has been studied thoroughly.
- 2) The -PG module is connected to the mainframe.
- 3) The load is connected to the output module. If the load is a diode, the anode of the load is connected to the OUT terminal. Note that with a diode load, a current limiting high power resistor must be placed in series with the diode to help limit the peak current. For initial testing, it is recommended that a resistive load be used. Factory tests are conducted using a 0.1 Ohm 4000 Watt resistive load.
- 4) The user-supplied lab power supply is connected with the positive terminal connected to the red SUPERCON on the PG module and the negative terminal connected to the black SUPERCON. The negative terminal is grounded. The power supply potential is set to zero.
- 5) Set the mainframe pulse width controls at the approximate desired values and apply a TTL trigger pulse (>50 ns) to the BNC connector (while insuring that the duty cycle is less than 10%).
- 6) Set the INT-EXT switch on INT.
- 7) Connect the rear panel M output to the scope (1 VOLT/DIV) and connect the TRIG out to the scope time base.
- 8) Turn on the prime power to the mainframe. The scope time base should be triggering.
- 9) Connect a scope probe across the resistive test load and apply prime power to the lab power supply (after first insuring that the output amplitude is set to zero).
- 10) Gradually increase the output amplitude on the lab power supply and observe the waveforms on the scope and the DC current level on the DC power supply. A rectangular pulse should appear on the scope (for both the load voltage and monitor channels) and the amplitude should increase as the amplitude control on the mainframe is rotated clockwise. At the same time, the average current supplied by the DC supply will increase. Observe the pulse width and pulse period on the scope and confirm that the duty cycle does not exceed 10% and that the peak current does not exceed 500 Amps (i.e. 0.5 Volts out from the monitor output).

- 11) Observe the DC current supplied by the DC supply and insure that the average current does not exceed 100 Amperes.
- 12) Adjust pulse width, pulse period (i.e. PRF) and amplitude to obtain the desired settings.
- 13) If additional assistance is required:

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## PERFORMANCE CHECK