

INSTRUCTIONS

MODEL AVOZ-A1A-C-N PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

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FIG. 1: MODEL AVOZ-C PULSE GENERATOR TEST ARRANGEMENT

(RESISTIVE LOAD, NO DIODE)

GENERAL OPERATING INSTRUCTIONS

- 1) The equipment should be connected in the general fashion shown above. Since the AVOZ unit provides an output pulse rise time as low as 10 ns a fast oscilloscope (at least 50 MHz and preferably 200 MHz) should be used to display the waveform. If a sampling scope is used, a 40 dB (or greater) attenuator should be used to insure a peak input to the scope of less than 0.5 Volts.
- 2) The TRIG output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some scopes, a 30 dB attenuator should be placed at the input to the scope trigger channel. The TRIG output precedes the main output when the front panel ADVANCE-DELAY switch is in the ADVANCE position. The TRIG output lags the main output when the switch is in the DELAY position.
- 3) To obtain a stable output display the PRF controls on the front panel should be set mid-range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY control and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF controls.
- 4) The detachable AV-LZ1 flexible output line which protrudes from the front panel of the mainframe has a characteristic impedance of one Ohm. The diode load and a non-inductive load resistor should be solder connected to the end of the line as shown below (using extremely short lead lengths (e.g. 0.2 cm) so as to reduce inductance). The series combination of the laser diode and the load resistor R_L should present one Ohm to the end of the line. For many diodes, a load resistor of one Ohm may be selected as a first choice. The diode current may be monitored by monitoring the voltage across the resistor (or by means of a current probe). An ultra fast rectifier diode (e.g. MBR160) may be placed across the laser diode to protect against reverse transients.

The AV-LZ1 cable inserts into the female out connector on the front panel. It is critically important that the “up” side of the line be visible.

- 5) The output pulse width is controlled by means of the front panel one turn PW control. To voltage control the pulse width, set the rear panel switch in the EXT position and apply 0 to +10 Volts between terminal A and ground ($R_{IN} \geq 10K$). (option).
- 6) The output pulse amplitude is controlled by means of the front panel one turn AMP control. To voltage control the output, set the rear panel switch in the EXT position and apply 0 to +10 Volts between terminal A and ground ($R_{IN} \geq 10K$). (option).
- 7) AVOZ units with a serial number higher than 6500 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. To resume operation, remove the overload condition by:
 - 1) Reducing PRF (i.e. switch to a lower range)
 - 2) Reducing pulse width (i.e. switch to a lower range)
 - 3) Removing output load short circuit (if any)

After the overload condition is removed, the overload light should extinguish and the output should reappear. In extreme cases, it may be necessary to briefly (10 seconds) remove the prime power to the instrument.

- 8) The unit can be converted from 110 to 220V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
- 9) For additional assistance:

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FIG. 2: FRONT PANEL CONTROLS

FRONT PANEL CONTROLS

- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. Varies PRF as follows:

RANGE 1	100 Hz - 1 kHz
RANGE 2	1 kHz - 10 kHz
RANGE 3	10 kHz - 100 kHz
RANGE 4	100 kHz - 1 MHz
- (3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (7). This delay is variable over the range of 0 to about 20 ns. The TRIG output precedes the main output when the ADVANCE-DELAY switch is in the ADVANCE position and lags when the switch is in the DELAY position.
- (4) TRIG Output. This output precedes the main output (7) and is used to trigger the sampling scope time base. The output is a TTL level 100 ns (approx) pulse capable of driving a fifty Ohm load.
- (5) PW Control. A one turn control which varies the output pulse width from 15 ns to 150 ns.
- (6) AMP Control. The output pulse amplitude is controlled by means of the one turn potentiometer (AMP).
- (7) OUT. Detachable 1 meter long AV-LZ1 flexible output line inserts into female connector from the front panel. Diode load and series matching resistor to be solder connected to end of line. Total resistance to equal one Ohm to obtain 50 amperes when mainframe outputs 20 Volts.
- (8) EXT-INT Control. With this toggle switch in the INT position, the PRF of the unit is controlled via an internal clock which in turn is controlled by the PRF controls. With the toggle switch in the EXT position, the unit requires a 0.2 us TTL level pulse applied at the TRIG input in order to trigger the output stages.

(9) AVOZ units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) until the overload is removed by:

- 1) Reducing PRF (i.e. switch to a lower range)
- 2) Reducing pulse width (i.e. switch to a lower range)
- 3) Removing output load short circuit (if any)

In extreme cases it may be necessary to briefly (e.g. 10 seconds) remove the prime power to the instrument.

FIG. 3: BACK PANEL CONTROLS

BACK PANEL CONTROLS

- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.5 A SB).

SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVOZ-A-C unit consists of the following basic modules:

- 1) AVOZ-A-PG pulse generator module
- 2) AVOZ-A-PS power supply module
- 3) AVOZ-A-CL clock module
- 4) +24V power supply board

The modules are interconnected as shown in Fig. 4. The clock module controls the output PRF and the relative delay between the main output and the TRIG output. The PG pulse generator module generates the output pulse. In the event of an instrument malfunction, it is most likely that the rear panel 1.0A SB fuse may have failed due to an output short circuit condition or to a high duty cycle condition. If the fuse has not blown, then the four Phillips screws on the back panel should be removed. The top cover may then be slid off and the operation of the clock and power supply modules checked. The clock module is functioning properly if:

- (a) 0.1 us TTL level outputs are observed at pins 2 and 3.
- (b) The PRF of the outputs can be varied over the range of 2 Hz to 20.0 kHz using the PRF and PRF FINE controls.
- (c) The relative delay between the pin 2 and 3 outputs can be varied by at least 1 us by the DELAY control.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed. The power supply board generates +24V DC to power the other modules. If the voltage is less than +24V, turn off the prime power and unsolder the lead from the 7824 regulator chip on the power supply board. Solder a 100 Ohm 5 Watt resistor to the 7824 output to ground and turn on the prime power. A voltage of +24 Volts should be read. If the voltage is less then the power supply board is defective and should be repaired or replaced.

PERFORMANCE CHECK SHEET