



**AVTECH ELECTROSYSTEMS LTD.**

NANOSECOND WAVEFORM ELECTRONICS  
SINCE 1975

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BOX 5120, LCD MERIVALE  
OTTAWA, ONTARIO  
CANADA K2C 3H4

## INSTRUCTIONS

MODEL AVR-EBF6-B

+100 mA to +1 Amp  
FORWARD RECOVERY  
TFR / VFR MEASUREMENT SYSTEM

SERIAL NUMBER: \_\_\_\_\_

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

### TECHNICAL SUPPORT

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## TABLE OF CONTENTS

<b>WARRANTY.....</b>	<b>2</b>
<b>TECHNICAL SUPPORT.....</b>	<b>2</b>
<b>TABLE OF CONTENTS.....</b>	<b>3</b>
<b>INTRODUCTION.....</b>	<b>6</b>
<b>SPECIFICATIONS.....</b>	<b>8</b>
<b>EUROPEAN REGULATORY NOTES.....</b>	<b>9</b>
EC DECLARATION OF CONFORMITY.....	9
DIRECTIVE 2002/95/EC (RoHS).....	9
DIRECTIVE 2002/96/EC (WEEE).....	9
<b>INSTALLATION.....</b>	<b>11</b>
VISUAL CHECK.....	11
POWER RATINGS.....	11
CONNECTION TO THE POWER SUPPLY.....	11
PROTECTION FROM ELECTRIC SHOCK.....	12
ENVIRONMENTAL CONDITIONS.....	13
<b>FUSES.....</b>	<b>14</b>
AC FUSE REPLACEMENT.....	14
DC FUSE REPLACEMENT.....	15
FUSE RATINGS.....	15
<b>FRONT PANEL CONTROLS.....</b>	<b>16</b>
<b>REAR PANEL CONTROLS.....</b>	<b>18</b>
<b>TIMING CONTROL.....</b>	<b>20</b>
BASIC TIMING CONTROL.....	20
TRIGGER MODES.....	21
GATING MODES.....	21
<b>BASIC AMPLITUDE CONTROL.....</b>	<b>23</b>
SETTING THE AMPLITUDE LEVELS.....	24
AMPLITUDE ACCURACY.....	24
INCORRECT ORIENTATION.....	25
CABLE LENGTHS.....	25
ACCESSIBLE VOLTAGES.....	25
<b>STANDARD TEST JIG MECHANICAL ASPECTS.....</b>	<b>26</b>

ALL TEST JIGS.....	26
AVX-TFR-MIX TEST JIG.....	26
AVX-TFR-ANB TEST JIG.....	29
AVX-TFR-MELF.....	30
<b>TYPICAL RESULTS.....</b>	<b>31</b>
1N5811 RESULTS.....	31
MQ1N5811US RESULTS.....	33
<b>TROUBLESHOOTING.....</b>	<b>34</b>
<b>PROGRAMMING YOUR PULSE GENERATOR.....</b>	<b>35</b>
KEY PROGRAMMING COMMANDS.....	35
ALL PROGRAMMING COMMANDS.....	36
<b>MECHANICAL INFORMATION.....</b>	<b>38</b>
TOP COVER REMOVAL.....	38
RACK MOUNTING.....	38
ELECTROMAGNETIC INTERFERENCE.....	38
<b>MAINTENANCE.....</b>	<b>39</b>
REGULAR MAINTENANCE.....	39
CLEANING.....	39
<b>WIRING DIAGRAMS.....</b>	<b>40</b>
WIRING OF AC POWER.....	40
PCB 158K - LOW VOLTAGE POWER SUPPLY, 1/3.....	41
PCB 158K - LOW VOLTAGE POWER SUPPLY, 2/3.....	42
PCB 158K - LOW VOLTAGE POWER SUPPLY, 3/3.....	43
PCB 168B - HIGH VOLTAGE DC POWER SUPPLY.....	44
PCB 104D - KEYPAD / DISPLAY BOARD, 1/3.....	45
PCB 104D - KEYPAD / DISPLAY BOARD, 2/3.....	46
PCB 104D - KEYPAD / DISPLAY BOARD, 3/3.....	47
PCB 201A - INTERLOCK CONTROL.....	48
MAIN WIRING.....	50
STANDARD TEST JIG WIRING (AVX-TFR-MIX).....	51
TEST JIG WIRING (AVX-TRR-SCHA / AVX-TRR-MSB-MELF).....	52
TEST JIG WIRING (AVX-TRR-SCHA / AVX-TRR-MSB-STUD).....	53
<b>PERFORMANCE CHECK SHEET.....</b>	<b>54</b>



## INTRODUCTION

The AVR-EBF6-B is a high performance, GPIB and RS232-equipped test system for measuring the  $t_{FR}$  and  $V_{FR}$  forward recovery characteristics of high speed rectifiers.

The AVR-EBF6-B consists of three main components:

1. The instrument mainframe, which generates voltage pulses in the range of 0 to +50V with rise times  $< 5$  ns.
2. A rise time filter, which increases the rise time of the voltage pulse to 10 ns (or optionally 8 ns or 12 ns),  $\pm 1.2$  ns.
3. A test jig, into which the device under test (DUT) is inserted.

Depending on the options and accessories ordered, the instrument may be supplied with more than one rise time filter and more than one test jig.

The mainframe connects to the rise time filter using a length of coaxial cable. The output of the filter connects to the test jig using another length of coaxial cable. A control cable connects the mainframe to the test jig, to control the safety interlocks. The test jig output is fed into the 50 Ohm input of an oscilloscope by a third length of coaxial cable.

The (user-supplied) oscilloscope is used to measure the  $V_{FR}$  and  $t_{FR}$  values based on the waveform from the test jig output.

The waveforms generated by this instrument are suitable for MIL-STD-750E Method 4026.3 tests. In the terminology of this standard:

- $I_F = 100$  mA to 1 A
- Rise Time = 10 ns (unless the optional 8 or 12 ns filters are used)
- Pulse width  $t_1 = 200$  ns to 100  $\mu$ s
- Generator Resistance  $R_S = 50$  Ohms
- Pulse frequency = 1 Hz to 2 kHz
- Response detector input impedance  $Z = 500$  Ohms

The test jig supplied with the instrument is carefully constructed such that the parasitic inductance is minimized. This prevents overestimation of  $V_{FR}$ , and provides increased  $t_{FR}$  accuracy.

Standard AVR-EBF6-B models include one AVX-TFR-MIX diode test jig. This standard test jig contains a variety of pin sockets and posts, which may be used to hold the diode device under test (DUT). The test jig has a hinged lid, which must be fully closed to protect the user from high voltages. The output will be automatically disabled if the lid is left open. The standard AVR-TFR-MIX test jig will accommodate TO-220AC (2 lead)

packages, DO-style packages with leads bent at 90°, and standard and reverse-polarity TO-3 packages.

This instrument may be ordered with the AVX-TFR-ANB test jig instead. This test jig accepts DO-41 and Microsemi Type E packages. In contrast to the AVX-TFR-MIX, no lead bending is required.

Test jigs accommodating MELF packages (AVX-TFR-MELF) and DO-4 and DO-5 stud packages (AVX-TFR-STUD) can also be provided as separate accessories.

While the provided test jig is intended to be flexible and easy to use, users can also develop their own test jigs easily.

The AVR-EBF6-B includes an internal trigger source, but it can also be triggered or gated by an external source. A front-panel pushbutton can also be used to trigger the instrument.

The AVR-EBF6-B features front panel keyboard and adjust knob control of the output pulse parameters along with a four line by 40-character backlit LCD display of the output amplitude, pulse repetition frequency, and delay. The instrument includes memory to store up to four complete instrument setups. The operator may use the front panel or the computer interface to store a complete “snapshot” of all key instrument settings, and recall this setup at a later time.

This instrument is intended for use in research, development, test and calibration laboratories by qualified personnel.

## SPECIFICATIONS

Model <sup>1</sup> :	AVR-EBF6-B
Recovery type:	Forward recovery
Intended application:	Most diode types
Basic waveform:	A positive pulse
Pulse polarity:	+
Voltage output <sup>2,6,8</sup> : (to $R_L = 50\Omega$ )	+5V to +50V
Corresponding diode current <sup>2,6</sup> (approx., depends on $V_{DIODE}$ ):	+100 mA to +1A
Pulse width (FWHM):	200 ns to 100 us
Maximum duty cycle:	0.5% (2.5 us max. at 2 kHz, 100 us max. at 50 Hz)
Rise time: (10%-90%)	No filter < 5 ns. Standard filter <sup>5</sup> : 10 ns Optional filter <sup>5</sup> : 8 ns Optional filter <sup>5</sup> : 12 ns
Output impedance during pulse (inside the mainframe):	50 Ohms
Maximum PRF:	2 kHz
Delay:	0 to $\pm 1.0$ s variable
Coaxial cable to test jig (supplied):	See "Basic Test Arrangement" for details
Supplied test jig <sup>4</sup> :	AVX-TFR-MIX. Includes pin sockets for TO-220AC (2 lead) packages, DO-style packages <sup>3</sup> with leads bent at 90°, and standard and reverse-polarity TO-3 packages. Lead bending is required for axial packages.
Alternate test jig for axial devices:	Add the suffix -ANB to the model number to replace the standard test jig with one that will accept DO-41 packages (0.205" x 0.107" body, max) and Microsemi Axial Type E packages (0.185" x 0.135" body, max). Unlike the standard jig, bending is NOT required to insert these packages. This jig will not accept any other type of package. This is also available separately as part number AVX-TFR-ANB.
Other test tigs:	Test jigs are available for MELF packages (AVX-TFR-MELF), DO-4 and DO-5 stud packages (AVX-TFR-MELF), and other packages. Contact Avtech for pricing and model numbers. These can be used in place of the standard supplied test jig, and can be ordered at a later time.
Mainframe connectors:	BNC
GPIB and RS-232 control:	Standard on -B units. See <a href="http://www.avtechpulse.com/gpib">http://www.avtechpulse.com/gpib</a> for details.
Trigger required:	Ext trig mode: + 5 Volts, 10 ns or wider (TTL)
Gate input	Active high or low, switchable. Suppresses triggering when active.
Power requirements:	100 - 240 Volts, 50 - 60 Hz
Dimensions:	H x W x D: 100 mm x 430 mm x 375 mm (3.9" x 17" x 14.8")
Chassis material:	cast aluminum frame and handles, blue vinyl on aluminum cover plates
Temperature range:	+5°C to +40°C

- 1) -B suffix indicates IEEE-488.2 GPIB and RS-232 control of amplitude, pulse width, PRF and delay (see <http://www.avtechpulse.com/gpib>).
- 2) For operation at amplitudes of less than 10% of full-scale, best results will be obtained by setting the amplitude near full-scale and using external attenuators on the output.
- 3) DO-15, DO-35, DO-41, DO-201AD, etc.
- 4) Customized jigs available upon request.
- 5) The 10 ns (10%-90%) rise time filter is included as a standard feature. To add an 8 ns filter, add the -f8ns option to the model number. To add a 12 ns filter, add the -f12ns option to the model number. The rise time filter accuracy is  $\pm 1.2$  ns.
- 6) The amplitude settings should not be relied upon for any degree of accuracy, because the dynamics of the device under test can affect the actual generated waveforms. Amplitude settings should always be verified by oscilloscope measurements.



## EUROPEAN REGULATORY NOTES

### EC DECLARATION OF CONFORMITY

We Avtech Electrosystems Ltd.  
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Canada K2C 3H4

declare that this pulse generator meets the intent of Directive 89/336/EEC for Electromagnetic Compatibility. Compliance pertains to the following specifications as listed in the official Journal of the European Communities:

EN 50081-1 Emission

EN 50082-1 Immunity

and that this pulse generator meets the intent of the Low Voltage Directive 72/23/EEC as amended by 93/68/EEC. Compliance pertains to the following specifications as listed in the official Journal of the European Communities:

EN 61010-1:2001 Safety requirements for electrical equipment for measurement, control, and laboratory use



### DIRECTIVE 2002/95/EC (RoHS)

This instrument is exempt from Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the Restriction of the use of certain Hazardous Substances (RoHS) in electrical and electronic equipment. Specifically, Avtech instruments are considered "Monitoring and control instruments" (Category 9) as defined in Annex 1A of Directive 2002/96/EC. The Directive 2002/95/EC only applies to Directive 2002/96/EC categories 1-7 and 10, as stated in the "Article 2 - Scope" section of Directive 2002/95/EC.

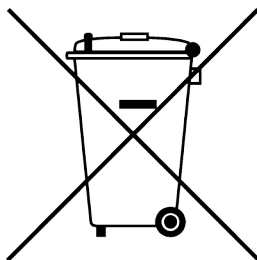
### DIRECTIVE 2002/96/EC (WEEE)

European customers who have purchased this equipment directly from Avtech will have completed a "WEEE Responsibility Agreement" form, accepting responsibility for WEEE

compliance (as mandated in Directive 2002/96/EC of the European Union and local laws) on behalf of the customer, as provided for under Article 9 of Directive 2002/96/EC.

Customers who have purchased Avtech equipment through local representatives should consult with the representative to determine who has responsibility for WEEE compliance. Normally, such responsibilities will lie with the representative, unless other arrangements (under Article 9) have been made.

Requirements for WEEE compliance may include registration of products with local governments, reporting of recycling activities to local governments, and financing of recycling activities.



## INSTALLATION

### VISUAL CHECK

After unpacking the instrument, examine to ensure that it has not been damaged in shipment. Visually inspect all connectors, knobs, liquid crystal displays (LCDs), and the handles. If the instrument has been damaged, file a claim immediately with the company that transported the instrument.

The following items should be with the instrument:

- 1) A power cord.
- 2) A GPIB cable.
- 3) Two instrumentation manuals (this manual and the “Programming Manual for -B Instruments”).
- 4) One test jig, with a hinged lid.
- 5) A 2 meter DB-9 control cable.
- 6) Two 60 cm lengths of SMA-to-BNC cable.
- 7) One 30 cm length of SMA-to-SMA cable.
- 8) One SMA female-female adapter.
- 9) One AVX-FILT-10NS accessory filter.
- 10) Units with -F8NS option only: One AVX-FILT-8NS accessory filter.
- 11) Units with -F12NS option only: One AVX-FILT-12NS accessory filter.

### POWER RATINGS


This instrument is intended to operate from 100 - 240 V, 50 - 60 Hz.

The maximum power consumption is 57 Watts. Please see the “FUSES” section for information about the appropriate AC and DC fuses.

This instrument is an “Installation Category II” instrument, intended for operation from a normal single-phase supply.

### CONNECTION TO THE POWER SUPPLY


An IEC-320 three-pronged recessed male socket is provided on the back panel for AC power connection to the instrument. One end of the detachable power cord that is supplied with the instrument plugs into this socket. The other end of the detachable power cord plugs into the local mains supply. Use only the cable supplied with the instrument. The mains supply must be earthed, and the cord used to connect the instrument to the mains supply must provide an earth connection. (The supplied cord does this.)

 Warning: Failure to use a grounded outlet may result in injury or death due to electric shock. This product uses a power cord with a ground connection. It must be connected to a properly grounded outlet. The instrument chassis is connected to the ground wire in the power cord.

The table below describes the power cord that is normally supplied with this instrument, depending on the destination region:

Destination Region	Description	Manufacturer	Part Number
Continental Europe	European CEE 7/7 "Schuko" 230V, 50Hz	Qualtek ( <a href="http://www.qualtekusa.com">http://www.qualtekusa.com</a> )	319004-T01
United Kingdom	BS 1363, 230V, 50Hz	Qualtek ( <a href="http://www.qualtekusa.com">http://www.qualtekusa.com</a> )	370001-E01
Switzerland	SEV 1011, 2 30V, 50Hz	Volex ( <a href="http://www.volex.com">http://www.volex.com</a> )	2102H-C3-10
Israel	SI 32, 220V, 50Hz	Volex ( <a href="http://www.volex.com">http://www.volex.com</a> )	2115H-C3-10
North America, and all other areas	NEMA 5-15, 120V, 60 Hz	Qualtek ( <a href="http://www.qualtekusa.com">http://www.qualtekusa.com</a> )	312007-01

## PROTECTION FROM ELECTRIC SHOCK

 Operators of this instrument must be protected from electric shock at all times. The owner must ensure that operators are prevented access and/or are insulated from every connection point. In some cases, connections must be exposed to potential human contact. Operators must be trained to protect themselves from the risk of electric shock. This instrument is intended for use by qualified personnel who recognize shock hazards and are familiar with safety precautions required to avoid possibly injury. In particular, operators should:

1. Keep exposed high-voltage wiring to an absolute minimum.
2. Wherever possible, use shielded connectors and cabling.
3. Connect and disconnect loads and cables only when the instrument is turned off.
4. Keep in mind that all cables, connectors, oscilloscope probes, and loads must have an appropriate voltage rating.
5. Do not attempt any repairs on the instrument, beyond the fuse replacement procedures described in this manual. Contact Avtech technical support (see page 2 for contact information) if the instrument requires servicing. Service is to be performed solely by qualified service personnel.

## ENVIRONMENTAL CONDITIONS

This instrument is intended for use under the following conditions:

1. indoor use;
2. altitude up to 2 000 m;
3. temperature 5 °C to 40 °C;
4. maximum relative humidity 80 % for temperatures up to 31 °C decreasing linearly to 50 % relative humidity at 40 °C;
5. Mains supply voltage fluctuations up to  $\pm 10$  % of the nominal voltage;
6. no pollution or only dry, non-conductive pollution.

## FUSES

This instrument contains four fuses. All are accessible from the rear-panel. Two protect the AC prime power input, and two protect the internal DC power supplies. The locations of the fuses on the rear panel are shown in the figure below:



### AC FUSE REPLACEMENT

To physically access the AC fuses, the power cord must be detached from the rear panel of the instrument. The fuse drawer may then be extracted using a small flat-head screwdriver, as shown below:



## DC FUSE REPLACEMENT

The DC fuses may be replaced by inserting the tip of a flat-head screwdriver into the fuse holder slot, and rotating the slot counter-clockwise. The fuse and its carrier will then pop out.

## FUSE RATINGS

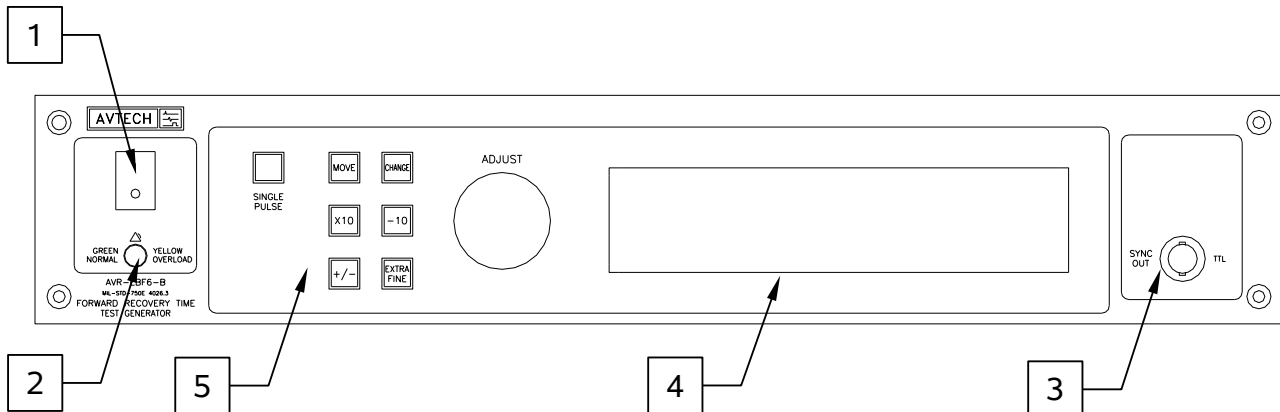
The following table lists the required fuses:

Fuses	Nominal Mains Voltage	Rating	Case Size	Manufacturer's Part Number (Wickmann)	Distributor's Part Number (Digi-Key)
#1, #2 (AC)	100-240V	0.5A, 250V, Time-Delay	5×20 mm	1950500000	WK5041-ND
#3 (DC)	N/A	1.6A, 250V, Time-Delay	5×20 mm	1951160000	WK5053-ND
#4 (DC)	N/A	0.5A, 250V, Time-Delay	5×20 mm	1950500000	WK5041-ND

The fuse manufacturer is Wickmann (<http://www.wickmann.com/>).

Replacement fuses may be easily obtained from Digi-Key (<http://www.digikey.com/>) and other distributors.

## FRONT PANEL CONTROLS



1. POWER Switch. This is the main power switch. When turning the instrument on, there may be a delay of several seconds before the instrument appears to respond.
2. OVERLOAD Indicator. When the instrument is powered, this indicator is normally green, indicating normal operation. If this indicator is yellow, an internal automatic overload protection circuit has been tripped. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a very low impedance), the protective circuit will disable the output of the instrument and turn the indicator light yellow. The light will stay yellow (i.e. output disabled) for about 5 seconds after which the instrument will attempt to re-enable the output (i.e. light green) for about 1 second. If the overload condition persists, the output will be disabled again (i.e. light yellow) for another 5 seconds. If the overload condition has been removed, the instrument will resume normal operation.

This overload indicator may flash yellow briefly at start-up. This is not a cause for concern.

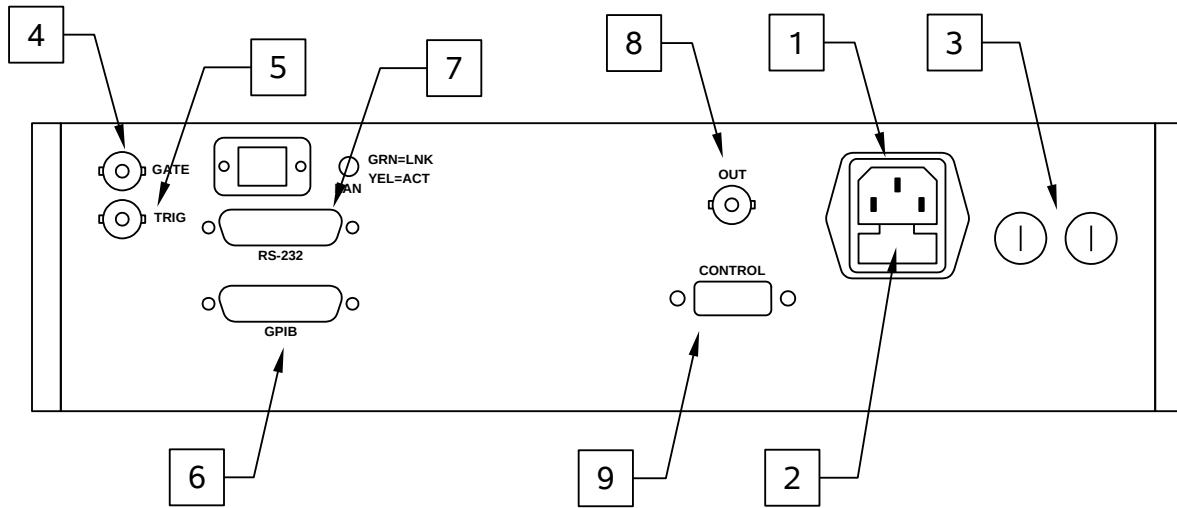
3. SYNC OUT. This connector supplies a SYNC output that can be used to trigger other equipment, particularly oscilloscopes. This signal leads (or lags) the main output by a duration set by the "DELAY" controls and has an approximate amplitude of +3 Volts to  $R_L > 50\Omega$  with a pulse width of approximately 100 ns.
4. LIQUID CRYSTAL DISPLAY (LCD). This LCD is used in conjunction with the keypad to change the instrument settings. Normally, the main menu is displayed, which lists the key adjustable parameters and their current values. The "Programming Manual for -B Instruments" describes the menus and submenus in detail.



5. KEYPAD.


Control Name	Function
MOVE	This moves the arrow pointer on the display.
CHANGE	This is used to enter the submenu, or to select the operating mode, pointed to by the arrow pointer.
×10	If one of the adjustable numeric parameters is displayed, this increases the setting by a factor of ten.
÷10	If one of the adjustable numeric parameters is displayed, this decreases the setting by a factor of ten.
+/-	If one of the adjustable numeric parameters is displayed, and this parameter can be both positive or negative, this changes the sign of the parameter.
EXTRA FINE	This changes the step size of the ADJUST knob. In the extra-fine mode, the step size is twenty times finer than in the normal mode. This button switches between the two step sizes.
ADJUST	This large knob adjusts the value of any displayed numeric adjustable values, such as frequency, pulse width, etc. The adjust step size is set by the "EXTRA FINE" button.  When the main menu is displayed, this knob can be used to move the arrow pointer.

## REAR PANEL CONTROLS



1. AC POWER INPUT. An IEC-320 C14 three-pronged recessed male socket is provided on the back panel for AC power connection to the instrument. One end of the detachable power cord that is supplied with the instrument plugs into this socket.
2. AC FUSE DRAWER. The two fuses that protect the AC input are located in this drawer. Please see the “FUSES” section of this manual for more information.
3. DC FUSES. These two fuses protect the internal DC power supplies. Please see the “FUSES” sections of this manual for more information.
4. GATE. This TTL-level (0 and +5V) logic input can be used to gate the triggering of the instrument. This input can be either active high or active low, depending on the front panel settings or programming commands. (The instrument triggers normally when this input is unconnected). When set to active high mode, this input is pulled-down to ground by a 1 k $\Omega$  resistor. When set to active low mode, this input is pulled-up to +5V by a 1 k $\Omega$  resistor.
5. TRIG. This TTL-level (0 and +5V) logic input can be used to trigger the instrument, if the instrument is set to triggering externally. The instrument triggers on the rising edge of this input. The input impedance of this input is 1 k $\Omega$ . (Depending on the length of cable attached to this input, and the source driving it, it may be desirable to add a coaxial 50 Ohm terminator to this input to provide a proper transmission line termination. The Pasternack ([www.pasternack.com](http://www.pasternack.com)) PE6008-50 BNC feed-thru 50 Ohm terminator is suggested for this purpose.)
6. GPIB Connector. A standard GPIB cable can be attached to this connector to allow the instrument to be computer-controlled. See the “Programming Manual for -B Instruments” for more details on GPIB control.

7. RS-232 Connector. A standard serial cable with a 25-pin male connector can be attached to this connector to allow the instrument to be computer-controlled. See the “Programming Manual for -B Instruments” for more details on RS-232 control.
8. PULSE OUT CONNECTOR. This BNC connector provides the pulse output signal to the rise time filter. This output should be connected to the corresponding input on the rise time filter using the supplied BNC-to-SMA coaxial cable and SMA female-female adapter.

 Caution: Voltages as high as 100V may be present on the center conductor of this output connector. Avoid touching this conductor. Connect to this connector using standard coaxial cable, to ensure that the center conductor is not exposed.

9. CONTROL Connector. This DB-9 female connector should be connected to the corresponding connector on the test jig using the supplied DB-9 cable. This cable contains the safety interlock signals that ensure that the test jig lid is closed. The pinout is as follows:

Pin 1 - To test jig switch 1.

Pin 2 - To test jig switch 2.

Pin 5 - Ground.

Pin 6 - To test jig switch 1.

Pin 7 - To test jig switch 2.

Pin 9 - Safety sensor power supply (+15V through 680 Ohms).

When the test jig lid is safely closed, Pin 1 is shorted to Pin 6, and Pin 2 is shorted to Pin 7.

## TIMING CONTROL

### BASIC TIMING CONTROL

This instrument can be triggered by its own internal clock or by an external TTL trigger signal. In either case, two output channels respond to the trigger: OUT and SYNC. The OUT channel is the signal that is applied to the load. Its amplitude and pulse width are variable. The SYNC pulse is a fixed-width TTL-level reference pulse used to trigger oscilloscopes or other measurement systems. When the delay is set to a positive value the SYNC pulse precedes the OUT pulse. When the delay is set to a negative value the SYNC pulse follows the OUT pulse.

These pulses are illustrated below, assuming internal triggering and a positive delay:



*Figure A*

If the delay is negative, the order of the SYNC and OUT pulses is reversed:



*Figure B*

The next figure illustrates the relationship between the signal when an external TTL-level trigger is used:

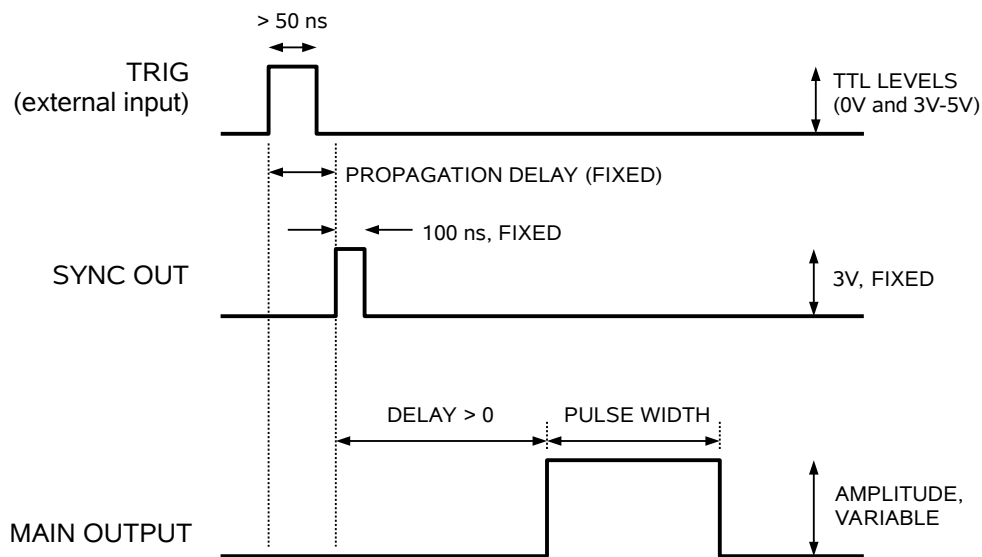


Figure C

As before, if the delay is negative, the order of the SYNC and OUT pulses is reversed.

## TRIGGER MODES

This instrument has four trigger modes:

- Internal Trigger: the instrument controls the trigger frequency, and generates the clock internally.
- External Trigger: the instrument is triggered by an external TTL-level clock on the back-panel TRIG connector.
- Manual Trigger: the instrument is triggered by the front-panel "SINGLE PULSE" pushbutton.
- Hold Trigger: the instrument is set to not trigger at all.

These modes can be selected using the front panel trigger menu, or by using the appropriate programming commands. (See the "Programming Manual for -B Instruments" for more details.)

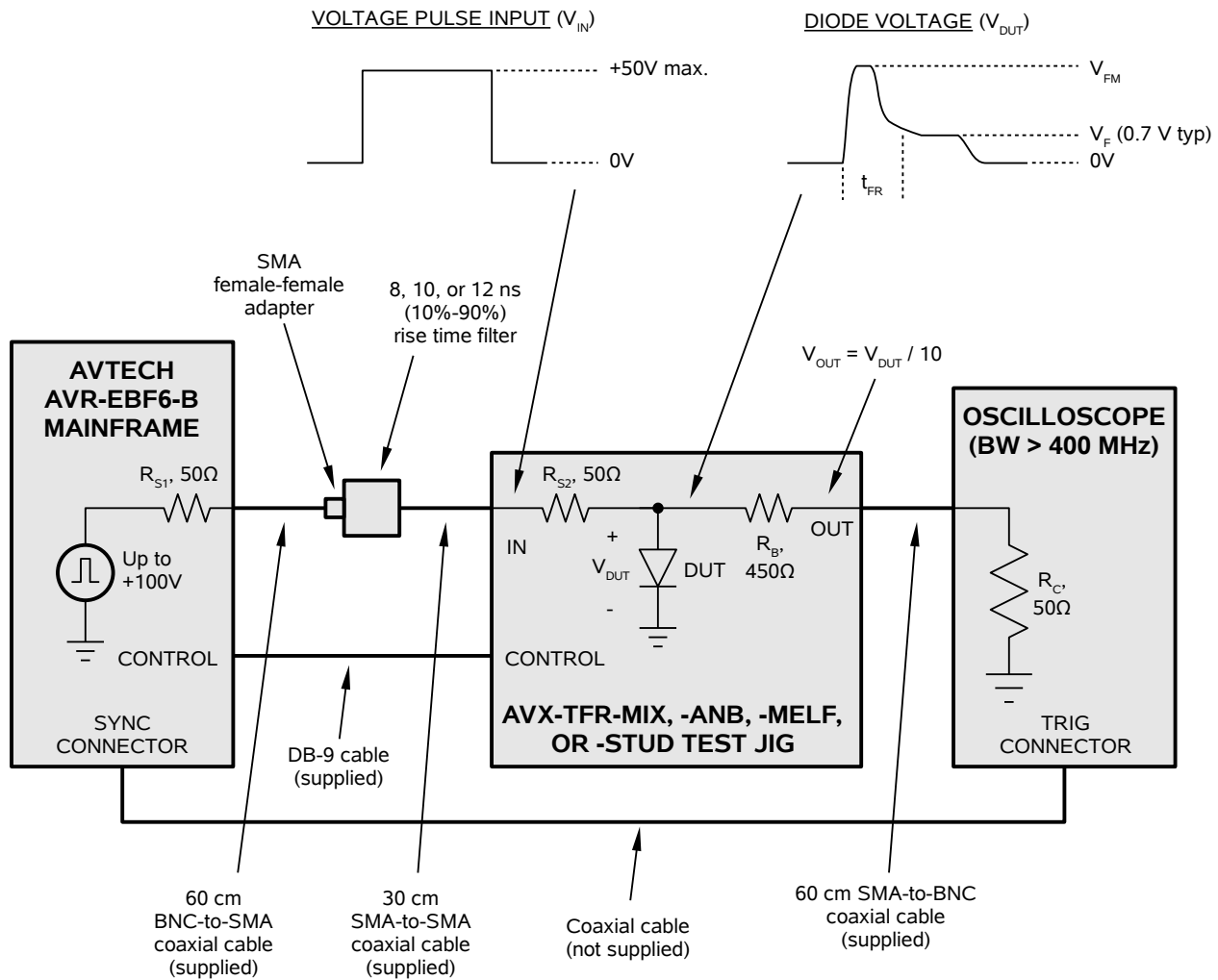
## GATING MODES

Triggering can be suppressed by a TTL-level signal on the rear-panel GATE connector. The instrument can be set to stop triggering when this input high or low, using the front-panel gate menu or the appropriate programming commands. When gated, the output

will complete the full pulse width if the output is high, and then stop triggering. Pulses are not truncated.

## BASIC AMPLITUDE CONTROL

The basic test arrangement for the AVR-EBF6-B is shown in the figure below:



The mainframe connects to the rise time filter using a 60 cm length of BNC-to-SMA coaxial cable. A female-female adapter is required at the input to the filter.

The output of the filter connects to the test jig using a 30 cm length of SMA-to-SMA coaxial cable.

A DB-9 control cable connects the mainframe to the test jig, to control the safety interlocks.

The test jig output is fed into the 50 Ohm input of an oscilloscope by a 60 cm length of BNC-to-SMA coaxial cable.

The SYNC output of the mainframe should be used to trigger the oscilloscope.

⚠ A 50 Ohm resistance ( $R_C$  in the diagram above) must be connected to ground on the output. This can be a discrete resistor, a feed-through terminator, or the input impedance of an oscilloscope (the latter method is shown in the diagram). If a high-speed sampling oscilloscope is used, the input should be protected by adding attenuator on the input.

The 450 Ohm resistance in series with the test jig output and the 50 Ohm input impedance of the oscilloscope form a 10:1 voltage, so the measured voltage is:

$$V_{OUT} = V_{DUT} / 10$$

### SETTING THE AMPLITUDE LEVELS

The amplitude pulse may be set from the front panel of the instrument, or by computer command. This amplitude is expressed in terms of the voltage present on the test jig input.

The amplitude is related to the forward diode current by:

$$I_{FORWARD} \approx (AMP - V_F) / (50\Omega + R_{DIODE-FORWARD})$$

where  $V_F$  is the forward voltage drop of the diode (typically 0.7V for the classic silicon PN junction diode, and usually somewhat lower for a Schottky diode), and  $R_{DIODE-FORWARD}$  is the effective resistance of the diode under forward bias (typically  $\approx 0$ ).

For instance, if the desired forward current amplitude is 500 mA, the amplitude should be set at 25.7V, so that  $(25.7V - 0.7V) / 50\Omega = 500$  mA.

### AMPLITUDE ACCURACY

Due to the variations in  $V_F$  and  $R_{DIODE-FORWARD}$  and as a function of operating conditions, the amplitude settings *should not be relied upon for any degree of accuracy*. Instead the voltage at the OUT terminal on the test jig should be monitored with a calibrated oscilloscope.

$R_B$  can be measured directly on the test jig (with the test jig disconnected) to determine calibrated relationships, if desired.  $R_C$  is provided by the user, and can be calibrated as required.



### INCORRECT ORIENTATION


The instrument and the DUT will not be damaged if the diode is installed with the incorrect orientation (i.e., with the anode and cathode reversed). However, incorrect waveforms will be generated,


### CABLE LENGTHS

The cable lengths are not critical. They may be increased or decreased as desired.

### ACCESSIBLE VOLTAGES

The mainframe provides pulsed voltages of up to 100V to the test jig. For this reason, the output is automatically disabled when the test jig lid is open. The lid must be closed to obtain measurements.

 Shielded cabling should be used for all connections to the "IN" and "OUT" terminals on the test jig, and the "OUT" connector on the mainframe.

 When used properly (with  $R_C = 50$  Ohms), the maximum voltage on the OUT terminal will be  $< 1V$ , approximately. However, if  $R_C$  is not connected and the DUT is not installed, the maximum voltage will at the OUT terminal may be as high as 100V. You may need to consider whether this scenario would damage your oscilloscope, and take appropriate precautions.

## STANDARD TEST JIG MECHANICAL ASPECTS

### ALL TEST JIGS

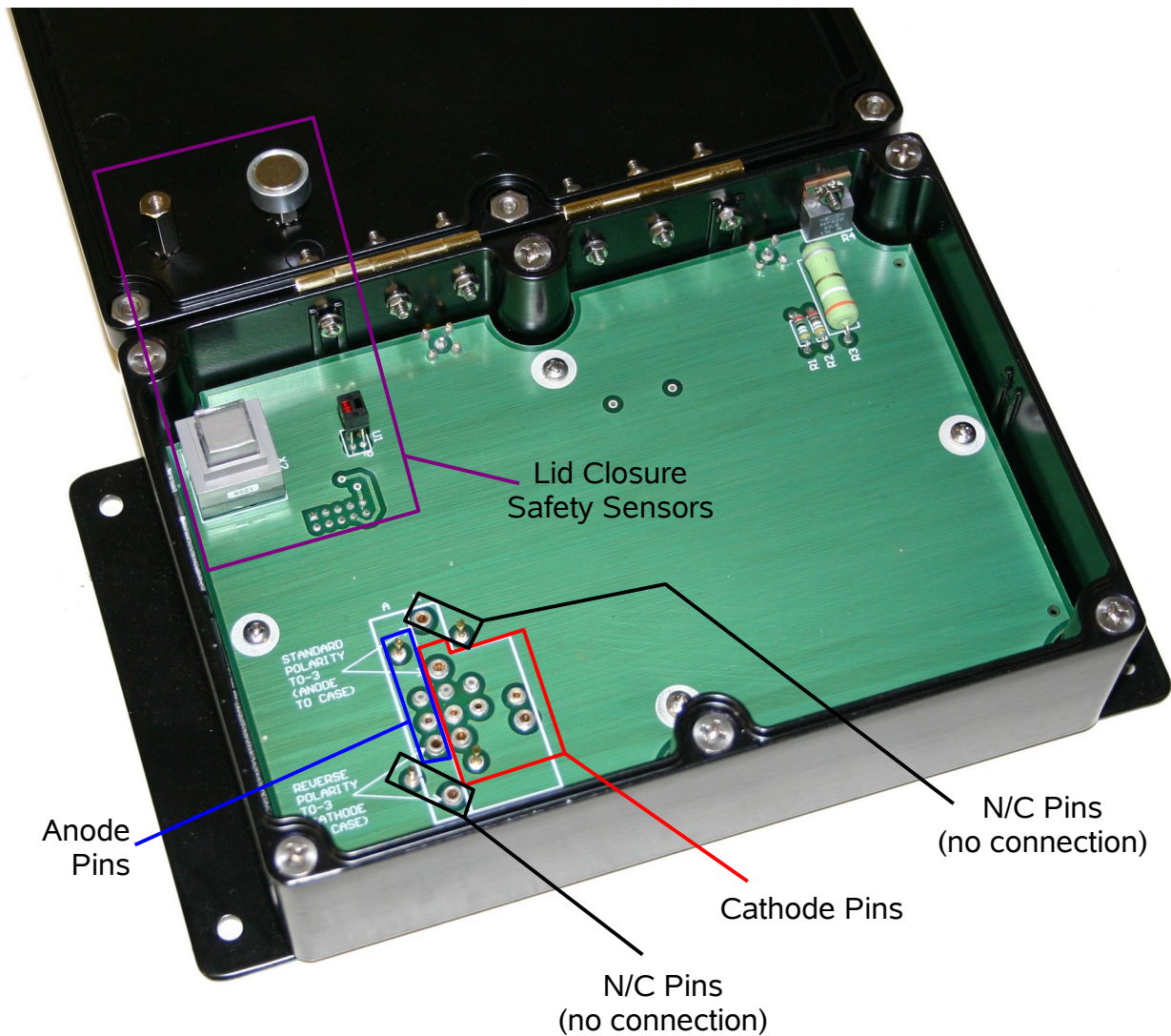
The IN, OUT, and CONTROL connectors are on the rear of the jig, below the hinges:



### AVX-TFR-MIX TEST JIG

One AVX-TFR-MIX test jig is normally supplied with the mainframe, unless the customer has requested a different or additional test jigs.

The AVX-TFR-MIX test jig accepts a range of through-hole and axial devices, using pin sockets and spring-loaded pins. It is intended for use with diodes in DO-41, TO-220, DO-204AR, TO-3 or similar packages. A photo of the arrangement is shown below:



The instrument and the DUT will not be damaged if the diode is installed with the incorrect orientation (i.e., with the anode and cathode reversed). However, incorrect waveforms will be generated.

The procedure for inserting most axial and TO-220 packages is straightforward. Simply insert the DUT between one of the Anode pin sockets (in the blue area above) and one of the Cathode sockets (in the red area above). Select the sockets with the most appropriate hole size, and try to minimize all lead lengths, to minimize parasitic inductance.

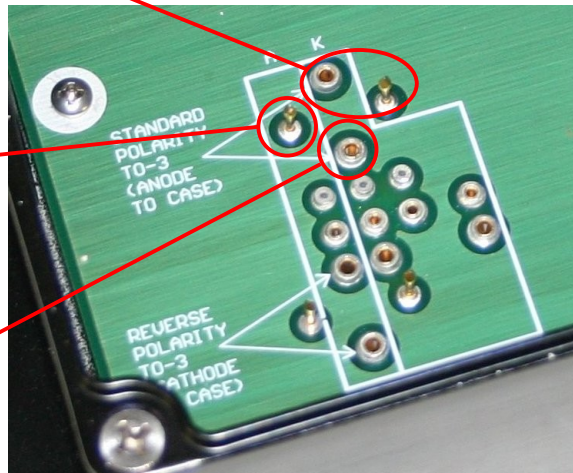
This jig will also accommodate a number of TO-3 configurations, outlined below. If the case is connected to the anode, and the pin(s) are used for the cathode, the arrangement shown below must be used:

This socket and spring pin provide mechanical support only. They are not electrically active.

**For TO-3 packages  
with Case = Anode**

This spring pin must contact the underside of the case. It provides the anode connection.

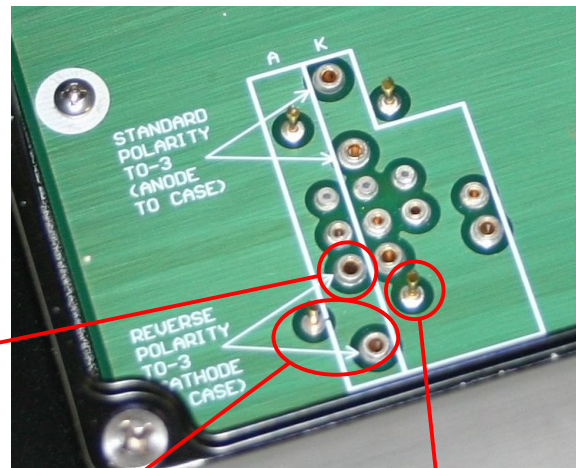
The cathode pin of interest must be inserted into this socket. For dual-diode devices, rotate the TO-3 package so that the desired diode cathode is inserted here.



If the case is connected to the cathode, and the pin(s) are used for the anode, the arrangement shown below must be used:

**For TO-3 packages  
with Case = Cathode**

The anode pin of interest must be inserted into this socket. For dual-diode devices, rotate the TO-3 package so that the desired diode anode is inserted here.



This socket and spring pin provide mechanical support only. They are not electrically active.

This spring pin must contact the underside of the case. It provides the cathode connection.

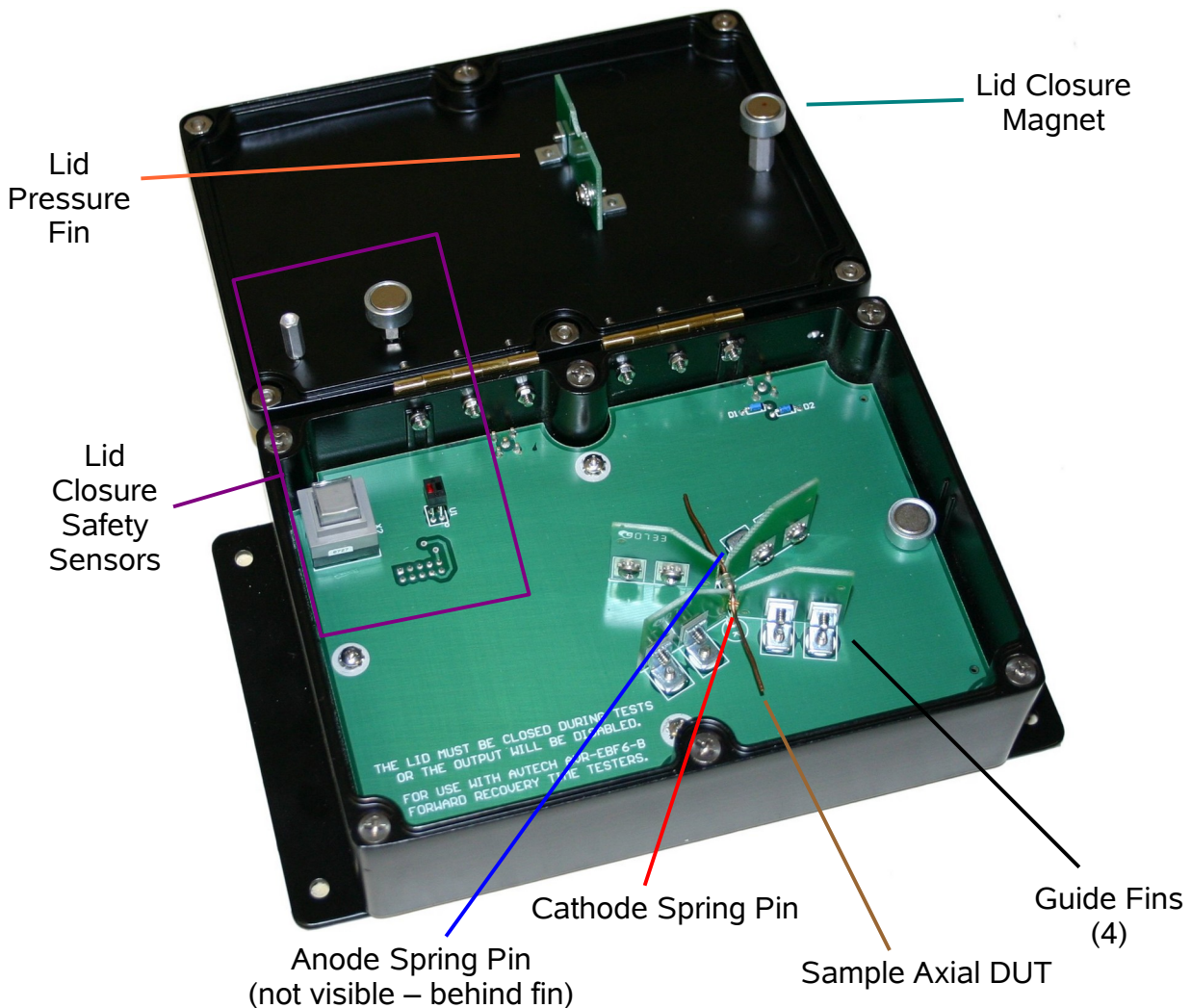
## AVX-TFR-ANB TEST JIG

If the AVR-EBF6-B has been ordered with the -ANB option, an AVX-TFR-ANB test jig replaces the standard AVX-TFR-MIX test jig. The AVX-TFR-ANB test jig may also be ordered separately. In contrast to the AVX-TFR-MIX, no lead bending is required.

The AVX-TFR-ANB test jig accepts two types of packages:

- DO-41 (0.205" x 0.107" body, maximum)
- Microsemi Axial Type E (0.185" x 0.135" body, maximum)

The DUT is installed in the test jig by dropping it over the area marked "DUT" on the test jig PCB. Four "fins" guide the DUT into the correct position between two spring-loaded test pins. When the hinged lid is closed, a fifth "fin" attached to the underside of the lid presses the DUT against the spring-loaded pins, to ensure good electrical contact. A DUT is shown in position in the photo below (with the lid open):

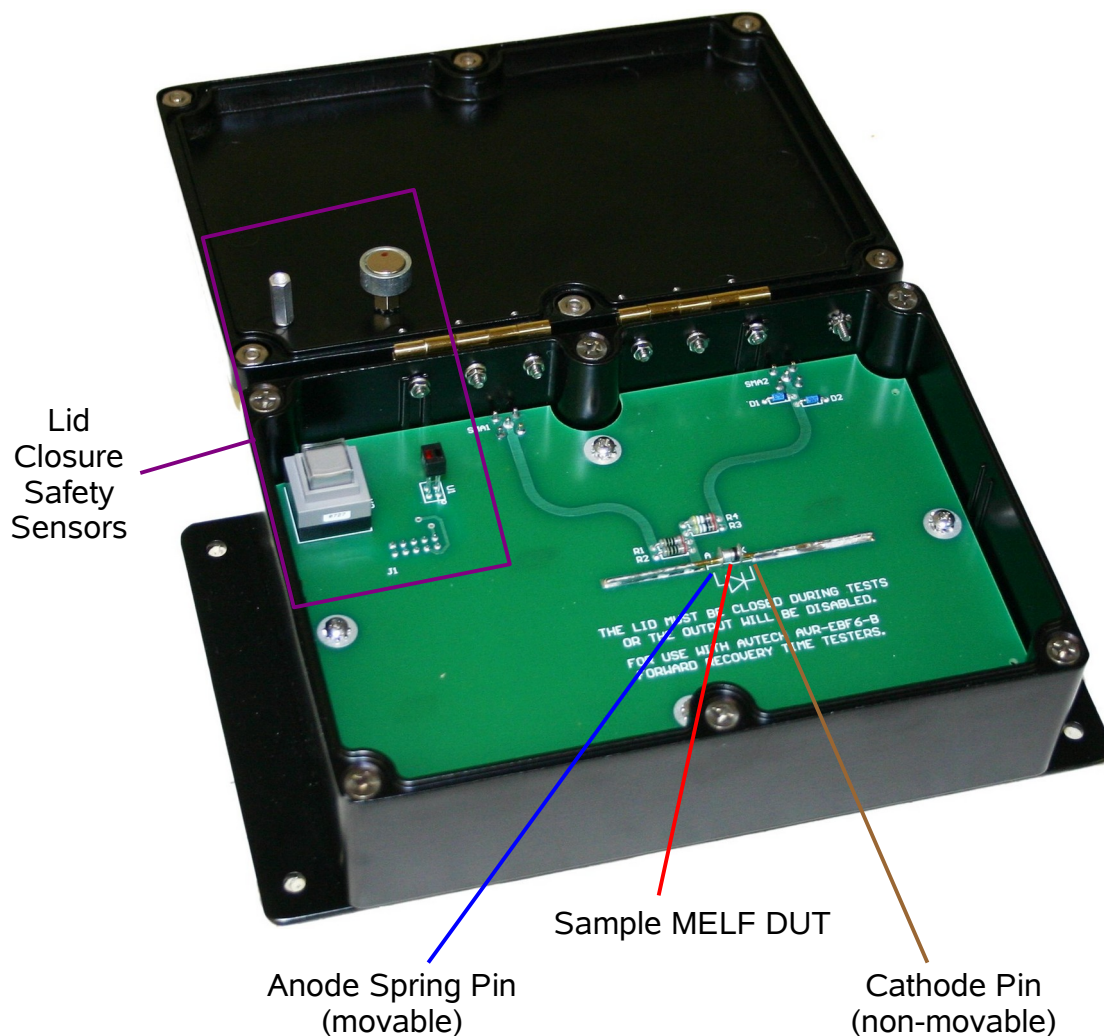


The instrument and the DUT will not be damaged if the diode is installed with the incorrect orientation (i.e., with the anode and cathode reversed). However, incorrect waveforms will be generated.

When closed, the lid is held in place by a magnetic latch. To open the lid, simply pull upwards on the handle at the front of the lid.

### AVX-TFR-MELF

The AVX-TFR-MELF jig, which may be ordered separately, accepts MELF packages as shown below:



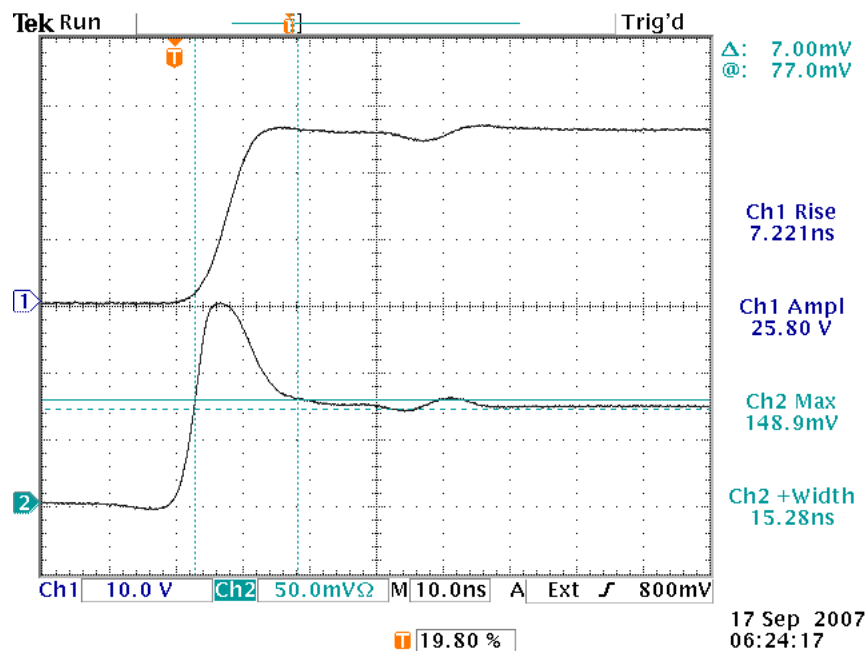
The anode spring pin must be pushed back to make room for the DUT. The cathode pin is not movable (the spring action has been disabled, to minimize series inductance).

## TYPICAL RESULTS

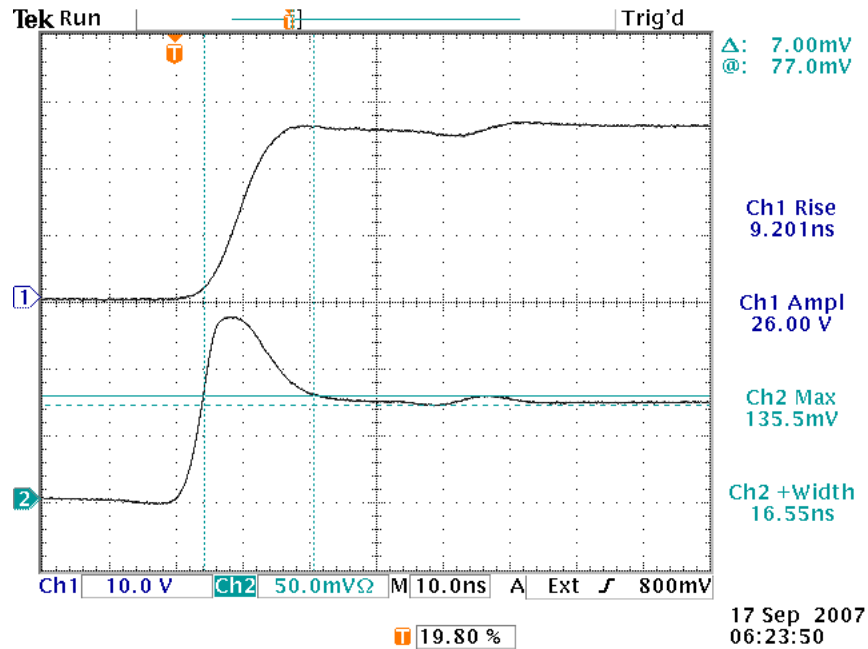
Obtaining meaningful results with the AVR-EBF6-B requires care, experience, and an understanding of diode transient behavior and the impact of inductive and capacitive parasitics. To assist the user, typical results are provided below. The user should be able to reliably duplicate these results.

### 1N5811 RESULTS

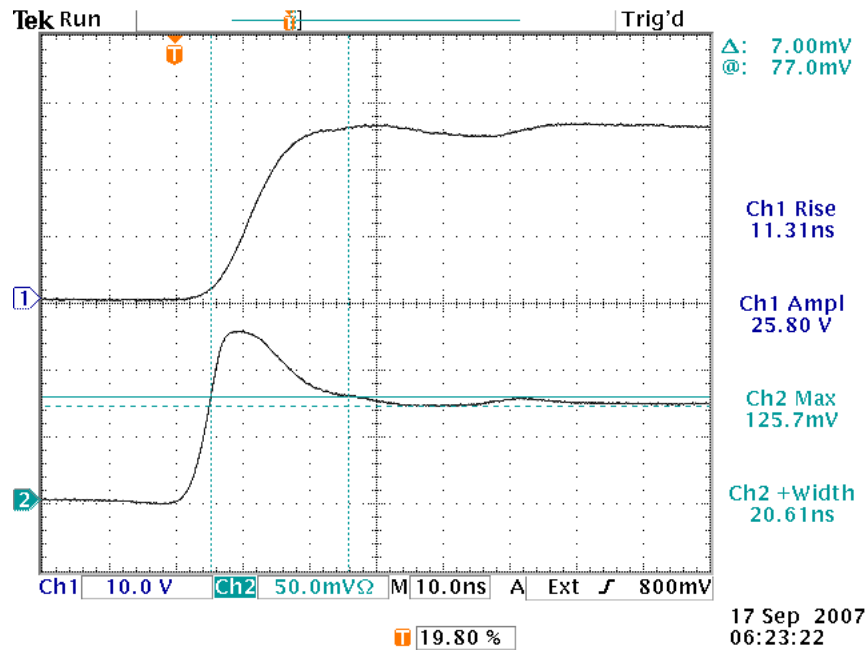
A customer-supplied 1N5811 engineering sample (sample #51) was tested in the AVX-TRF-ANB jig, using the 8 ns, 10 ns, and 12 ns rise time filters, with the amplitude set to +25.7V (such that the forward current  $I_F = 500$  mA). The following forward recovery waveforms were obtained at the test jig "OUT" terminal:



Top – Voltage input to the test jig (+25.7V, with ~ 8 ns rise time). 10 V/div, 10 ns/div.  
 Bottom – Test jig output ( $V_{DUT}/10$ ). 50 mV/div, 10 ns/div.



Top – Voltage input to the test jig (+25.7V, with ~ 10 ns rise time). 10 V/div, 10 ns/div.  
 Bottom – Test jig output ( $V_{DUT}/10$ ). 50 mV/div, 10 ns/div.



Top – Voltage input to the test jig (+25.7V, with ~ 12 ns rise time). 10 V/div, 10 ns/div.  
 Bottom – Test jig output ( $V_{DUT}/10$ ). 50 mV/div, 10 ns/div.

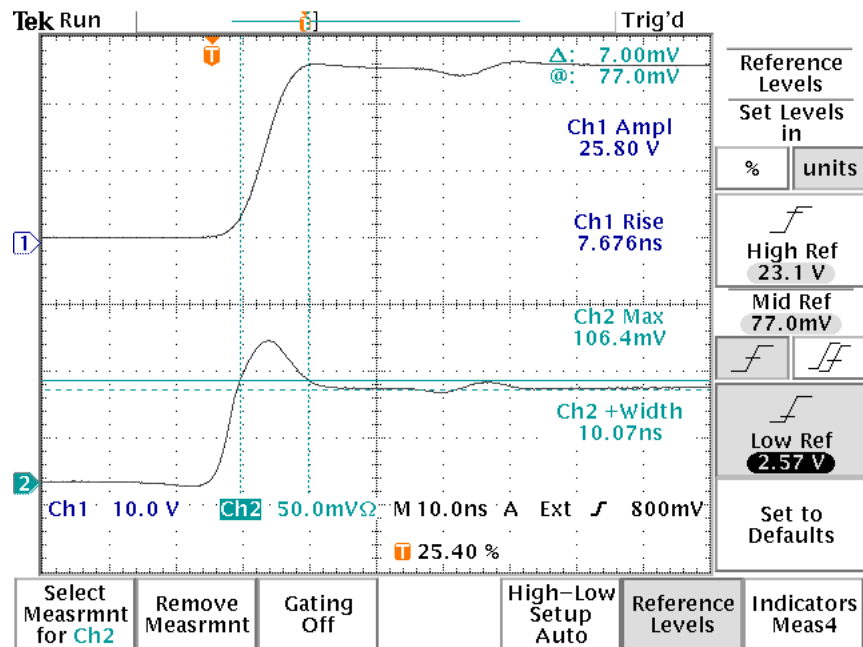
The results are summarized in the following table:



<b>Filter Used</b>	<b><math>V_{FM}</math></b>	<b><math>V_F</math></b>	<b><math>t_{FR}</math></b>
8 ns	1.489 V	0.70 V	15.28 ns
10 ns	1.355 V	0.70 V	16.55 ns
12 ns	1.257 V	0.70 V	20.61 ns

### MQ1N5811US RESULTS

The Microsemi MQ1N5811US was tested with the amplitude set to +25.7V (such that the forward current  $I_F = 500$  mA), using an 8 ns rise time filter and the AVX-TFR-MELF test jig. The following forward recovery waveform was obtained at the test jig "OUT" terminal:



Top – Voltage input to the test jig (+25.7V, with ~ 8 ns rise time). 10 V/div, 10 ns/div.  
Bottom – Test jig output ( $V_{DUT}/10$ ). 50 mV/div, 10 ns/div.

Under these conditions,  $V_{FM} = 1.064$  V,  $V_F = 0.70$  V, and  $t_{FR} = 10.07$  ns (measured at  $1.1 \times V_F$ ).

## TROUBLESHOOTING

If you obtain “strange” output waveforms, or unexpected values of  $t_{RR}$ , keep these points in mind:

- 1) The test jig output *must* be terminated with 50 Ohms.
- 2) Keep device lead lengths as short as possible, to minimize parasitic inductance.
- 3) The test jig lid must be closed, or the pulser output will be disabled.

For technical support, contact [info@avtechpulse.com](mailto:info@avtechpulse.com). Sample waveforms and digital photos of your setup are always helpful!

## PROGRAMMING YOUR PULSE GENERATOR

### KEY PROGRAMMING COMMANDS

The “Programming Manual for -B Instruments” describes in detail how to connect the pulse generator to your computer, and the programming commands themselves. A large number of commands are available; however, normally you will only need a few of these. Here is a basic sample sequence of commands that might be sent to the instrument after power-up:

*rst	(resets the instrument)
trigger:source internal	(selects internal triggering)
frequency 1000 Hz	(sets the frequency to 1000 Hz)
pulse:delay 1 us	(sets the delay to 1 us)
pulse:width 10 us	(sets the positive pulse width to 10 us)
volt:ampl +25.7	(sets the positive pulse amplitude to +25.7 V)
output on	(turns on the output)

For triggering a single event, this sequence would be more appropriate:

*rst	(resets the instrument)
trigger:source hold	(turns off all triggering)
output on	(turns on the output)
pulse:delay 1 us	(sets the delay to 1 us)
pulse:width 10 us	(sets the positive pulse width to 10 us)
volt:ampl +25.7	(sets the positive pulse amplitude to +25.7 V)
trigger:source immediate	(generates a single non-repetitive trigger event)
trigger:source hold	(turns off all triggering)
output off	(turns off the output)

To set the instrument to trigger from an external TTL signal applied to the rear-panel TRlg connector, use:

*rst	(resets the instrument)
trigger:source external	(selects external triggering)
pulse:delay 1 us	(sets the delay to 1 us)
pulse:width 10 us	(sets the positive pulse width to 10 us)
volt:ampl +25.7	(sets the positive pulse amplitude to +25.7 V)
output on	(turns on the output)

These commands will satisfy 90% of your programming needs.

## ALL PROGRAMMING COMMANDS

For more advanced programmers, a complete list of the available commands is given below. These commands are described in detail in the “Programming Manual for -B Instruments”. (Note: this manual also includes some commands that are not implemented in this instrument. They can be ignored.)


<u>Keyword</u>	<u>Parameter</u>	<u>Notes</u>
LOCAL		
OUTPut:		
:[STATe]	<boolean value>	
:PROTection		
:TRIPped?		[query only]
REMOTE		
[SOURce]:		
:FREQUency		
[:CW   FIXed]	<numeric value>	
[SOURce]:		
:PULSe		
:PERiod	<numeric value>	
:WIDTh	<numeric value>	
:DELay	<numeric value>	
:GATE		
:LEVel	High   Low	
[SOURce]:		
:VOLTage		
[:LEVel]		
[:IMMediate]		
[:AMPLitude]	<numeric value>	
:PROTection		
:TRIPped?		[query only]
STATUS:		
:OPERation		
:[EVENT]?		[query only, always returns "0"]
:CONDition?		[query only, always returns "0"]
:ENABle	<numeric value>	[implemented but not useful]
:QUESTionable		
:[EVENT]?		[query only, always returns "0"]
:CONDition?		[query only, always returns "0"]
:ENABle	<numeric value>	[implemented but not useful]
SYSTEM:		
:COMMunicate		
:GPIB		
:ADDRes	<numeric value>	
:SERial		
:CONTRol		
:RTS	ON   IBFull   RFR	
:[RECeive]		
:BAUD	1200   2400   4800   9600	
:BITS	7   8	
:ECHO	<boolean value>	
:PARity		
:[TYPE]	EVEN   ODD   NONE	
:SBITS	1   2	
:ERRor		

: [NEXT]?		[query only]
: COUNT?		[query only]
: VERsion?		[query only]
TRIGger:		
: SOURce	INTernal   EXTernal   MANual   HOLD   IMMEDIATE	
*CLS		[no query form]
*ESE	<numeric value>	
*ESR?		[query only]
*IDN?		[query only]
*OPC		
*SAV	0   1   2   3	[no query form]
*RCL	0   1   2   3	[no query form]
*RST		[no query form]
*SRE	<numeric value>	
*STB?		[query only]
*TST?		[query only]
*WAI		[no query form]


## MECHANICAL INFORMATION

### TOP COVER REMOVAL

If necessary, the interior of the instrument may be accessed by removing the four Phillips screws on the top panel. With the four screws removed, the top cover may be slid back (and off).

 Always disconnect the power cord and allow the instrument to sit unpowered for 10 minutes before opening the instrument. This will allow any internal stored charge to discharge.

There are no user-adjustable internal circuits. For repairs other than fuse replacement, please contact Avtech (info@avtechpulse.com) to arrange for the instrument to be returned to the factory for repair. Service is to be performed solely by qualified service personnel.

 Caution: High voltages are present inside the instrument during normal operation. Do not operate the instrument with the cover removed.

### RACK MOUNTING

A rack mounting kit is available. The -R5 rack mount kit may be installed after first removing the one Phillips screw on the side panel adjacent to the front handle.

### ELECTROMAGNETIC INTERFERENCE

To prevent electromagnetic interference with other equipment, all used outputs should be connected to shielded loads using shielded coaxial cables. Unused outputs should be terminated with shielded coaxial terminators or with shielded coaxial dust caps, to prevent unintentional electromagnetic radiation. All cords and cables should be less than 3m in length.

## MAINTENANCE

### REGULAR MAINTENANCE

This instrument does not require any regular maintenance.

On occasion, one or more of the four rear-panel fuses may require replacement. All fuses can be accessed from the rear panel. See the “FUSES” section for details.

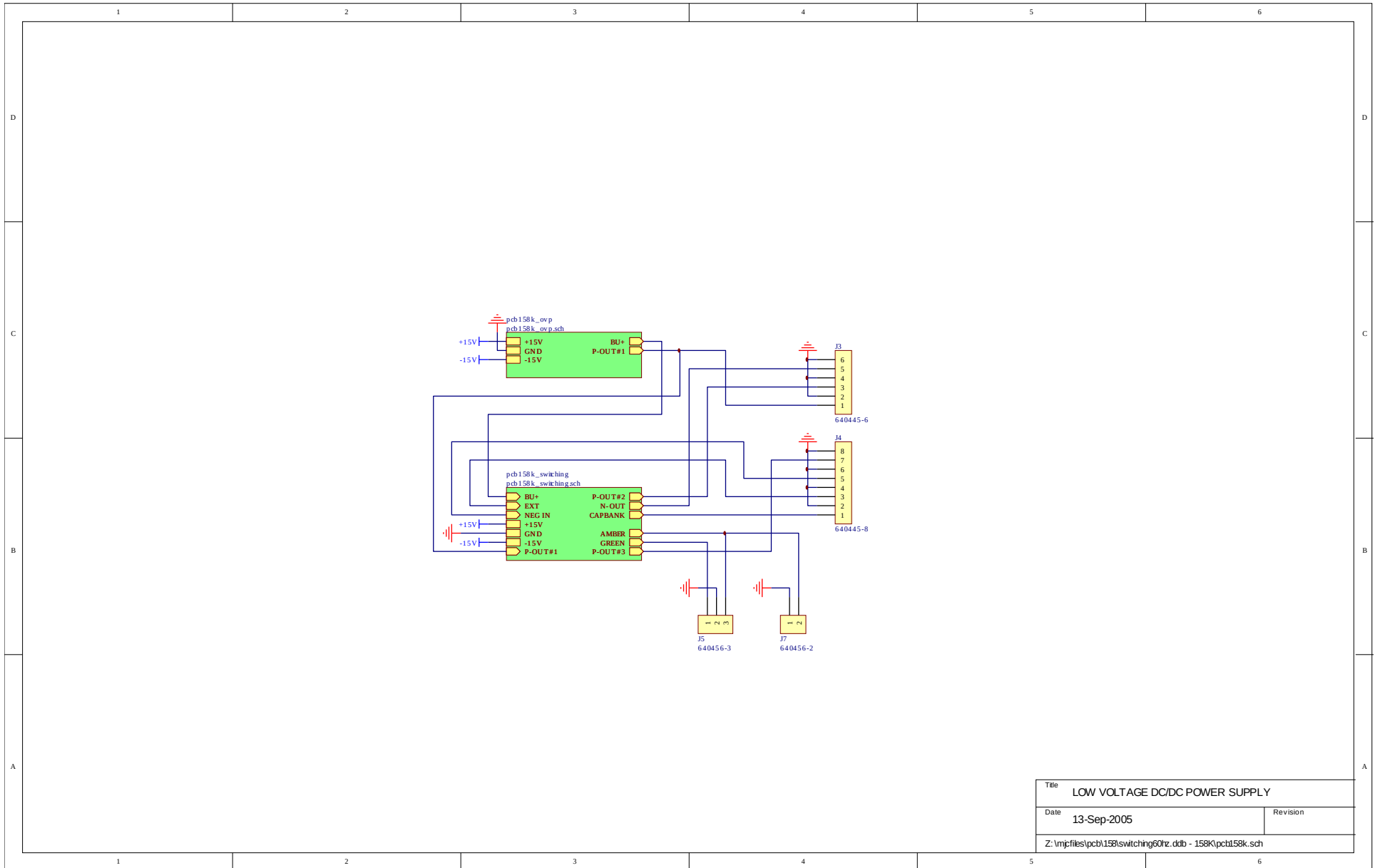
### CLEANING

If desired, the interior of the instrument may be cleaned using compressed air to dislodge any accumulated dust. (See the “TOP COVER REMOVAL” section for instructions on accessing the interior.) No other cleaning is recommended.



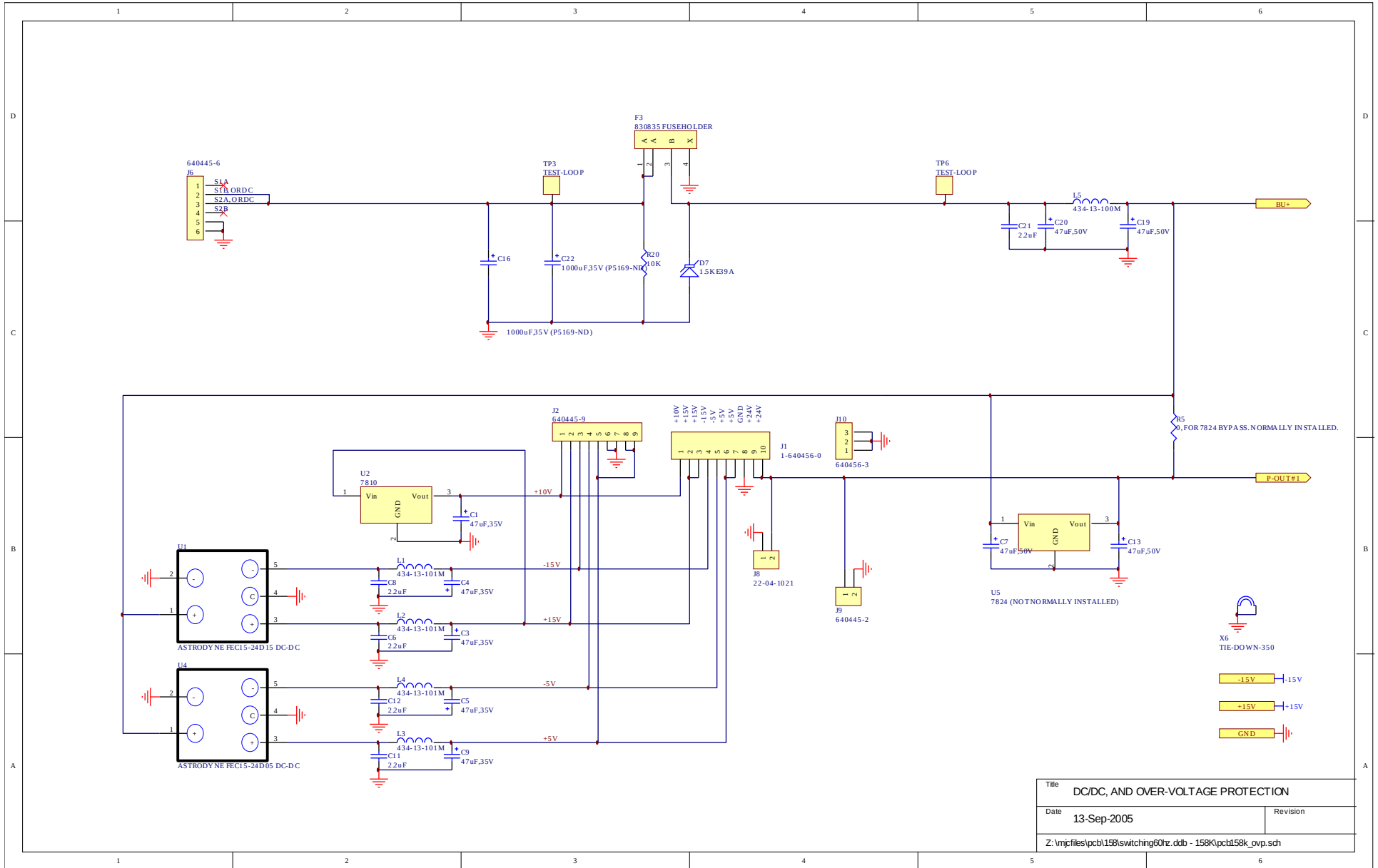


# PCB 158K - LOW VOLTAGE POWER SUPPLY, 1/3



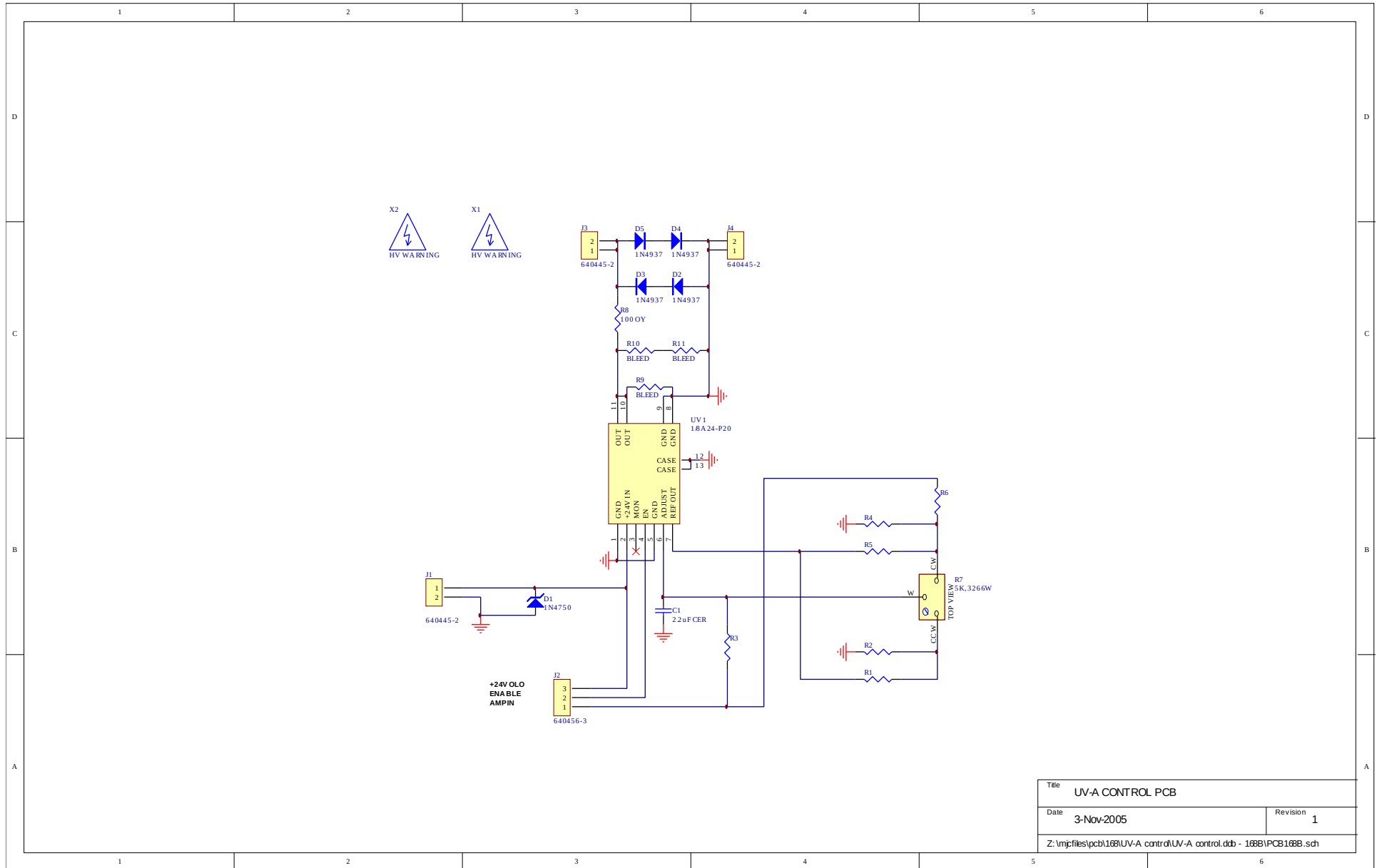
Title	LOW VOLTAGE DC/DC POWER SUPPLY	
Date	13-Sep-2005	Revision
Z:\mpjfiles\pcb158\switching01rz.ddb - 158K\pcb158k.sch		

# PCB 158K - LOW VOLTAGE POWER SUPPLY, 2/3



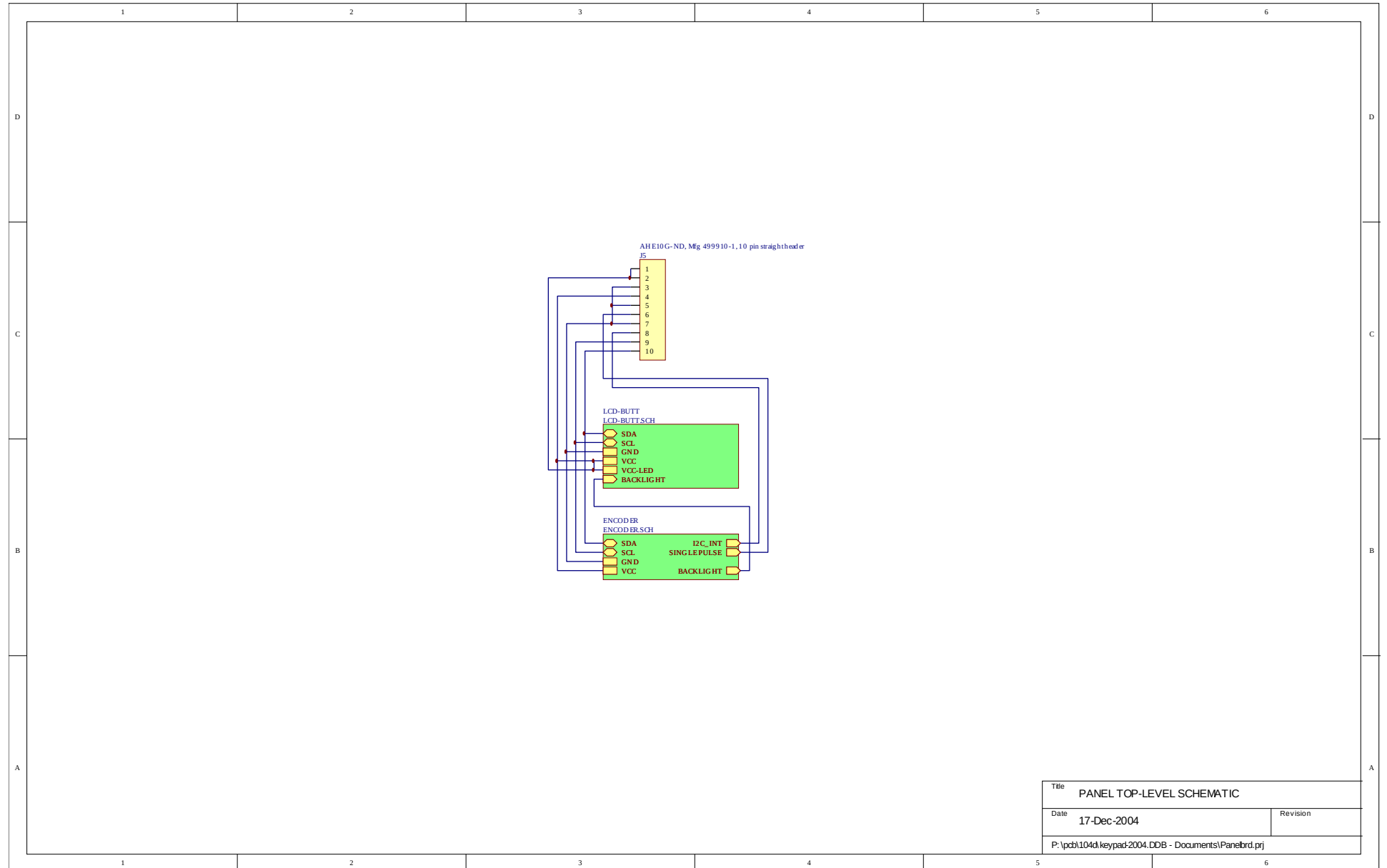


# PCB 168B - HIGH VOLTAGE DC POWER SUPPLY



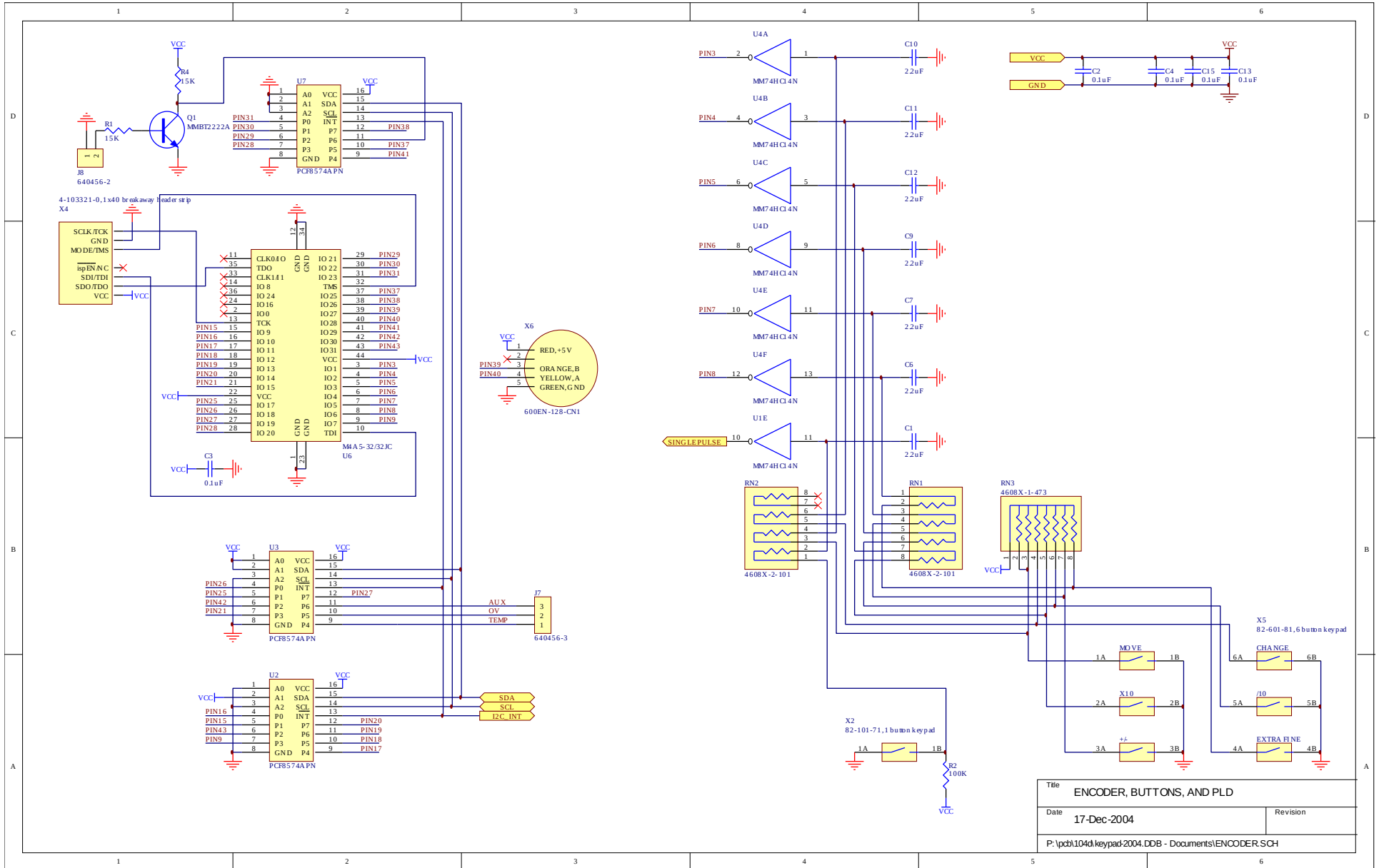
Title UV-A CONTROL PCB	
Date 3-Nov-2005	Revision 1
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# PCB 104D - KEYPAD / DISPLAY BOARD, 1/3

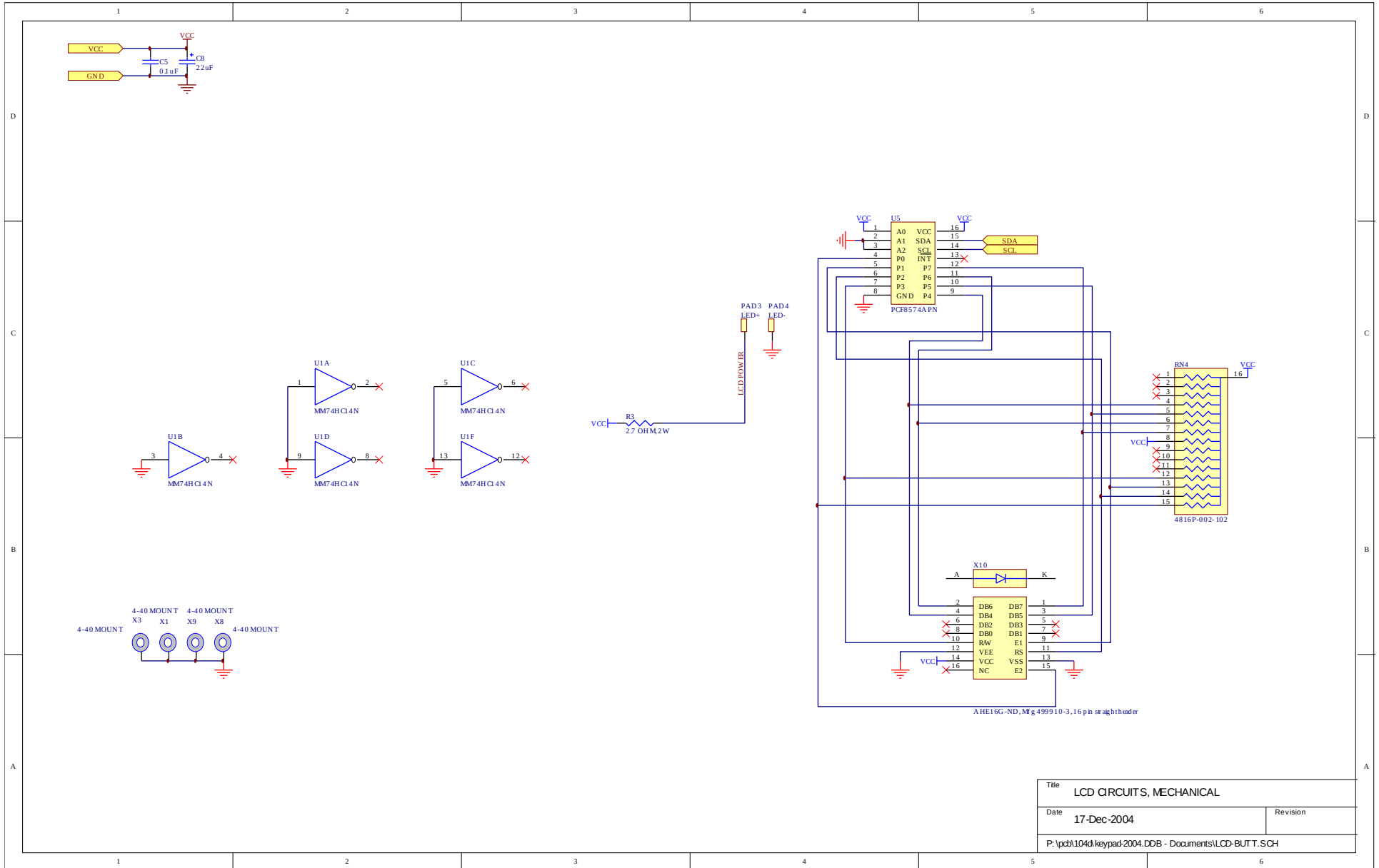


Title	PANEL TOP-LEVEL SCHEMATIC	
Date	17-Dec-2004	Revision
P:\pcb\104d\keypad\2004.DDB - Documents\Panelbrd.prj		

# PCB 104D - KEYPAD / DISPLAY BOARD, 2/3

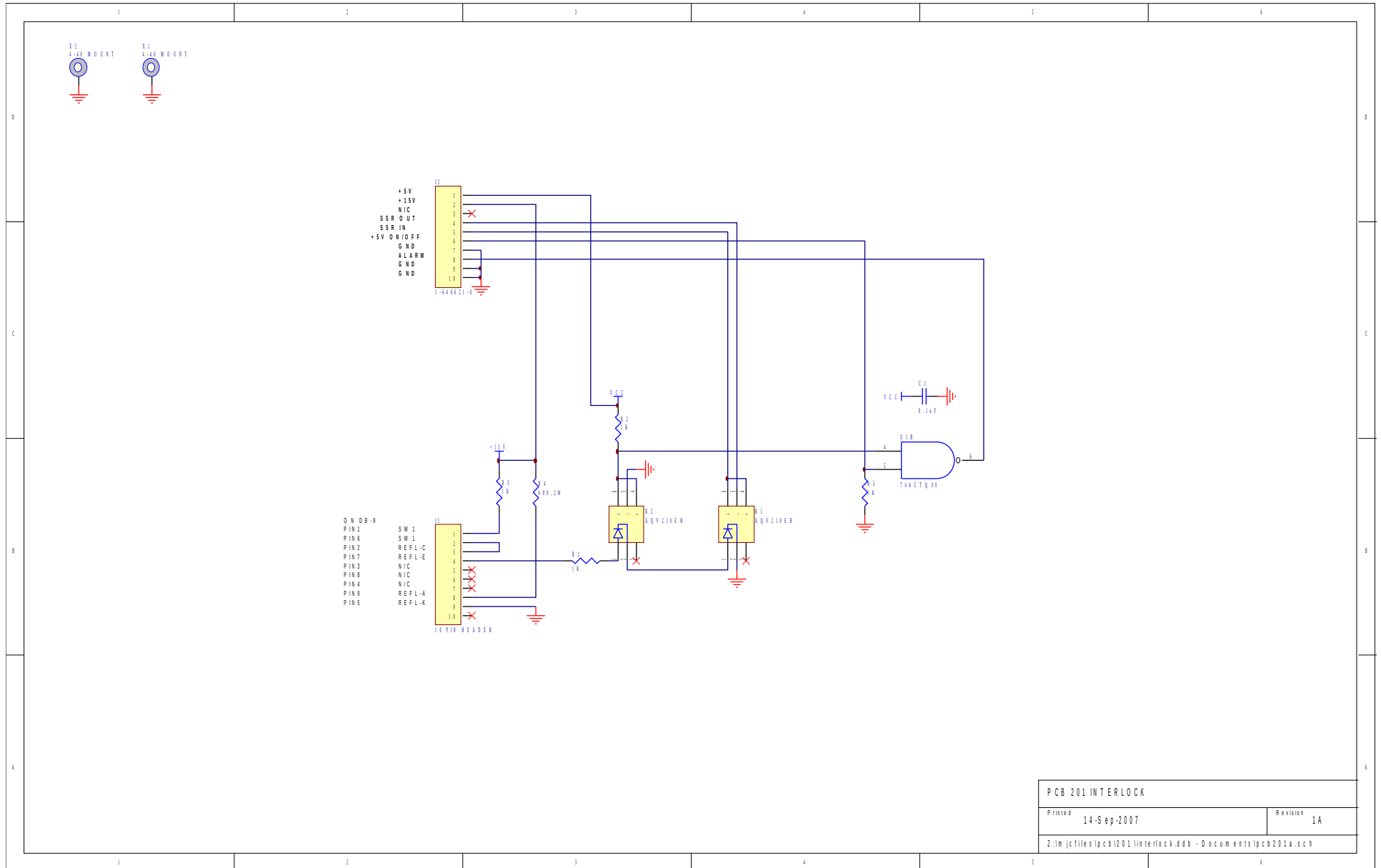


# PCB 104D - KEYPAD / DISPLAY BOARD, 3/3



Title		LCD CIRCUITS, MECHANICAL	
Date	17-Dec-2004	Revision	
P:\pcb\104d\keypad2004.DDB - Documents\LCD-BUTT.SCH			

# PCB 201A - INTERLOCK CONTROL

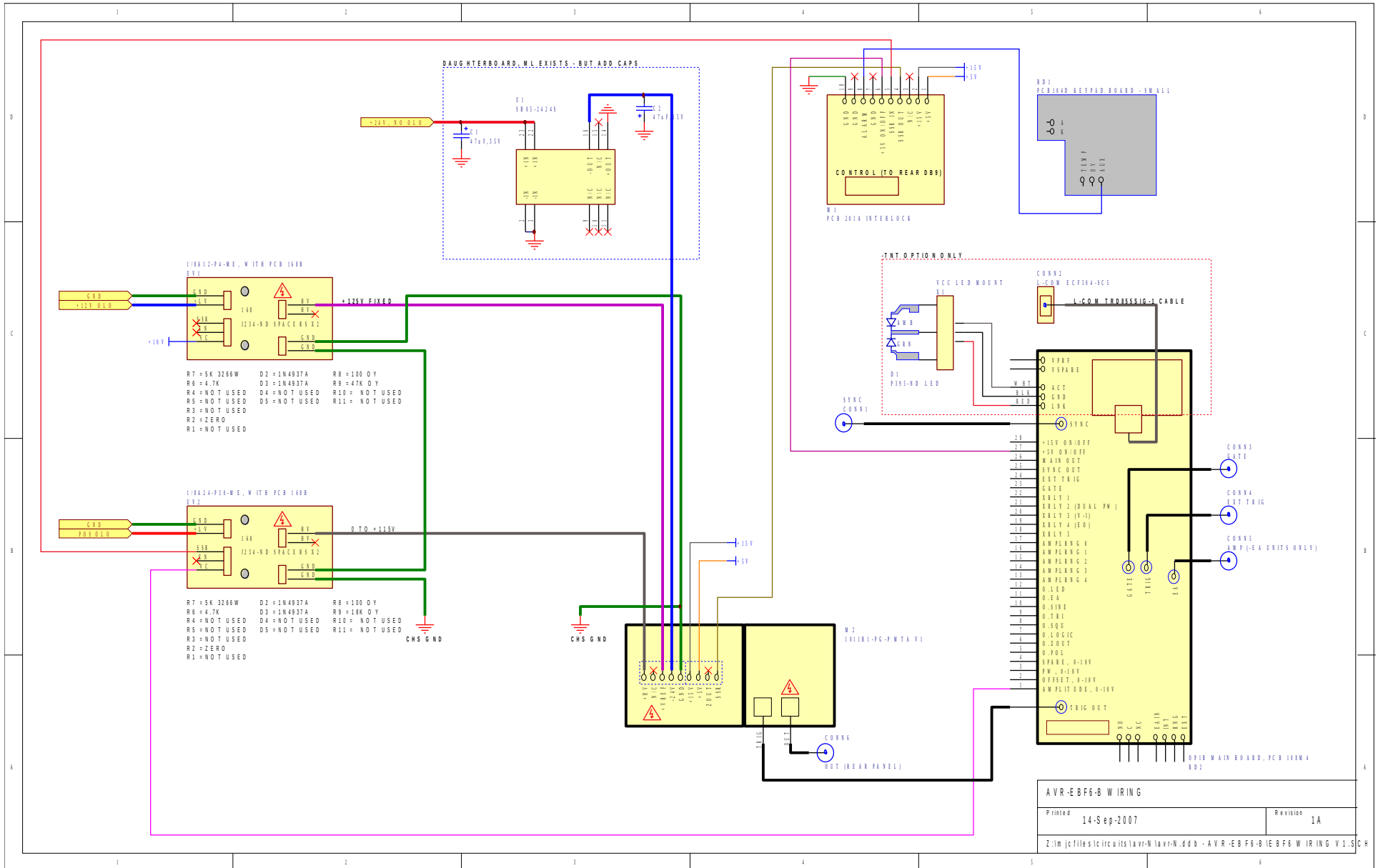


PCB 201 INTERLOCK	
Printed 14-Sep-2007	Revision 1A
Z:\m\jc\files\pcb\201\interlock.ddb - Documents\pcb201a.sch	

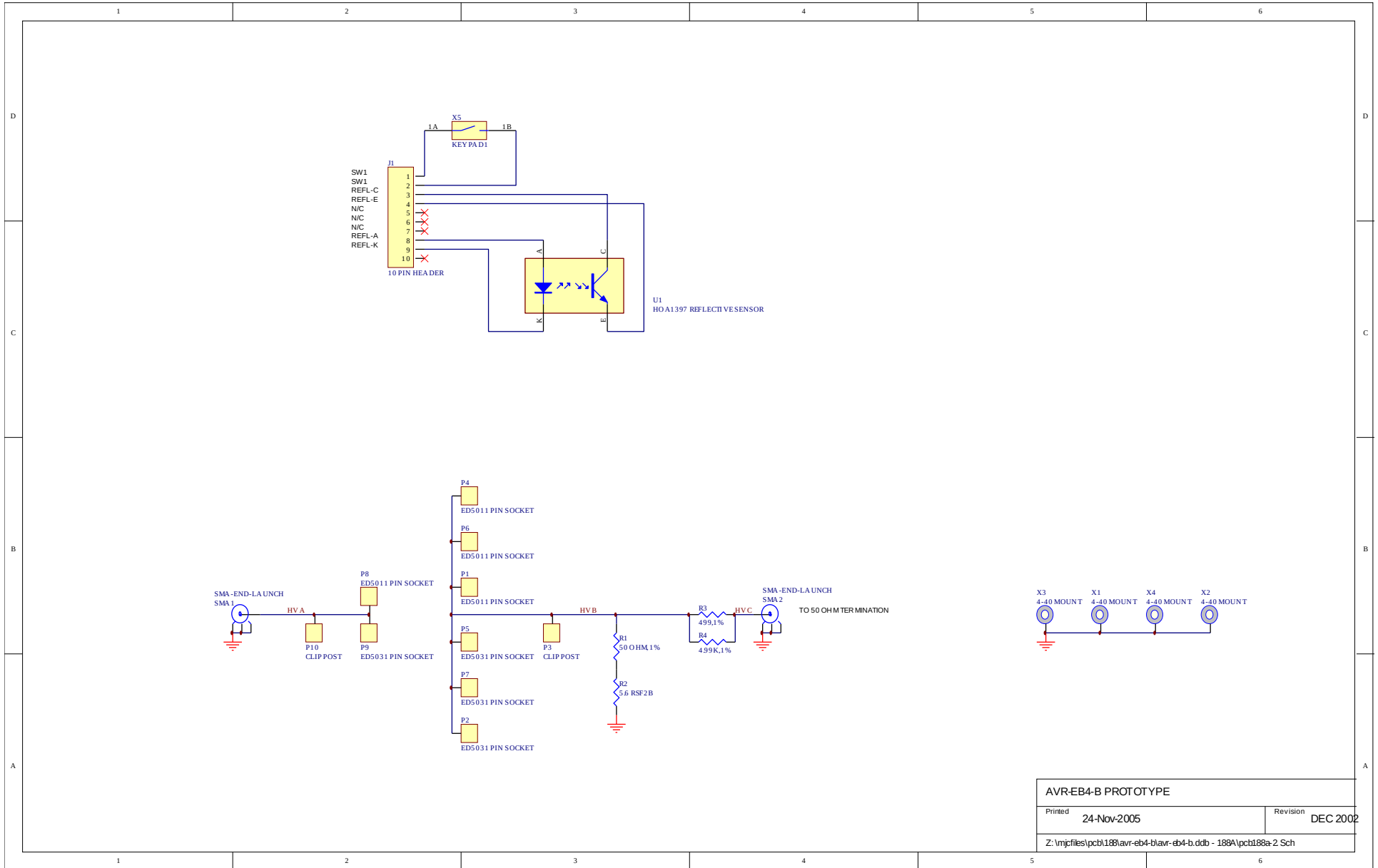




# MAIN WIRING

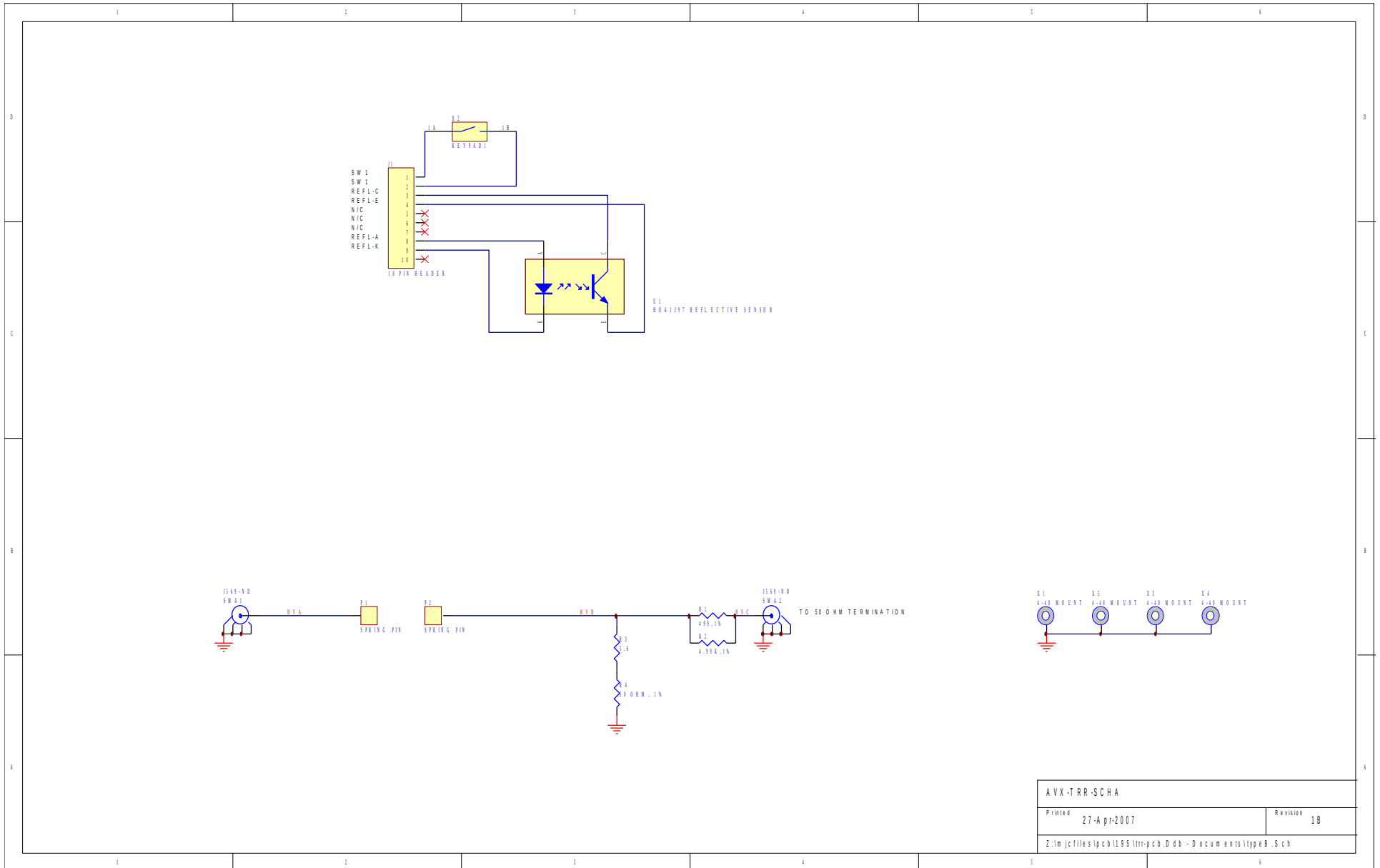


# STANDARD TEST JIG WIRING (AVX-TFR-MIX)



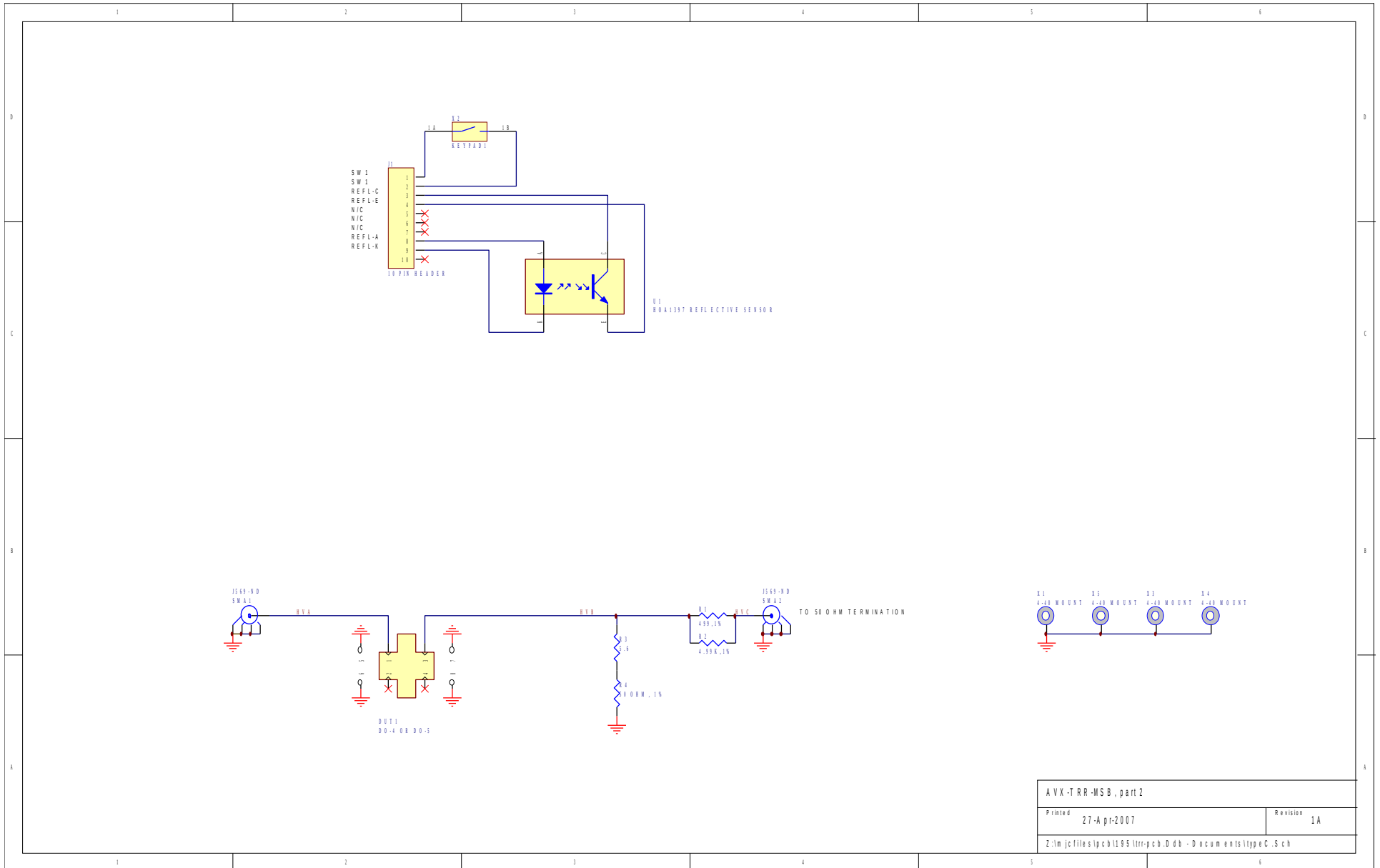
AVR-EB4-B PROTOTYPE	
Printed 24-Nov-2005	Revision DEC 2002
Z:\m\pfiles\pct1188\avr-eb4-blavr-eb4-b.ddb - 188A\pct1188a-2 Sch	

# TEST JIG WIRING (AVX-TRR-SCHA / AVX-TRR-MSB-MELF)



AVX-TRR-SCHA	
Printed 27-Apr-2007	Revision 18
Z:\m\jc\files\ipc\b1195\trr-pcb.Ddb - Documents\typeB.Sch	

# TEST JIG WIRING (AVX-TRR-SCHA / AVX-TRR-MSB-STUD)



PERFORMANCE CHECK SHEET