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INSTRUCTIONS

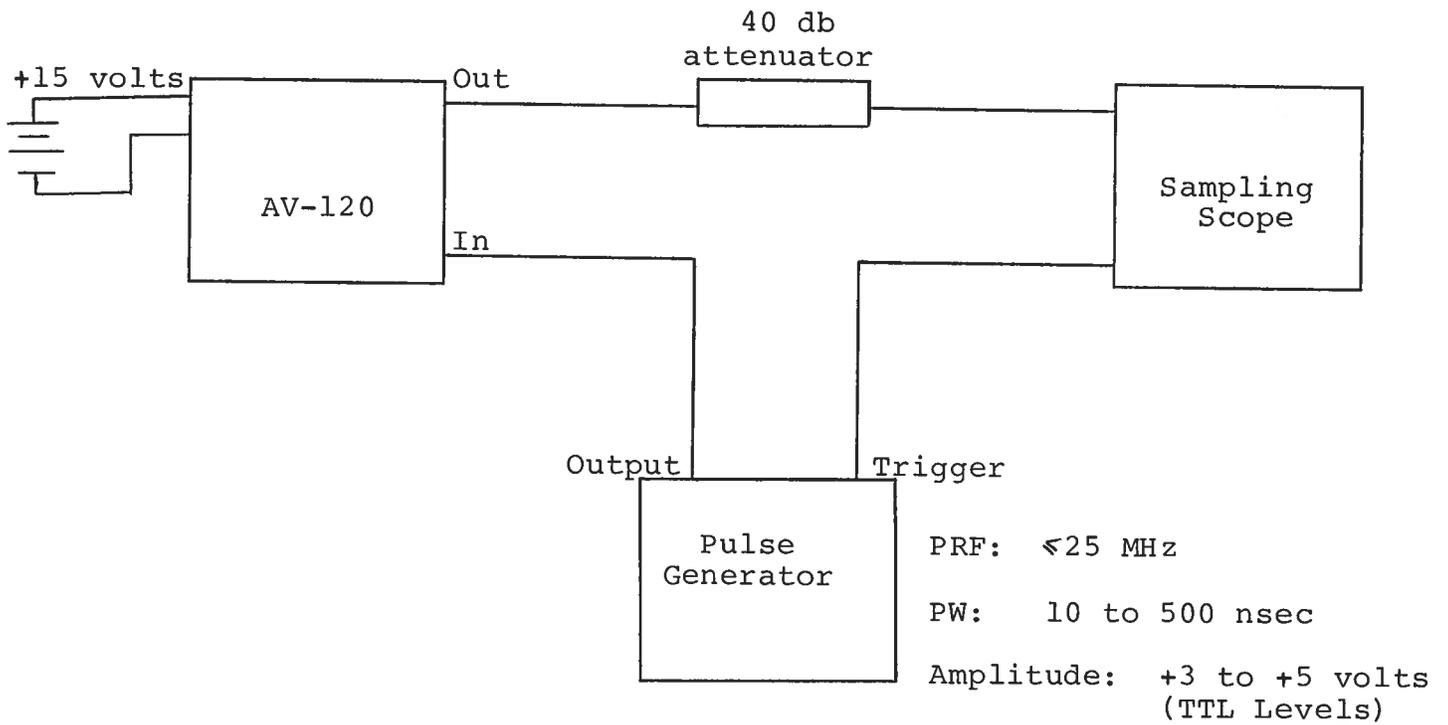
MODEL AV-120-0S PULSE GENERATOR-DRIVER

S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AV-120 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one GHz.
- 2) The use of a 40 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.
- 4) WARNING: Model AV-120 may fail if triggered at a PRF greater than 25 MHz or if the duty cycle exceeds 50% or if the PW exceeds 500 nsec.
- 5) The output amplitude is controlled by means of the one turn potentiometer (AMP).
- 6) The output pulse width is approximately equal to the input pulse width.
- 7) When triggering the AV-120 from a high speed lab pulse generator it may be necessary to shunt the input to the AV-120 by a 50 ohm resistor to eliminate reflection which may interfere with the operation of the lab pulse generator.
- 8) The 3 inputs of Model AV-120 operate as an OR gate.
- 9) To DC offset the output, connect a DC power supply set to the desired offset values to the rear panel OS terminals (±50 volts).

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Note: Not adjusted

- 1) The bandwidth capability of components used to display the power generator output signal (transformer, cables, connectors, etc.) should exceed one GHz.
- 2) The use of a 50 ohm attenuator will insure a peak front signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set to the 0.1 to 1.0 usec range. These settings should be as shown in the above diagram.
- 4) WARNING: Model AV-150 may fail if triggered at a PRF greater than 55 Hz or if the duty cycle exceeds 50% or if the PW exceeds 500 usec.
- 5) The output amplitude is controlled by means of the one turn potentiometer (AMP).
- 6) The output pulse width is approximately equal to the input pulse width.
- 7) When triggering the AV-150 from a high speed lab output generator it may be necessary to short the input to the AV-150 by a 50 ohm resistor to eliminate reflection which may interfere with the operation of the lab pulse generator.
- 8) The input to Model AV-150 should be an OR gate.
- 9) To DC offset the output, connect a DC power supply set to the desired offset value to the rear panel DC terminals (50V offset).