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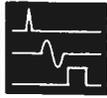
INSTRUCTIONS

MODEL AV-155A-C-PN-EA-EO-M-ORTEL2 PULSE GENERATOR

S.N.: 7278 (REVISED)

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

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CAUTION

To avoid possible damage to the laser diode, the following start up and shut down sequences should be followed:

START UP SEQUENCE

- 1) POWER switch is OFF (i.e. button out).
- 2) Set amplitude controls fully CCW.
- 3) Set OPERATE-STANDBY switch to STANDBY position.
- 4) Connect the laser diode to the output terminals.
CAUTION: Insure that the diode connector corresponds to the polarity switch setting.
- 5) Apply prime power by setting the POWER switch in the ON position (i.e. push button in).
- 6) Set OPERATE-STANDBY switch to OPERATE position.
- 7) Set amplitude to desired level. Do not change the position of the OPERATE-STANDBY switch from OPERATE to STANDBY without first setting the amplitude controls to fully CCW.

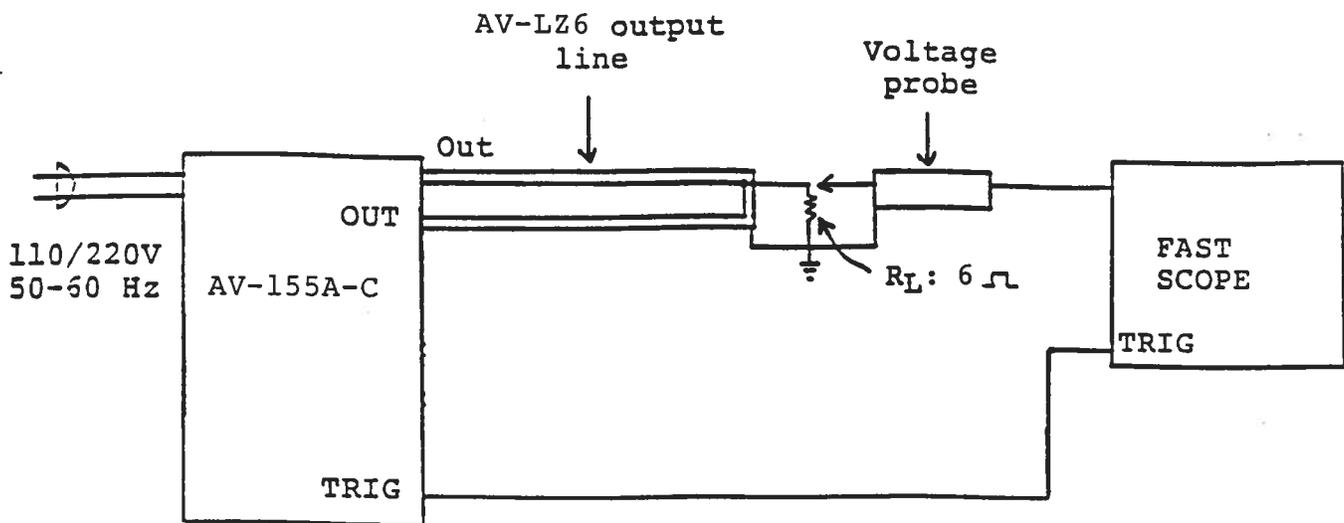
SHUT DOWN SEQUENCE

- 1) Set amplitude controls fully CCW.
- 2) Set OPERATE-STANDBY switch to the STANDBY position.
- 3) Turn off the prime power by pushing the POWER switch button.
- 4) Remove the laser diode from the output terminals.

Fig. 1

MODEL AV-155A-C PULSE GENERATOR TEST ARRANGEMENT

(RESISTIVE LOAD, NO DIODE)



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (probes, cables, connectors, etc.) should exceed 100 MHz.
- 2) A low-inductance resistor should be used as the load. Note that an inductance of 50 nh will yield an inductance spike of about one Volt.
- 3) To control the unit via the internal clock, set the INT-EXTA-EXTB switch in the INT position. Set the PRF range switch in either position.
- 4) The output pulse width is controlled by means of the front panel one turn PW control and the X1-X10 range switch. The control should initially be set mid range and the pulse width adjusted using an oscilloscope.
- 5) The output pulse amplitude is controlled by means of the front panel ten turn AMP control. Note that the load voltage range (i.e. compliance voltage) of the unit is 4 Volts so the load resistance must be sufficiently low (ideally six Ohm) so that the load voltage does not exceed 4 Volts. To control the output pulse amplitude electronically, place the rear panel AMP switch in the EXT position and apply 0 to +10 VDC between the "A" BANANA connector and ground ($R_{IN} \geq 10K$, $BW \geq 10$ kHz).
- 6) The output DC bias (or offset) is controlled by the front panel DC bias control. The polarity of the bias is always the same as the output pulse polarity. To control the DC bias electronically, place the rear panel OFFSET switch in the EXT position and apply 0 to +10 VDC between the "A" BANANA connector and ground ($R_{IN} \geq 10K$, $BW \geq 10$ kHz).
- 7) The polarity of the output pulse and bias (or offset) are both controlled by the front panel two-position polarity switch.
- 8) The DELAY control controls the relative delay between the reference output pulse provided at the TRIG output and the main output. This delay is variable over the range of 0 to 200 ns.
- 9A) The unit may also be triggered externally by setting the INT-EXTA-EXTB switch in either the EXTA or EXTB position and applying a trigger pulse to the TRIG BNC.

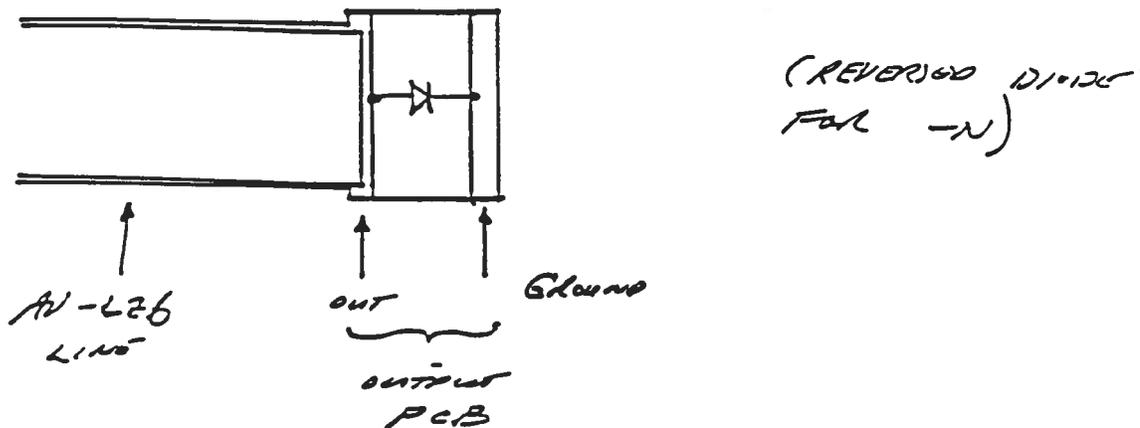
- 9B) When the switch is in the A position the unit operates as a voltage to current converter as follows:

$$I_{OUT} = 0.1 V_{IN}$$

Note that in this mode, the input amplitude should not exceed +2 Volts and also note that the PRF pulse width, amplitude and DC bias controls on the front panel are inactive.

- 9C) When the switch is in the B position, the unit requires a TTL input trigger pulse (PW > 50 ns) and the output pulse width, amplitude and DC bias are controlled by the front panel controls. The input trigger rate should not exceed 10 MHz as this may result in damage to the unit.

- 10) After confirming the general operation of the instrument using a resistive load, the resistive load should be removed and the diode load should be solder connected (as shown below) to the end of the AV-L26 flexible line which protrudes from the front panel. **CAUTION:** If the polarity switch is in the "POS" position, the laser diode anode and cathode must be connected as shown below. If the polarity switch is in the "NEG" position, the anode and cathode connection must be removed.



For units with the S5 option, the diode plugs directly into the socket provided.

- 11) The diode impedance should equal the characteristic impedance of the output line (6 Ohm). If the diode impedance is higher than six Ohm, the output rise time will be higher than 10 ns. This problem may be solved by replacing the AV-LZ6 line with a higher impedance version. The flexible output transmission line (AVTECH Model AV-LZ6) may be changed by desoldering the line from the 1/16" glass epoxy circuit board which protrudes from the -PG module. The -PG module is accessed by removing the four Phillips screws on the back panel of the instrument. The top cover will then slide back (and off), thereby exposing the -PG module.
- 12) If the diode impedance is less than six Ohm, the output waveform may exhibit severe overshoot. The addition of a small series resistor will serve to reduce the overshoot. Also if the diode has significant inductance (>10 nh) overshoot will be observed and for this reason the diode lead lengths must be extremely short (≤ 0.1 inches).
- 13) The unit can be converted from 100 to 220V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
- 14) For additional assistance:

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Fax: (613) 226-2802

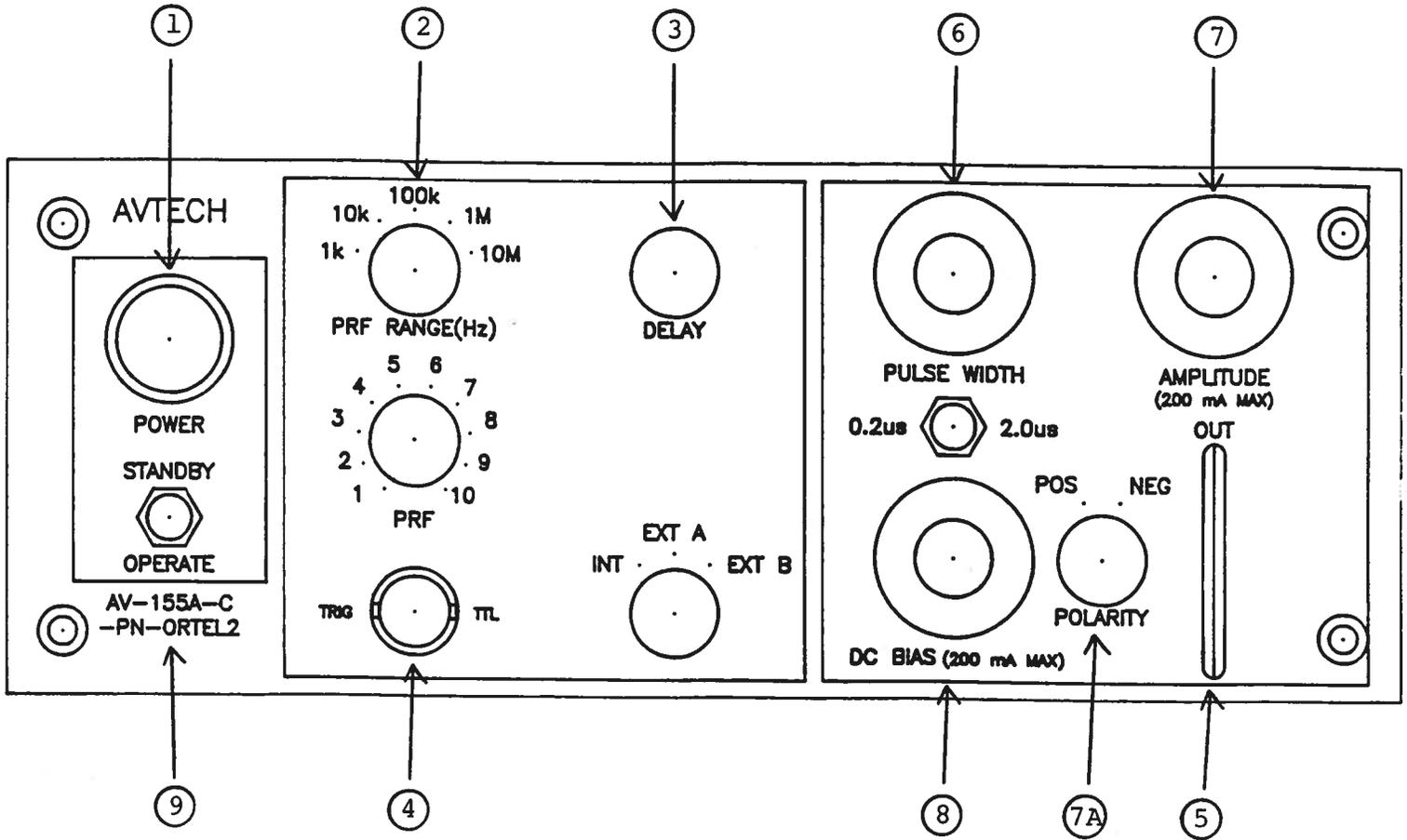


Fig. 2

FRONT PANEL CONTROLS

- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. Varies PRF from 100 Hz to 1 MHz as follows:

100 Hz to 1 kHz
1 kHz to 10 kHz
10 kHz to 100 kHz
100 kHz to 1 MHz
1 MHz to 10 MHz

- (3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (5). This delay is variable over the range of 0 to about 200 ns.
- (4) TRIG. When the INT-EXTA-EXTB switch is in the INT position this output is used to trigger the scope time base. The output is a TTL level 20 ns (approx.) pulse capable of driving a fifty Ohm load. The external trigger signal is applied at this input when the toggle switch is in the EXTA or EXTB position. When the switch is in the A position the unit operates as a voltage to current converter as follows:

$$I_{OUT} = 0.1 V_{IN}$$

Note that in this mode, the input amplitude should not exceed +2 Volts and also note that the PRF, pulse width, amplitude and DC bias controls on the front panel are inactive.

When the switch is in the B position, the unit requires a TTL input trigger pulse (PW > 50 ns) and the output pulse width, amplitude and DC bias are controlled by the front panel controls. The input trigger rate should not exceed 10 MHz as this may result in damage to the unit.

- (5) OUT. 1 meter long AV-LZ6 flexible output line protrudes from the front panel. Diode load and series matching resistor to be solder connected to end of line. For units with -S5 option, diode plugs into the socket provided.
- (6) PULSE WIDTH. Varies output pulse width as follows (INT and EXTB MODE only):
20 ns to 200 ns
200 ns to 2 us

- (7) PULSE AMPLITUDE. Ten turn amplitude control varies output pulse amplitude from 0 to 200 mA (INT and EXTB mode only).
- (7A) POLARITY. This two-position switch determines the polarity of the output pulse (and DC offset).
- (8) DC BIAS AMPLITUDE. Ten turn offset control varies DC offset from 0 to 200 mA (INT and EXTB mode only).
- (9) STANDBY-OPERATE. When this switch is in the STANDBY position, a set of relay controls shorts out the laser diode whether the prime power is ON or OFF. When the switch is in the OPERATE position, the relay contacts open, provided the prime power is on. See the START UP and SHUT DOWN SEQUENCE notes preceding Fig. 1 for a full discussion of the operation of this switch.

BACK PANEL CONTROLS

- 1) Power Entry Module. Detachable line cord connects to this point. Also contains voltage selector card and line fuse (0.5 A SB).
- 2) 1.0 A SB Fuses. Limits current supplied to the output stages.
- 3) Monitor Option. BNC connector provides a coincident replica of the output pulse. For $R_L \geq 1K$.

$$I_{LOAD} = 200 V_M \text{ (Volts, mA)}$$

$$\text{For } R_L = 50 \text{ Ohm}$$

$$I_{LOAD} = 400 V_M \text{ (Volts, mA)}$$

V_M is the monitor output voltage, amplitude is determined using a scope.

- 4) PULSE AMP. To control the output pulse amplitude electronically, place the rear panel AMP switch in the EXT position and apply 0 to +10 VDC between the "A" BANANA connector and ground ($R_{IN} \geq 10K$, $BW \geq 10 \text{ kHz}$).
- 5) OFFSET AMP. To control the DC bias electronically, place the rear panel OFFSET switch in the EXT position and apply 0 to +10 VDC between the "A" BANANA connector and ground ($R_{IN} \geq 10K$, $BW \geq 10 \text{ kHz}$).
- 6) PG NULL. A one turn pot which is to be used to set any spurious DC offset to zero. To set, attach a 10 Ohm resistive load to the output and set both the pulse amplitude and DC bias controls fully CCW. Monitor the voltage across the resistive load using a scope set at 50 mV/DIV and adjust the PG NULL pot to obtain zero volts.
- 7) M NULL. A one turn pot which is to be used to zero any DC offset in the monitor circuit. This pot is used to set the monitor output to zero after the PG NULL control (see 6 above) is set to zero.

TOP COVER REMOVAL

To remove the top cover, remove the four Phillips screws on the top cover and then slide the cover back and off.

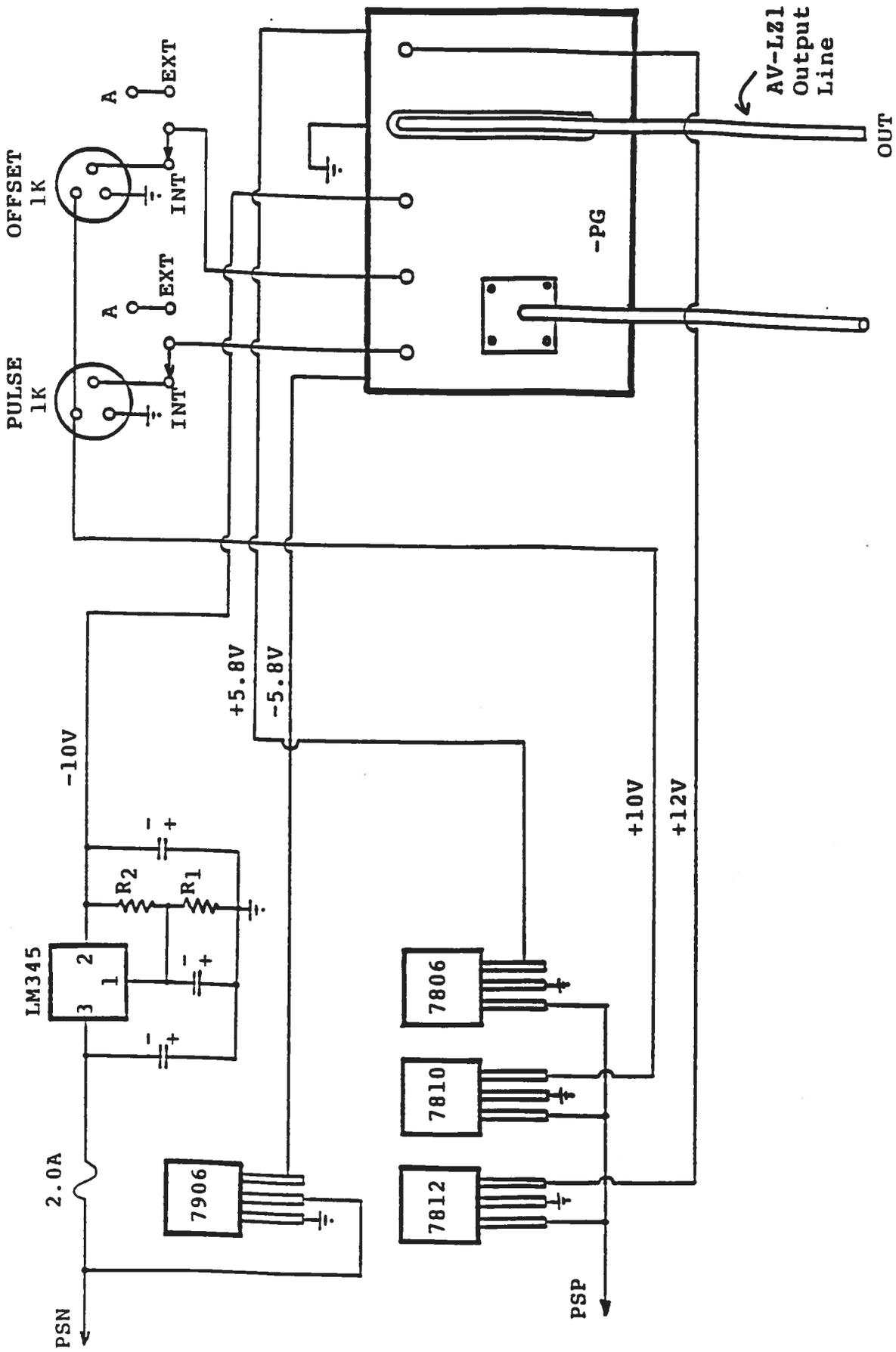


Fig. 5: AV-155-PS-DUP3 BLOCK DIAGRAM

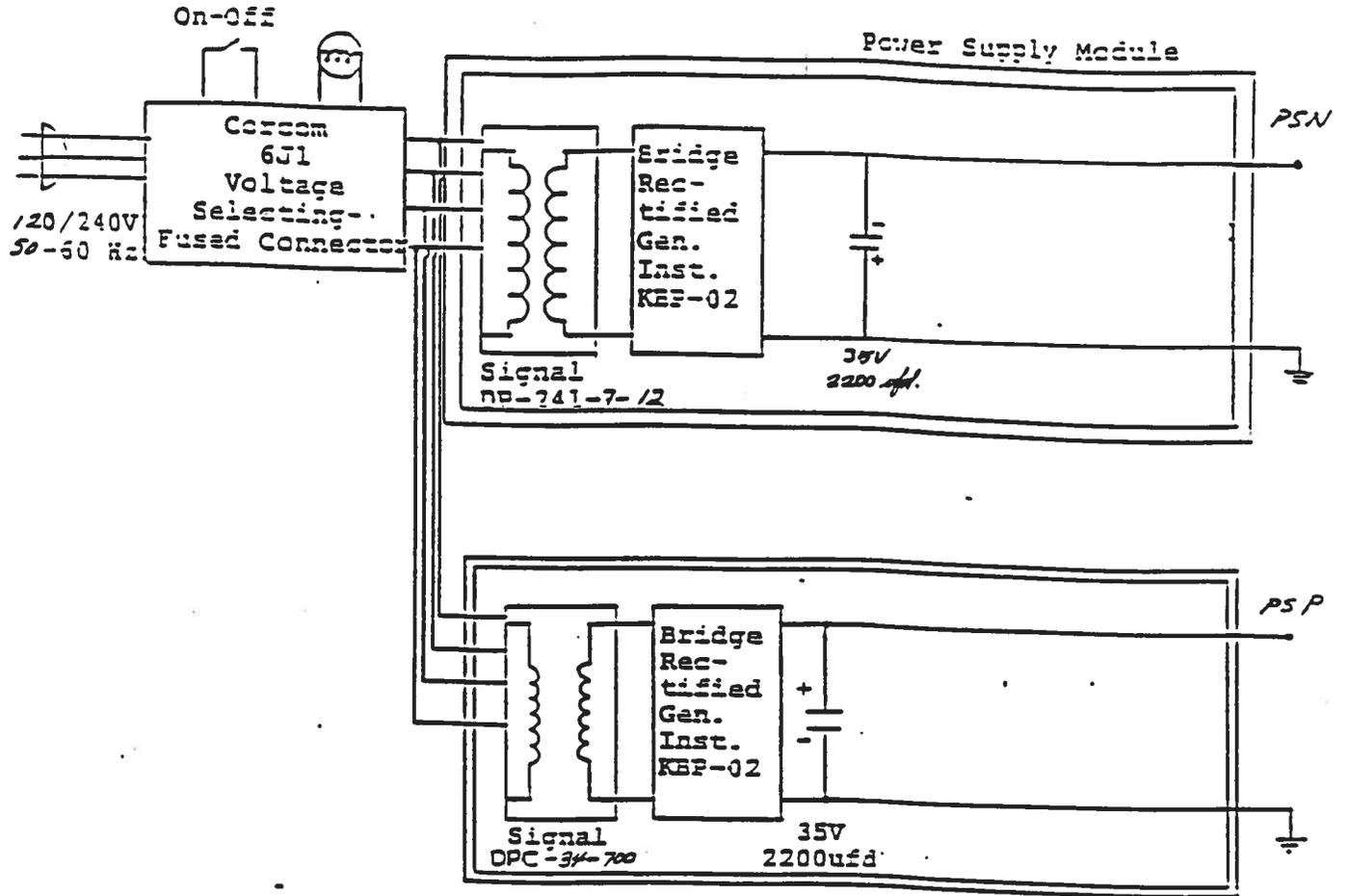
SYSTEM DESCRIPTION AND REPAIR

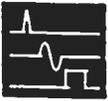
Model AV-155A-C consists of a clock module (-CL2), a pulse generator module (AV-155A-PG) and a power supply which supplies -12V, -5.8V, +15V and +5.8 Volts to the modules. The power supply and block diagram are shown in Figs. 4 and 5.

If the instrument does not provide an output, check the line fuse and the 1.0 A SB fuses. If the fuses are not at fault, remove the top cover and check the -12V, -5.8V, +15V and +5.8 voltage levels. If the voltage levels are correct then the -PG module is defective. The sealed -PG module must then be returned to AVTECH for repair and replacement.

Fig. 4

POWER SUPPLY



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Fax Ref No: 10593 From: Avtech Electrosystems Ltd.

To: Ortel Our Fax No: (613) 226-2802

Date: February 16, 1995

Attn: Jerry Byrd Receivers Fax No: 818-281-8190
Tel: 818-293-3652

Subject: AV-155A-C quotation No. of pages: 1

Following my FAX quotation of February 13th and our subsequent conversation, I am pleased to provide the following additional price and delivery quotation:

Model designation: **AV-155A-C-PN-EA-EO-ORTEL2**

Price: **\$5,082.00 US each, FOB destination**

Delivery: **45 days**

The -EA option provides the necessary electronic control of the pulse amplitude while the -EO option provides electronic control of the DC offset. The ORTEL2 option provides a 10 kHz bandwidth for the EA and EO options and, in addition, provides for a single cable output line. The output polarity is then controlled solely by the front panel two-position switch. In addition to the above options, you may also wish to consider the current monitor option (-M, \$280.00 US) since this provides a replica of the output current waveform.

Please call me again (1-800-265-6681) if you require any additional information or modifications to the above quotation.

Rgds

Dr. Walter Chudobiak
Chief Engineer

WC:pr

June 8/95

Disk: AV-

Name: 155#7278.INS