



# AVTECH ELECTROSYSTEMS LTD.

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SINCE 1975

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## INSTRUCTIONS

MODEL AVG-3-EA-OS-M IMPULSE GENERATOR

S.N.:

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

### TECHNICAL SUPPORT

Phone: 613-226-5772 or 1-800-265-6681

Fax: 613-226-2802 or 1-800-561-1970

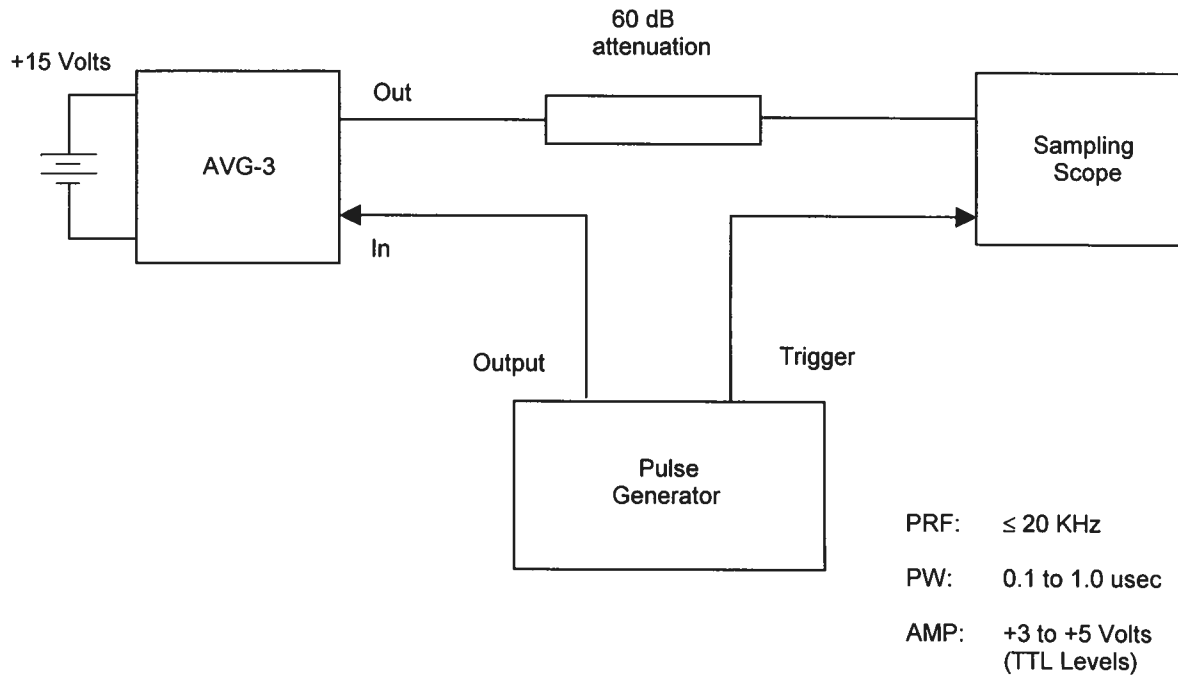
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Manual Reference: Q:\office\instructword\Avg\AVG-3-EA-OS-M-ed.doc, created February 16, 2001

**FIG. 1: IMPULSE GENERATOR TEST ARRANGEMENT**

## GENERAL OPERATING INSTRUCTIONS

- 1) The bandwidth capability of components and instruments used to display the impulse generator output signal (attenuators, cables, connectors, etc.) should exceed 1.0 gigahertz.
- 2) The use of 60 dB attenuation will insure a peak input signal to the sampling scope of less than one Volt.
- 3) In general, the pulse generator trigger delay control should be set in the 100 ns range. Other settings should be as shown in the above diagram. The impulse generator output is delayed with respect to the trigger input signal by about 70 ns. (typically).
- 4) The impulse generator can withstand an infinite VSWR on the output port.
- 5) The output amplitude may be varied from 0 to 500 Volts by applying 0 to +10 VDC to the "AMP" solder terminal ( $R_{IN} \geq 1K$ ). (-EA Option.)
- 6) A coincident attenuated ( $\times 10$ ) replica is provided at the "M" SMA connector (when terminated in 50 Ohms). (-M Option)
- 7) A DC offset of 0 to  $\pm 50$  Volts (+200 mA max) can be applied to the output by applying the required DC potential to the "OS" solder terminal. Note that the DC current must not exceed  $\pm 200$  mA.
- 8) For additional assistance:

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Feb 16, 2001