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NANOSECOND WAVEFORM ELECTRONICS SINCE 1975

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INSTRUCTIONS

MODEL AVG-4C-C IMPULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

TECHNICAL SUPPORT

Phone: 613-226-5772 or 1-800-265-6681 Fax: 613-226-2802 or 1-800-561-1970

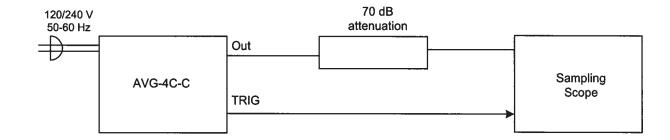
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Manual Reference: Q:\office\instructword\Avg\AVG-4C-C, edition2.doc, created October 30, 2002

FIG. 1: IMPULSE GENERATOR TEST ARRANGEMENT



<u>CAUTION</u>: Exercise due caution when employing this instrument as it provides output pulses as high as 1000 Volts!!

GENERAL OPERATING INSTRUCTIONS

- The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
- The use of 70-dB attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one Volt.

 CAUTION: Insure that the attenuator can withstand the exceptionally high input voltage level (1000 Volts).
- 3) The TRIG output channel provides TTL level signals.

To obtain a stable output display the PRF control on the front panel should be set mid-range while the PRF range switch may be in either range (but not in the EXT position).

The DELAY controls and the scope triggering controls are then adjusted to obtain a stable output.

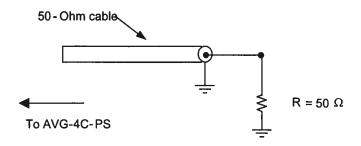
4) The general pulse shape is controlled by the following three controls:

<u>PWA, PWB</u>: The main shape controls. Clockwise rotation increased the pulse width (and amplitude). Normally the two controls are set at the same value and normally are set max clockwise.

<u>TAB</u>: This control is adjusted to peak the output amplitude.

After changing the PRF dramatically, it may be necessary to re-adjust the three controls to re-obtain the desired output pulse shape.

The main output must see a well-defined 50-Ohm load and low loss 50-Ohm cable (eg, 80 mil or larger semi-rigid) should be used. This cable can be as long as several feet. The termination load should be of the form shown below:

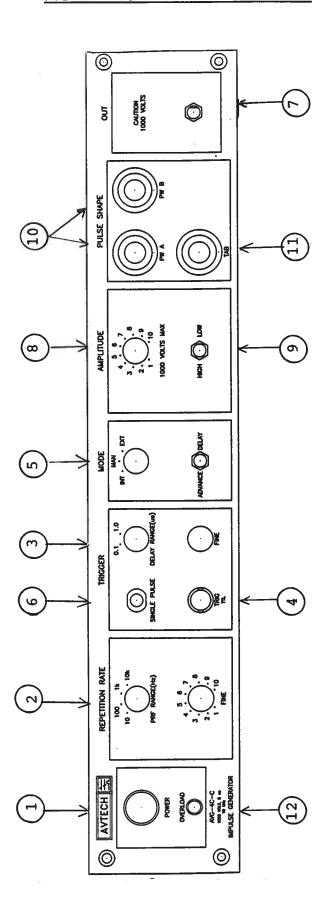


- The output amplitude is controlled by the front panel one-turn amplitude control and the two position High-Low switch. With the switch in the high position, the output amplitude may be varied from 300 to 1000 volts. With the switch in the low position, the amplitude may be varied from 0 to 850 volts.
- 7) The unit may be triggered externally by placing the MODE switch in the EXT position and applying a TTKL level pulse (PW > 50 ns) to the TRIG connector.
- 8) The unit can be converted from 120 to 240V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector cable connector assembly.
- 9) For additional assistance:

Tel: (613) 226-5772 Fax: (613) 226-2802

Email: info@avtechpulse.com

FIG. 2: FRONT PANEL CONTROLS



FRONT PANEL CONTROLS

- (1) <u>ON-OFF Switch</u>. Applies basic prime power to all stages.
- (2) PRF Control. The PRF RANGE and PRF controls determine output PRF as follows:

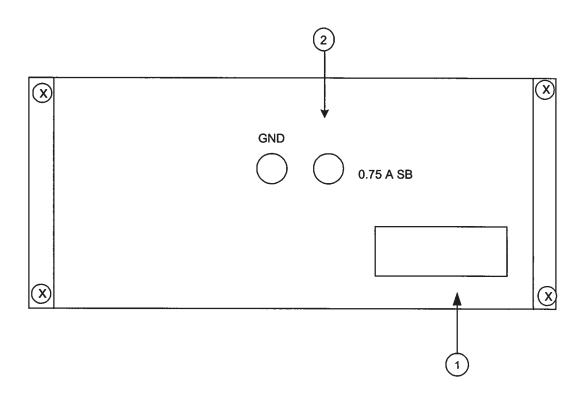
	PRF MIN	PRF MAX
Range 1	1 Hz	10 Hz
Range 2	10 Hz	100 kHz
Range 3	100 Hz	1 kHz
Range 4	1 kHz	10 kHz

- (3) <u>DELAY Control</u>. Controls the relative delay between the reference output pulse provided at the TRIG output (4) the main output (7). This delay is variable over the range of 0 to about 1.0 μs. Delay LEADS and LAGS depending on the position of the ADVANCE-DELYA switch.
- (4) TRIG Output. This output is used to trigger the scope time base. The output is a TTL level 100 ns (approx.) pulse capable of driving a 50-Ohm load. This output precedes the output at (7) if the two-position ADVANCE-DELAY switch is in the ADVANCE position. This output follows the output at (7) if the switch is in the DELAY position. The external trigger signal is applied at this input when the MODE switch is in the EXT position.
- (5) MODE. With this switch in the INT position, the PRF of the AVG unit is controlled via an internal clock, which in turn is controlled by the PRF controls. With the switch in the EXT position, the AVG unit requires a 50 ns (or wider) TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.
- (6) <u>SINGLE PULSE</u>. For single pulse manual operation, set the front panel MODE switch in the MAN position and push the SINGLE PULSE button.
- (7) OUT Connector. SMA connector provides output to the load ($R_L = 50$ Ohm).
- (8) <u>AMP Control</u>. A one-turn control and two position range switch, which vary the output pulse amplitude from 300 to 1000 volts (high) or 0 to 850 volts (low).

- (9) <u>POLARITY</u>. The output polarity is controlled by the two-position polarity switch.
- (10) <u>PWA, PWB</u>. The main shape controls. Clockwise rotation of the two ten-turn controls increases the pulse width (and amplitude to a lesser extent). Normally the two controls are set at the same value and normally are set fully clockwise.
- (11) <u>TAB</u>. This ten-turn control is adjusted to peak the output amplitude (at which point the output pulse width in minimized).
- OVERLOAD INDICATOR. AVG-4C units with a serial number higher than 5600 are protected by an automatic overload protective circuit, which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle) the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turnoff again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by reducing the PRF.

Note that the overload light may illuminate when the prime power is first applied. The light will extinguish after a few seconds and the instrument will then function normally.

FIG. 3: BACK PANEL CONTROLS



BACK PANEL CONTROLS

(1) <u>FUSED CONNECTOR</u>, <u>VOLTAGE SELECTOR</u>. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse. For AC line voltages of 110-120V, the power selector card should be installed so that the "120" marking is visible from the rear of the instrument.

For AC line voltages of 220-240V, the power selector card should be installed so that the "240" marking is visible from the rear of the instrument.

If it is not set for the proper voltage, remove the fuse and then grasp the card with a pair of pliers and remove it. Rotate horizontally through 180 degrees. Reinstall the card and the correct fuse.

In the 120V setting, a 1.0A slow blow fuse is required. In the 240V setting, a 0.5A slow blow fuse is required.

(2) 0.75A SB: This fuse limits the DC current supplied to the output stage.

TOP COVER REMOVAL

The interior of the instrument may be accessed by removing the four Philips screws on the top panel. With the four screws removed, the top cover may be slid back (and off). CAUTION: DC potentials as high as 425 Volts are employed in the chassis interior and so it is recommended that the unit be returned to AVTECH for any required servicing.

The -R5 Rack Mount Kit may be installed after first removing the one Philips screw on the side panel adjacent to the front handle.

Oct 30, 2002