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INSTRUCTIONS

MODEL AVB1-C-M1 PULSE GENERATOR

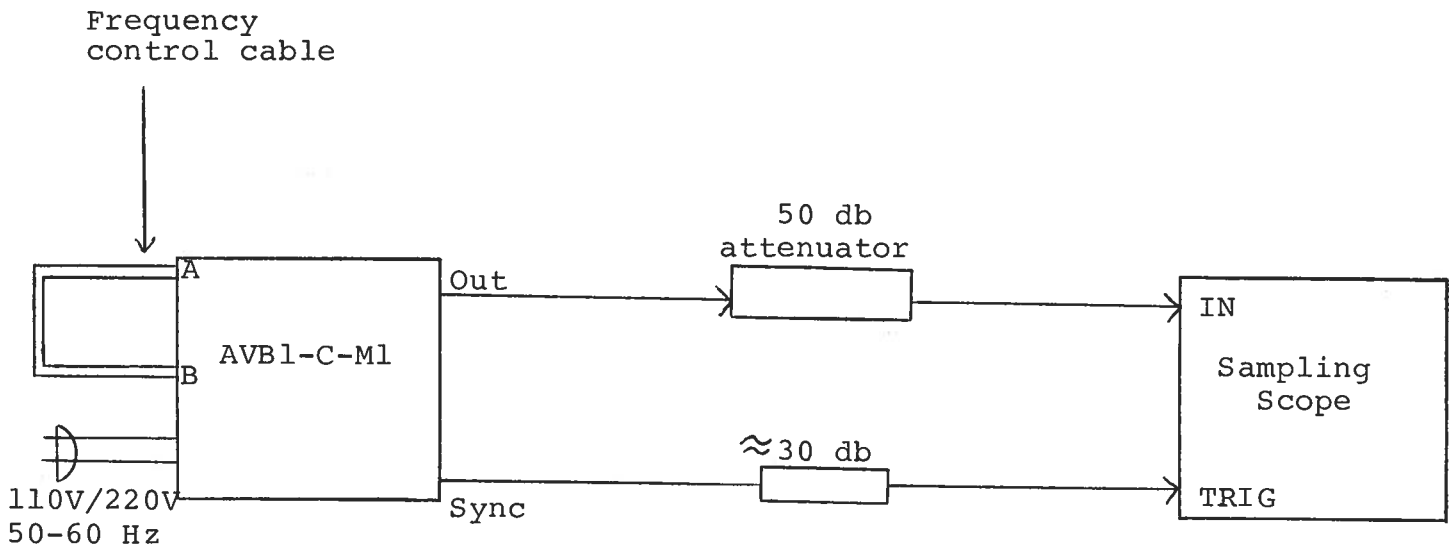
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### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

Fig. 1

PULSE GENERATOR TEST ARRANGEMENT

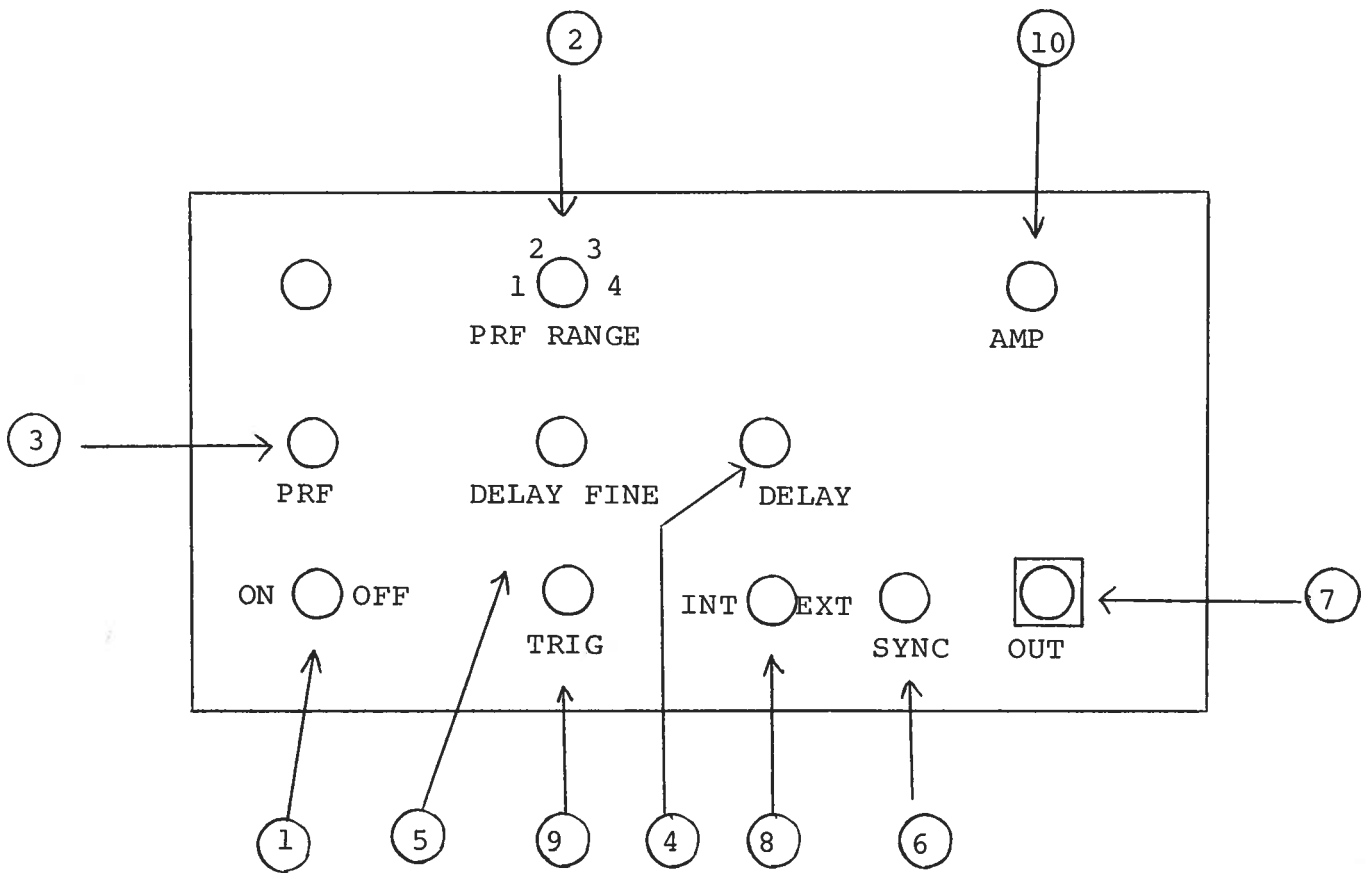


Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
- 2) The use of 50 db attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one volt.
- 3) The sync output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some sampling scopes, a 30 db attenuator should be placed at the input to the sampling scope trigger channel.
- 4) To obtain a stable output display the PRF control on the front panel should be set mid-range while the PRF range switch may be in either range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY controls and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF control and by means of the PRF range switch.
- 5) The output frequency is controlled primarily by the length of miniature 50 ohm coaxial cable connected between the A and B ports on the instrument back panel. The frequency control cable may be fabricated from RG 174 miniature coax with American 2001 - 7188 connectors (or the equivalent) or from 85 mil semi-rigid cable with American 2001 - 5032 connectors (or the equivalent). The output signal half-period ( $T/2$ ) and cable length ( $L$ ) are related linearly ( $\approx 1.5$  nsec per foot of cable). CAUTION: The center conductor on the frequency control cable is at a DC potential of +130V and so the instrument should be turned off when the cable is to be changed.
- 6) The output frequency is also slightly dependent on the setting of the PW P and PW N pot controls on the back panel. Clockwise rotation of the pot controls tends to increase the width of positive and negative going segments of the output sinusoid.
- 7) The output amplitude is controlled by the one turn front panel AMP control. Also, the output amplitude can be controlled using a 0 to +10 volt control voltage by removing the jumper wire between banana plugs A and B on the back panel and applying 0 to +10V to connector B ( $R_{IN} > 1K$ ).

Fig. 2

FRONT PANEL CONTROLS



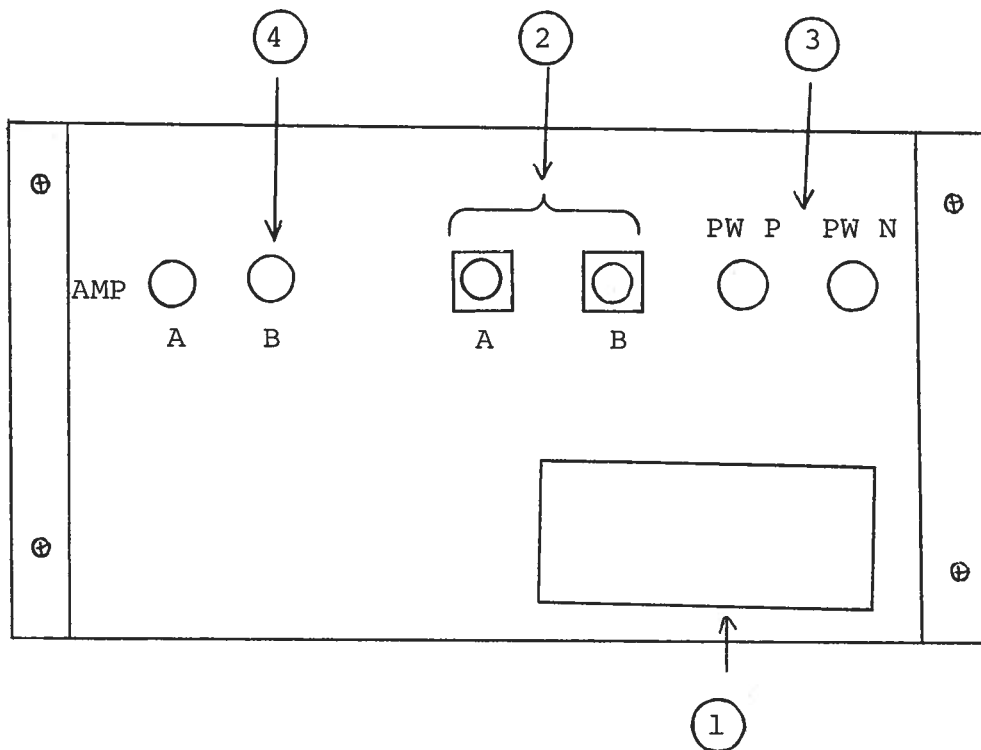
- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. The PRF RANGE and PRF controls determine
- (3) output PRF as follows:

	PRF MIN	PRF MAX
Range 1	100 Hz	1 KHz
Range 2	1 KHz	10 KHz
Range 3	10 KHz	100 KHz
Range 4	100 KHz	1 MHz

- (4) DELAY Controls. Controls the relative delay between the
- (5) reference output pulse provided at the SYNC output (6)
- and the main output (7). This delay is variable over
- the range of 0 to at least 500 nsec.
- (6) SYNC Output. This output precedes the main output (7)
- and is used to trigger the sampling scope time base.
- The output is a TTL level 100 nsec (approx) pulse
- capable of driving a fifty ohm load.
- (7) OUT. SMA output connect provides output to a fifty ohm
- load.
- (8) EXT-INT Control. With this toggle switch in the INT
- position, the PRF of the AVB unit is controlled via an
- internal clock which in turn is controlled by the PRF
- controls. With the toggle switch in the EXT position,
- the AVB unit requires a 0.2 usec TTL level pulse applied
- at the TRIG input in order to trigger the output stages.
- In addition, in this mode, the scope time base must be
- triggered by the external trigger source.
- (9) TRIG Input. The external trigger signal is applied at
- this input when the EXT-INT toggle switch is in the EXT
- position.
- (10) AMP. A one turn control which varies the output
- amplitude from 0 to 100 volts (peak to peak).

Fig. 3

BACK PANEL CONTROLS

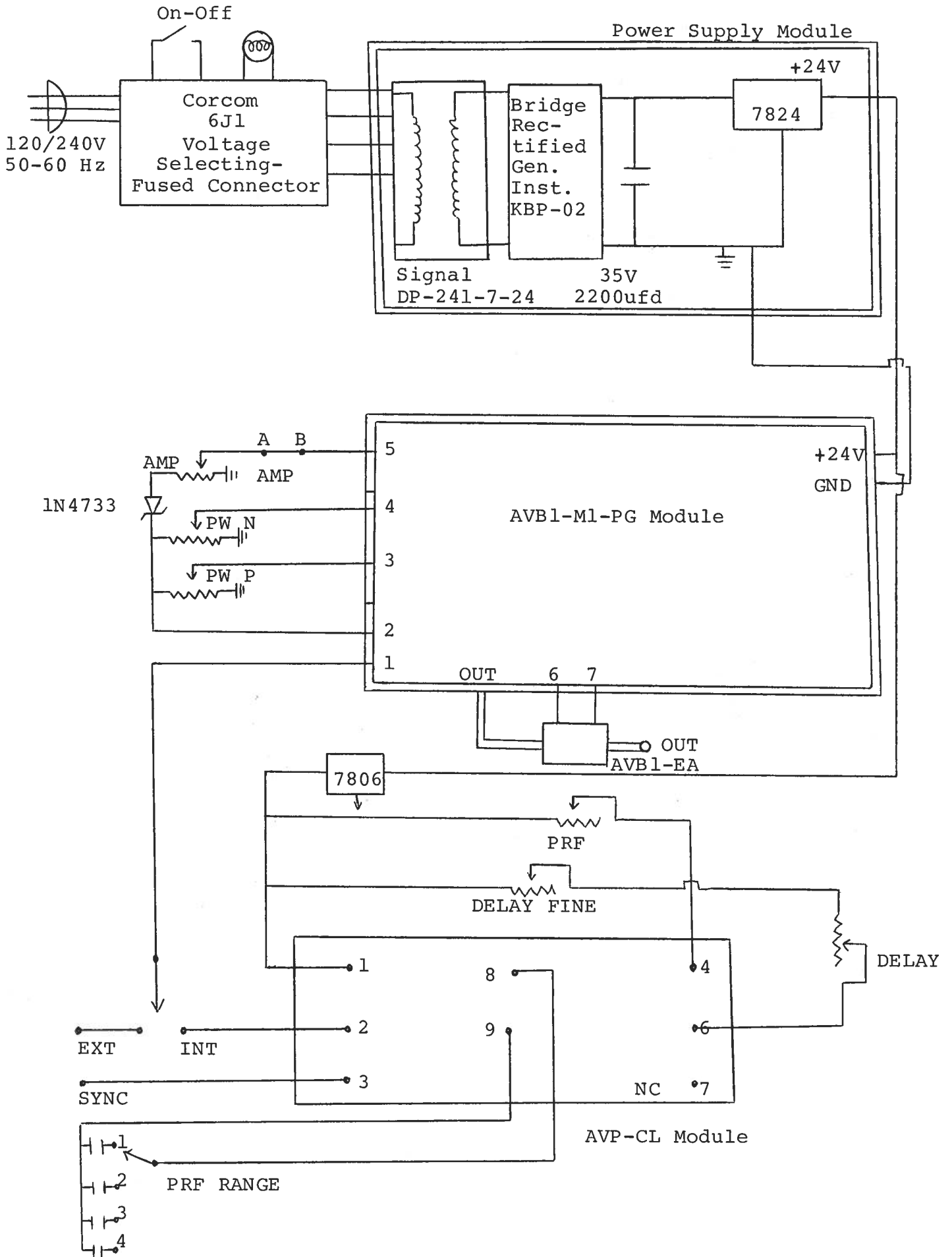


- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse.
- (2) FREQUENCY CONTROL CABLE. The output frequency is controlled primarily by the length of miniature 50 ohm coaxial cable connected between the A and B ports on the instrument back panel. The frequency control cable may be fabricated from RG 174 miniature coax with American 2001 - 7188 connectors (or the equivalent) or from 85 mil semi-rigid cable with American 2001 - 5032 connectors (or the equivalent). The output signal half-period ( $T/2$ ) and cable length ( $L$ ) are related linearly ( $\approx 1.5$  nsec per foot of cable). CAUTION: The center conductor on the frequency control cable is at a DC potential of +130V and so the instrument should be turned off when the cable is to be changed.
- (3) OUTPUT FREQUENCY FINE Control. The output frequency is also slightly dependent on the setting of the PW P and PW N pot controls on the back panel. Clockwise rotation of the pot controls tends to increase the width of positive and negative going segments of the output sinusoid.
- (4) Electronic AMP Control. To voltage control the output amplitude, remove the jumper wire between A and B and apply 0 to +10 volts to connector B ( $R_{IN} \gg 1K$ ).



Fig. 4

SYSTEM BLOCK DIAGRAM



## SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVB1-C consists of a pulse generator module (AVB1-PG), a clock module (AVP-CL) and a power supply board which supplies +24 volts (600 mA max) to the pulse generator module. In the event that the unit malfunctions, remove the instrument cover by removing the four Phillips screws on the back panel of the unit. The top cover may then be slid off. Measure the voltage at the +24 V pin of the PG module. If this voltage is substantially less than +24 volts, unsolder the line connecting the power supply and PG modules and connect 100 ohm 10 W load to the PS output. The voltage across this load should be about +24V DC. If this voltage is substantially less than 24 volts the PS module is defective and should be repaired or replaced. If the voltage across the resistor is near 24 volts, then the PG module should be replaced or repaired. The sealed PG module must be returned to Avtech for repair (or replacement). The clock module provides a 0.1 usec TTL level trigger pulse at Pin 2 to trigger the PG module and a 0.1 usec TTL level sync pulse at Pin 3 to trigger the sampling scope display device. The output at Pin 3 precedes the output at Pin 2 by almost 0 to 100 nsec depending on the DELAY control setting. The clock module is powered by +5.8 V supplied by the PG module (from Pin 2 to Pin 1). With the INT-EXT switch in the EXT position, the clock module is disconnected from the PG module. The clock module is functioning properly if:

- a) 0.1 usec TTL level outputs are observed at Pins 2 and 3.
- b) The PRF of the outputs can be varied over the range of 1 KHz to 1 MHz using the PRF and PRF RANGE controls.
- c) The relative delay between the Pin 2 and 3 outputs can be varied by at least 500 nsec by the DELAY control.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed.

SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The 5VBI-2 consists of a pulse generator module (5VBI-PG1), a clock module (5VBI-CL) and a power supply board which supplies 12V volts (A00 mA max) to the pulse generator module. In the event that the unit malfunctions, remove the instrument cover by removing the four Phillips screws on the rear panel of the unit. The fan cover will then be slid off. Measure the voltage at the 5V pin of the PG module. If this voltage is substantially less than 4.5V volts, unplug the PG module and connect 400 ohm 1/2 watt resistor to the PG output. The voltage across this load should be about 4.5V DC. If this voltage is substantially less than 4.5V volts the PG module is defective and should be repaired or replaced. If the voltage across the resistor is near 4.5V volts, then the PG module should be replaced by a new one. The sealed PG module will be returned to 5VBI-2 for repair or replacement. The clock module provides a 0.1 uSec TTL level trigger pulse at Pin 2 to trigger the PG module and a 0.1 uSec TTL level sync pulse at Pin 3 to trigger the sampling scope display device. The output at Pin 1 provides the output at Pin 2 by almost 0 to 100 nSec depending on the DELAY control setting. The clock module is powered by 5.0V supplied by the PG module from Pin 2 to Pin 11. With the INT-EXT switch in the EXT position, the clock module is disconnected from the PG module. The clock module is functionally provided by:

- a) 0.1 uSec TTL level outputs are observed at Pins 2 and 3.
  - b) The PRF of the outputs can be varied over the range of 1 kHz to 1 MHz using the PRF and PRF RANGE controls.
  - c) The relative delay between the Pin 2 and 3 outputs can be varied by at least 20 nSec by the DELAY control.
- The sealed clock module must be returned to 5VBI-2 for repair or replacement if the above conditions are not observed.