



AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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INSTRUCTIONS

MODEL AVD-1000-C-F5 MONOCYCLE GENERATOR

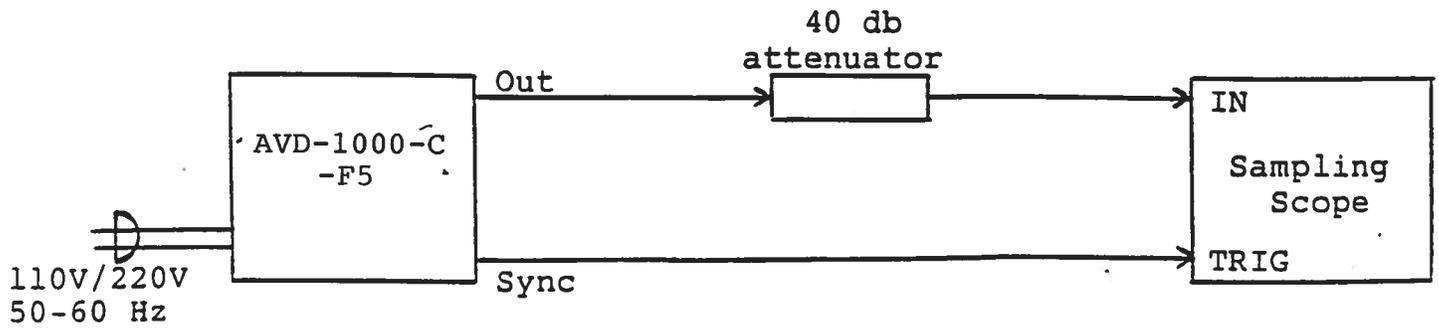
S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

Fig. 1

MONOCYCLE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 40 dB attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one Volt.
- 3) The TRIG output channel provides a 500 mV 20 ns wide pulse to fifty Ohms. The sampling scope should be set to trigger on the positive edge of the TRIG pulse.
- 4) To obtain a stable output display the front panel TRIG toggle switch should be in the INT position. The DELAY control and the scope triggering controls are then adjusted to obtain a stable output.
- 5) An external clock may be used to control the output PRF of the AVD unit by setting the front panel TRIG toggle switch in the EXT position and applying a 0.2 us (approx.) TTL level pulse to the TRIG BNC connector input.
- 6) The unit can be converted from 110 to 220V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
- 7) For additional assistance:

Tel: (613) 226-5772

Fax: (613) 226-2802

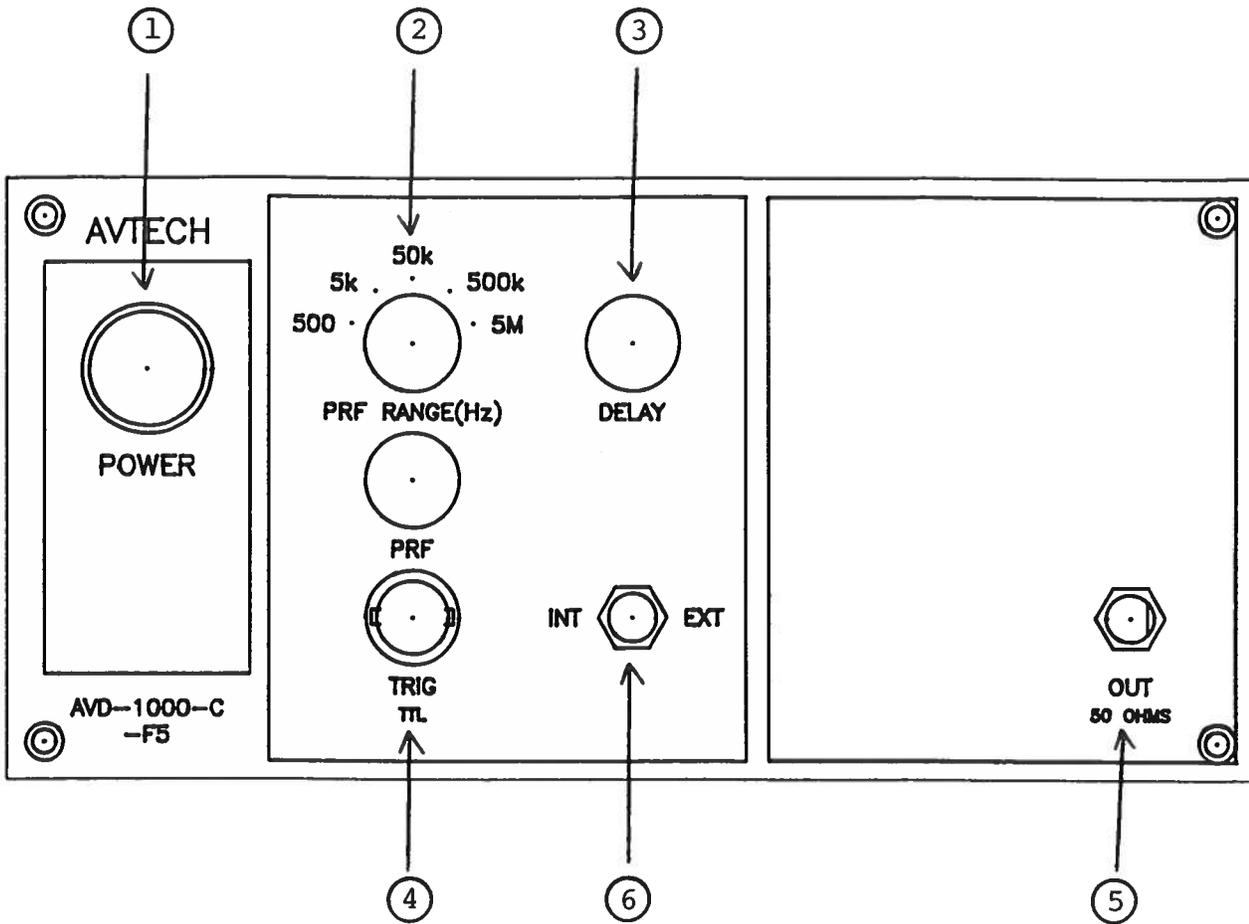


Fig. 2

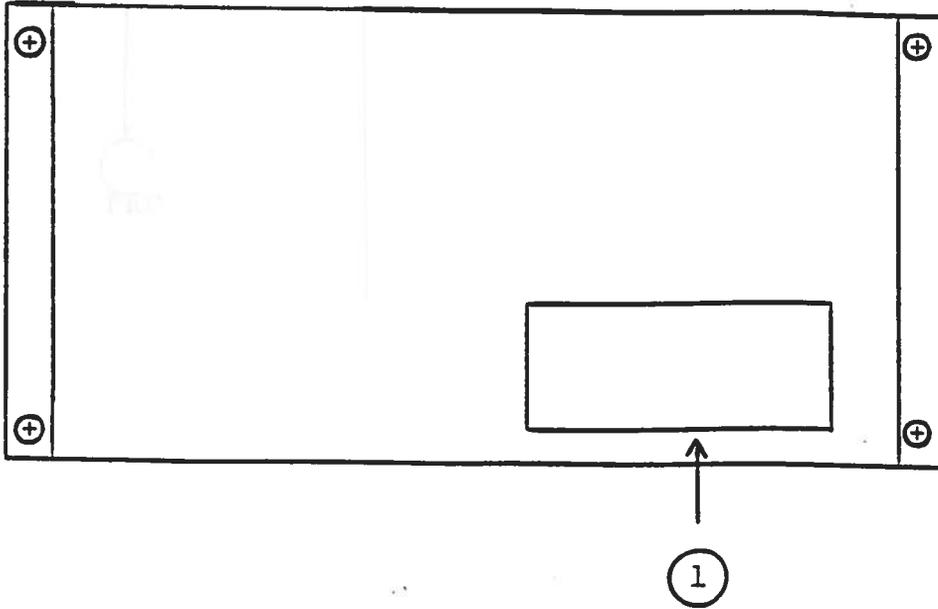
FRONT PANEL CONTROLS

- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. Varies PRF from 50 Hz to 5 MHz as follows:

Range 1	50 Hz	to	500 Hz
Range 2	500 Hz	to	5 kHz
Range 3	5 kHz	to	50 kHz
Range 4	50 kHz	to	500 kHz
Range 5	500 kHz	to	5 MHz
- (3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (5). This delay is variable over the range of 0 to about 150 ns.
- (4) TRIG Output. This output precedes the main output (5) and is used to trigger the scope time base. The output is a 500 mV 20 ns (approx.) pulse capable of driving a fifty Ohm load. Set scope to trigger on positive edge. The external trigger signal is applied at this input when the EXT-INT toggle switch is in the EXT position.
- (5) OUT Connector. SMA connector provides output to a fifty Ohm load.
- (6) EXT-INT Control. With this toggle switch in the INT position, the PRF of the AVD unit is controlled via an internal clock (fixed at 5.0 MHz). With the toggle switch in the EXT position, the AVD unit requires a 0.1 us TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.

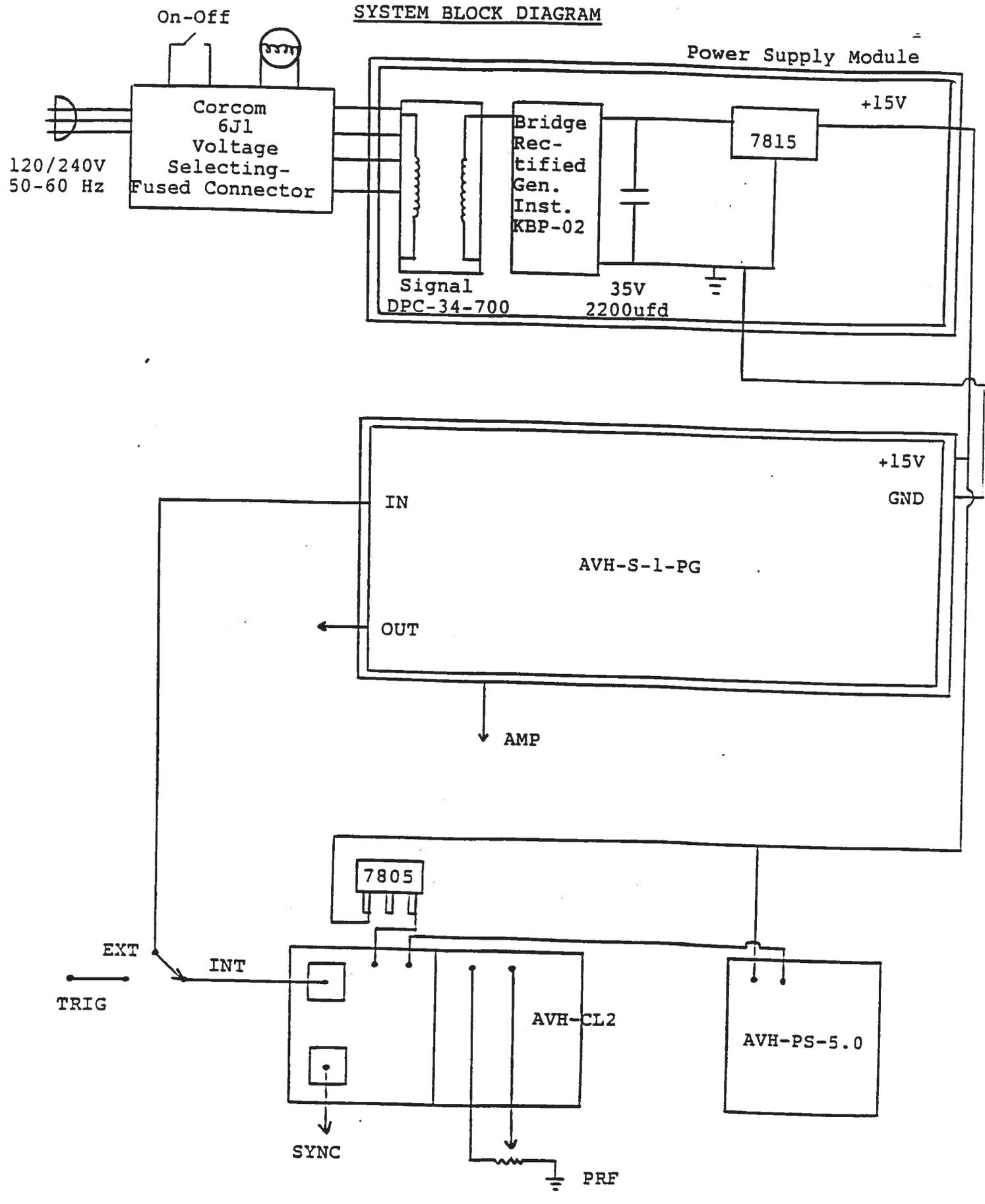
Fig. 3

BACK PANEL CONTROLS



- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.25 A SB).

SYSTEM BLOCK DIAGRAM



SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVD-C consists of a pulse generator module (AVD-C) a clock module (AVD-CL2) and a power supply board which supplies +15 Volts (600 mA max) to the pulse generator module. In the event that the unit malfunctions, remove the instrument cover by removing the four Phillips screws on the back of the unit. The top cover may then be slid off. Measure the voltage at the +15V pin of the PG module. If this voltage is substantially less than +15 Volts, unsolder the line connecting the power supply and PG modules and connect 50 Ohm 10 W load to the PS output. The voltage across this load should be about +15 V DC. If this voltage is substantially less than 15 Volts the PS module is defective and should be repaired or replaced. If the voltage across the resistor is near 15 Volts, then the PG module should be replaced or repaired. The sealed PG module must be returned to Avtech for repair (or replacement). The clock module provides a 0.1 us TTL level trigger pulse at pin 2 to trigger the PG module and a 0.1 us TTL level sync pulse at pin 3 to trigger the sampling scope display device. The output at pin 3 precedes the output at pin 2 by almost 0 to 100 ns depending on the DELAY control setting. The clock module is powered by +15V. With the INT-EXT switch in the EXT position, the clock module is disconnected from the PG module. The clock module is functioning properly if:

- a) 0.1 us TTL level outputs are observed at pins 2 and 3.
- b) The PRF of the outputs can be varied over the range of 5 MHz using the PRF control.
- c) The relative delay between the pin 2 and 3 outputs can be varied by at least 100 ns by the DELAY control.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed.

July 27/95

Disk: AVC, AVD, AVE

Name: D1000CF5.INS