



AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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INSTRUCTIONS

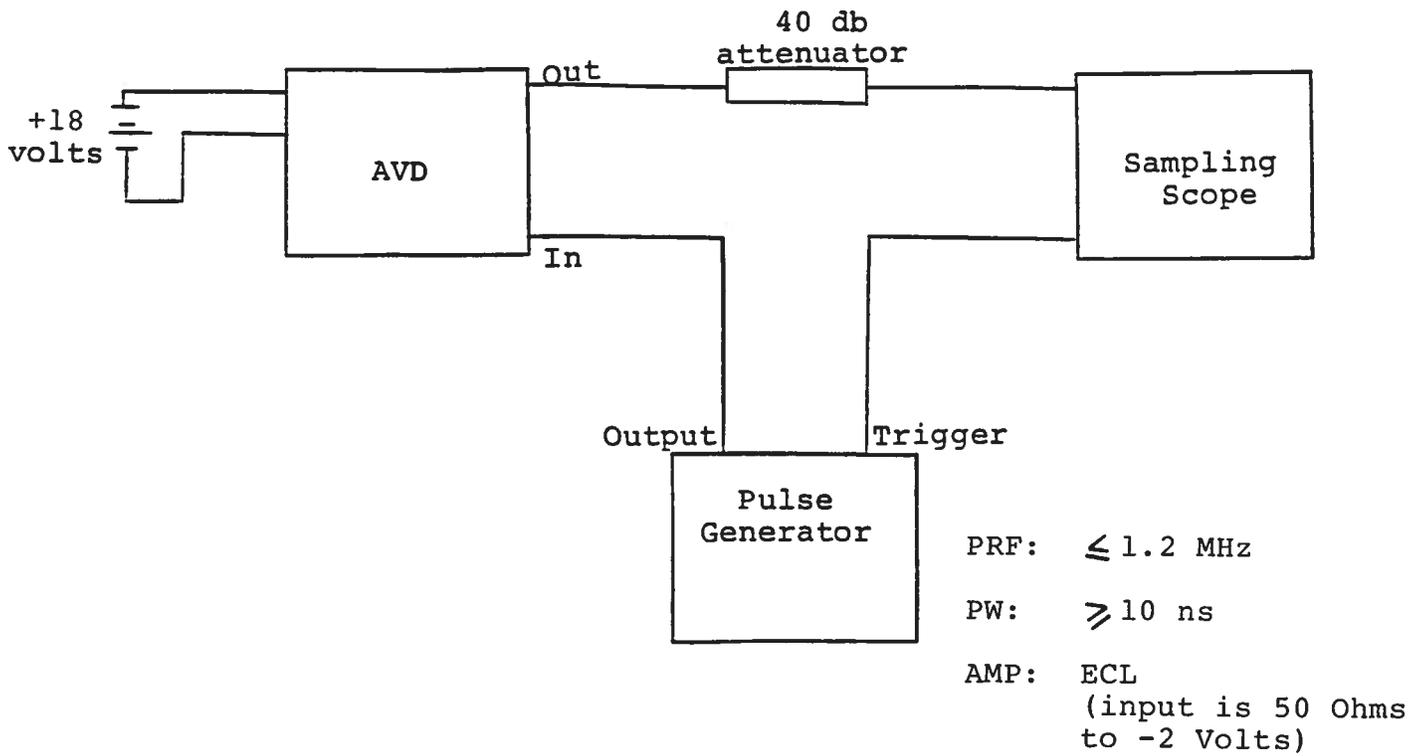
MODEL AVD-5000-BAT3 MONOCYCLE GENERATOR

S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MONOCYCLE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the monocycle generator output signal (attenuators, cables, connectors, etc.) should exceed 10 GHz.
- 2) The use of a 40 dB attenuator will insure a peak input signal to the sampling scope of less than one Volt.
- 3) In general, the pulse generator delay control should be set in the 100 ns range. Other settings should be as shown in the above diagram. The monocycle generator output is delayed with respect to the trigger input signal by about 50 ns (typically).
- 4) The monocycle generator can withstand an infinite VSWR on the output port.
- 5) The output frequency can be adjusted (slightly) by rotating the front panel 10 turn trim pot. Clockwise rotation reduces the output frequency.

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Fax Ref No: 776 From: Avtech Electrosystems Ltd.

To: Battelle Our Fax No: (613) 226-2802

Date: August 22, 1996

Attn: R. Parks Gribble Receivers Fax No: 509-375-3621
Tel: 509-375-2238

Subject: Quotation No. of pages: 2

Following our telephone conversation of August 21st, I am pleased to offer the following price and delivery quotation:

Model designation: AVD-5000-BAT3.

Output waveform: 5.0 GHz monocycle.

Output amplitude: ≥ 4 Volts peak to peak (to 50 Ohms).

Baseline offset: 0.0 Volts.

PRF: 256 kHz to 1.024 MHz (equals input trigger PRF).

Baseline spurs or perturbations: < 26 dB below peak from 500 ps to 20 ns.

Input trigger: ECL compatible, PW ≥ 10 ns.

Input trigger impedance: 50 Ohms.

Propagation delay:

≤ 50 ns. Will employ temperature compensation in an attempt to maintain delay drift to less than 5 ns over the temperature range of 0 to +55°C. Note that this is an exploratory effort and we do not guarantee success. Note that this temperature compensation feature significantly increases the price of the unit.

Jitter:

< 16 ps (RMS).

Prime power:

+18 VDC, 350 mA (max).

Package:

1.7" x 3.0" x 6.0" cast aluminum with blue enamel. Four 4-40 tapped mounting holes on bottom face (3.0" x 6.0").

Input connectors:

a. Prime power:

Solder pin feed through with solder pin ground, located on one end of Pomona box (1.7" x 3.0").

b. Trigger input:

Female SMA, located same end as prime power.

Output connector:

Female SMA, located on opposite end of the input connectors.

Price:

\$2,698.00 US each, FOB destination.

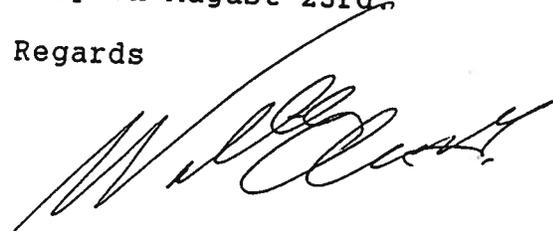
Delivery:

60 days ARO.

Thank you for your continuing interest in our products. Please call me again (1-800-265-6681) if you require any additional information.

Please note that SN 7199 has been repaired and will be ready to ship on August 23rd.

Regards



Dr. Walter Chudobiak
Chief Engineer

WC:pr

Feb 10/97

Disk: AVC, AVD, AVE

Name: DS000BAT.INS.