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SINCE 1975

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INSTRUCTIONS

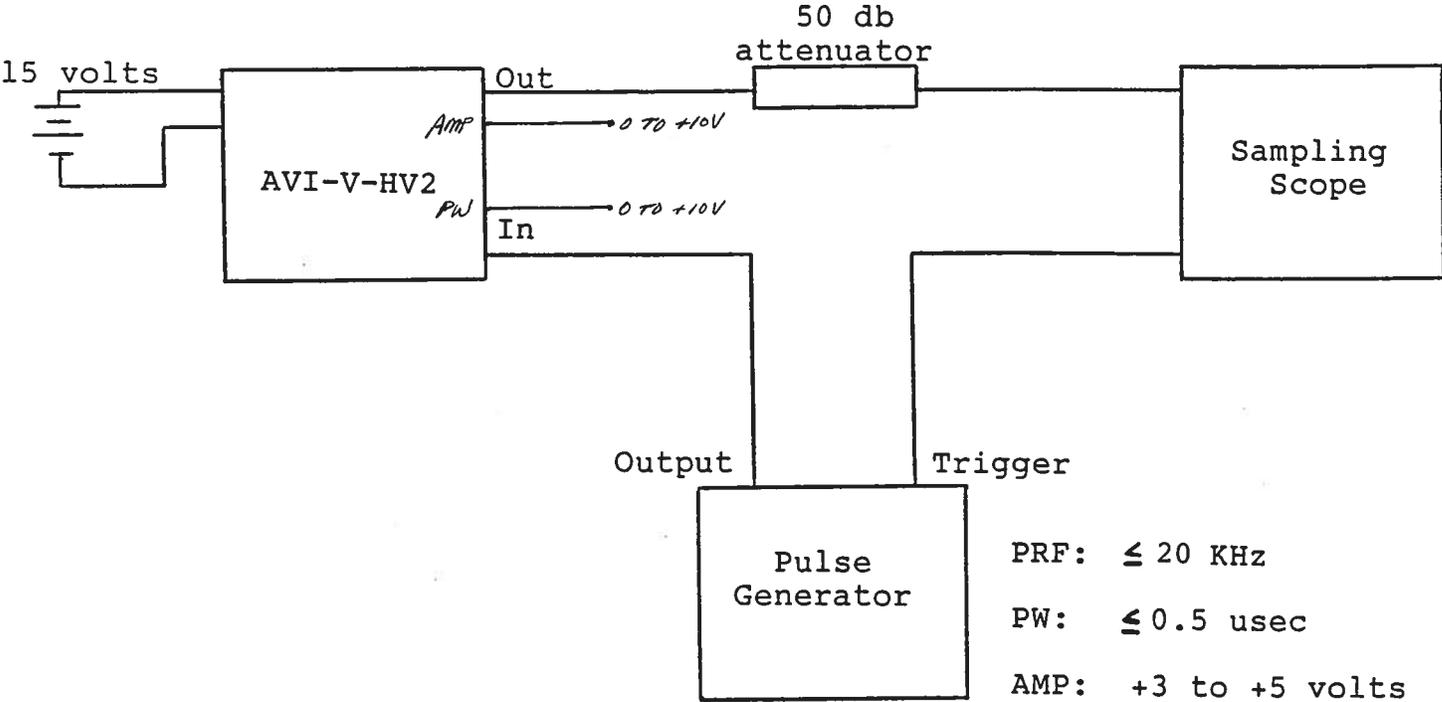
MODEL AVI-V-HV2-EA-EW-CEA PULSE GENERATOR

S.N.:

## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVI-V-HV2 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed 10 gigahertz.
- 2) The use of a 50 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec. range. Other settings should be as shown in the above diagram.
- 4) The Model AVI-V-HV2 pulse generator can withstand an infinite VSWR on the output port.
- 5) WARNING: Model AVI-V-HV2 may fail if triggered at a PRF greater than 20 KHz.
- 6) The output pulse width is controlled by applying 0 to +10 VDC to the PW solder terminal ( $R_{IN} \geq 10K$ ).
- 7) The output amplitude is controlled by applying 0 to +10 VDC to the AMP solder terminal ( $R_{IN} \geq 10K$ ).

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Notes

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed 10 percent.
- 2) The use of a 20 db attenuation will reduce the peak input signal to the sampling scope or less than one volt.
- 3) In general, the narrow pulse generator trigger delay control should be set in the 0.1 to 1.0 msec. range. Other settings should be as shown in the above program.
- 4) The Model AV1-VHVS pulse generator can withstand an infinite VSWR on the output port.
- 5) WARNING: Model AV1-VHVS may fail if triggered at a rate greater than 50 kHz.
- 6) The output pulse width is controlled by applying 0 to +10 VDC to the PW solder terminal (P14 x 10K).
- 7) The output amplitude is controlled by applying 0 to +10 VDC to the AMR solder terminal (P14 x 1K).