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INSTRUCTIONS

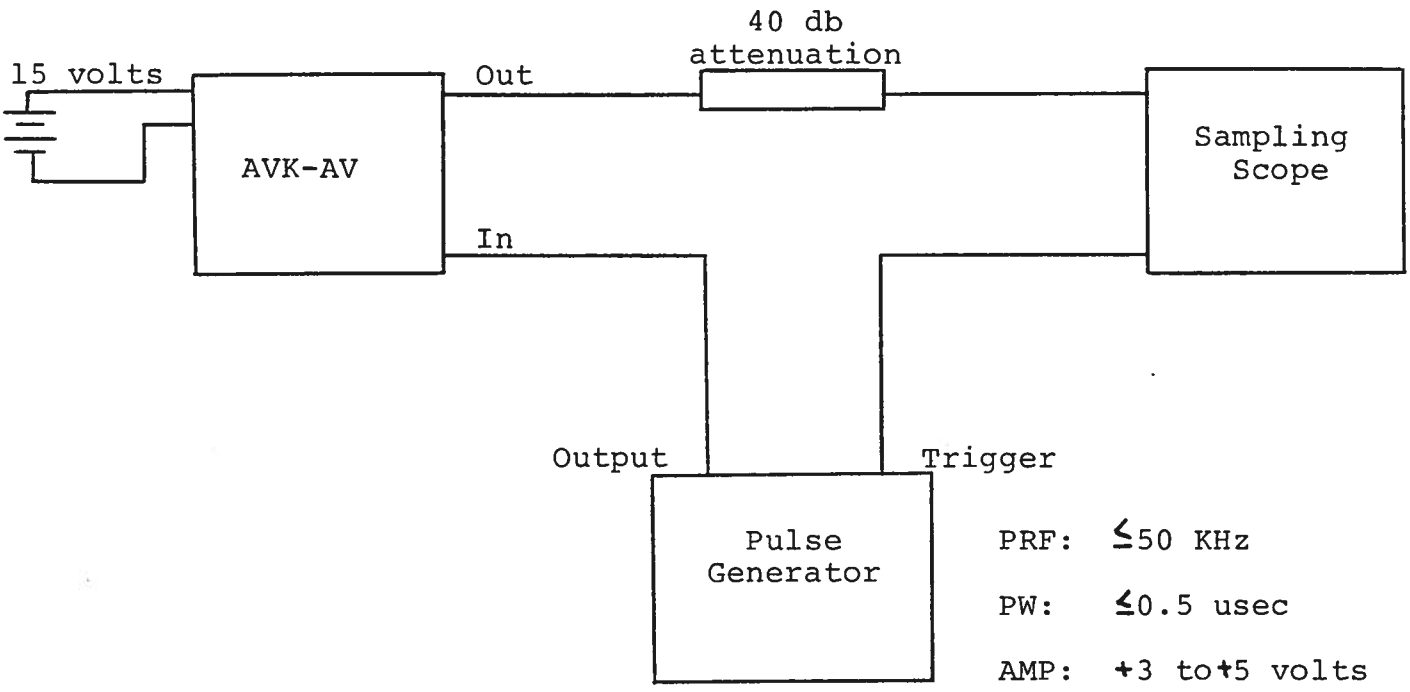
MODEL AVK-AV-EW PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVK-AV PULSE GENERATOR TEST ARRANGEMENT



Notes:

1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.

2) The use of a 40 db attenuator will insure a peak input signal to the sampling scope of less than one volt.

3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec. range. Other settings should be as shown in the above diagram.

4) The Model AVK-AV pulse generator can withstand an infinite VSWR on the output port.

5) WARNING: Model AVK-AV may fail if triggered at a PRF greater than 50 KHz.

6) The output pulse width is controlled by means of 0 to +10 volts applied to the front panel PW solder terminal ($R_{IN} \geq 10K$). (The PW control is typically active over the range of +3.0 to +8.5 volts).

7) The output pulse amplitude is controlled by means of the one-turn potentiometer (AMP). The pulse width may reduce by several nanoseconds as the output amplitude is reduced from maximum to minimum.

8) Some properties of the output pulse may change as a function of the amplitude pot setting. For some demanding applications, it may be desirable to use a combination of external attenuators and the amplitude pot to achieve the desired output amplitude.

9) For models equipped with the DC offset option, the required DC output offset voltage is applied to the OS terminals (max voltage $\pm$50 volts).

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-OS