

AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
ENGINEERING - MANUFACTURING

□ P.O. BOX 265
OGDENSBURG
NEW YORK
13669
(315) 472-5270

□ BOX 5120, STN. "F"
OTTAWA, ONTARIO
CANADA K2C 3H4
TEL: (613) 226-5772
FAX: (613) 226-2802
TELEX: 053-4591

INSTRUCTIONS

MODEL AVL-2A-C-BSC1 PULSE GENERATOR

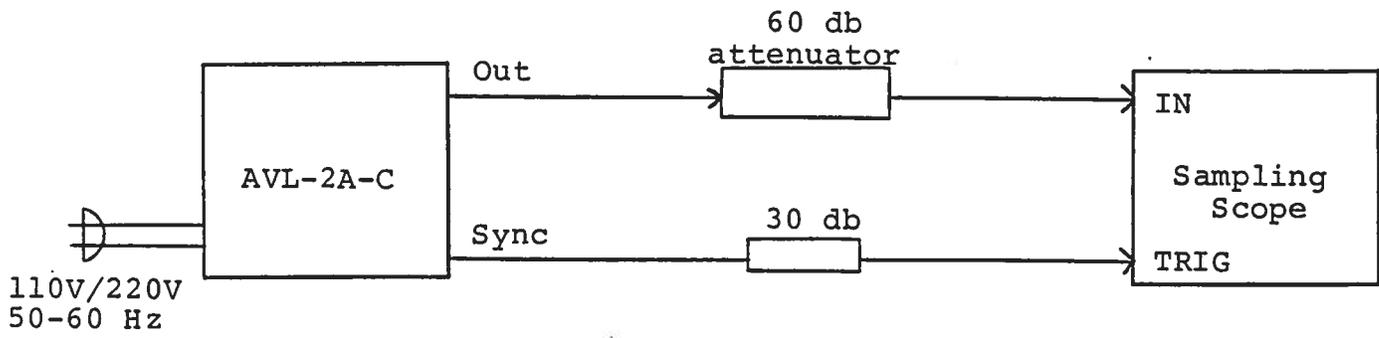
S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

Fig. 1

PULSE GENERATOR TEST ARRANGEMENT



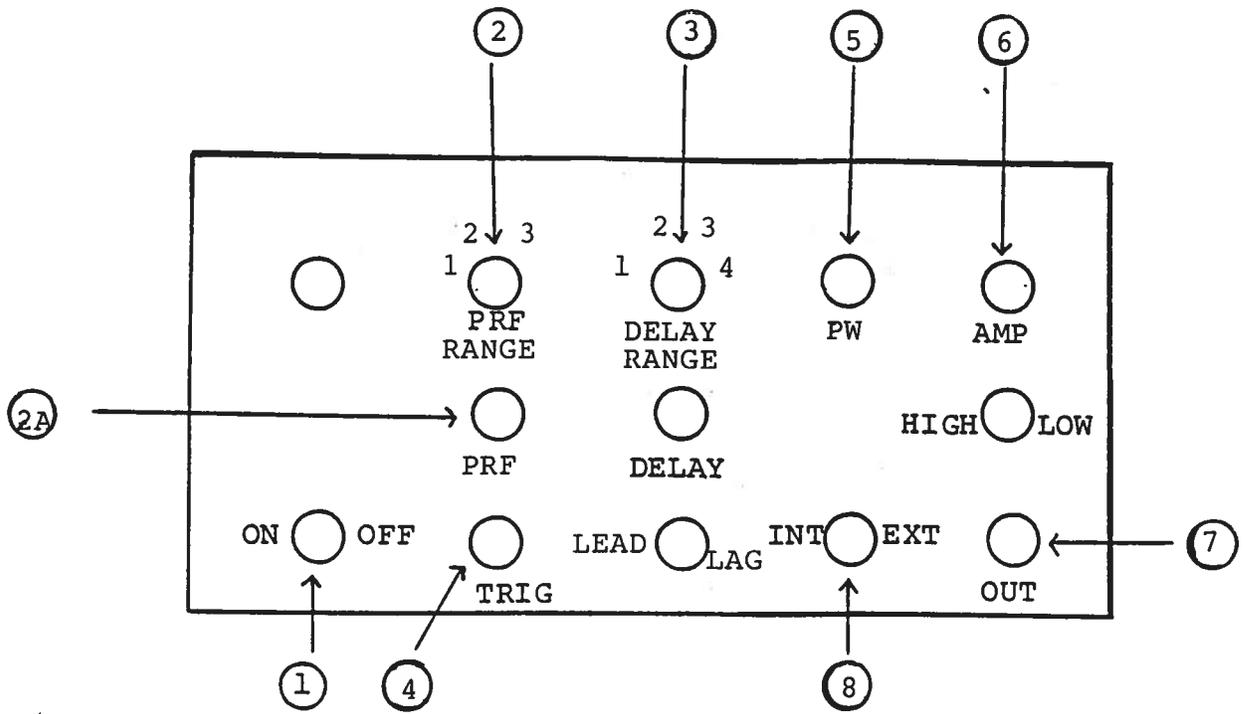
Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
- 2) The use of 60 db attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one volt.
- 3) The TRIG output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some scopes, a 30 db attenuator should be placed at the input to the scope trigger channel. The TRIG output precedes the main output when the front panel LEAD-LAG switch is in the LEAD position. The TRIG output lags the main output when the switch is in the LAG position.
- 4) To obtain a stable output display the PW and PRF controls on the front panel should be set mid-range. The front panel TRIG toggle switch should be in the INT position. The DELAY controls and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF controls.
- 5) The output pulse width is controlled by means of the one turn potentiometer (PW). The pot should initially be set mid-range and the pulse width adjusted using an oscilloscope. The output will degenerate to an impulse and eventually vanish as the pot is turned fully counter clockwise. Units with the -W option are shipped with an 8 inch length of RG-58 cable connected between ports A and B on the back panel. This cable provides a maximum PW of about 100 nsec. The PW pot control is active over about 90° with this cable. The maximum PW may be extended up to 400 nsec by attaching RG-58 cable lengths of about 100 feet between ports A and B. With 100 feet of cable attached, the PW pot control is active over about 300°. The maximum attainable PW varies linearly with cable length for cable lengths between 6" and 100 feet. CAUTION: Insure that the input power is off before attempting to change the cable between ports A and B. Potentials as high as 400V exist on the centre conductor between ports A and B.
- 6) The output pulse amplitude is controlled by means of the one turn potentiometer (AMP) and the HIGH-LOW switch adjacent to the AMP control. With the switch in the HIGH position, the output amplitude is variable over the range of 75 to 170 volts while in the LOW position the output amplitude is variable over the range of about 0 to 150 volts.

- 7) An external clock may be used to control the output PRF of the AVL unit by setting the front panel TRIG toggle switch in the EXT position and applying a 0.2 usec (approx.) TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the SYNC output. **WARNING:** Model AVL-2A may fail if triggered at a PRF greater than 20 KHz.
- 8) Model AVL-2A can withstand an infinite VSWR on the output port but is intended for operation with 50 ohm loads.
- 9) **NOTE:** The lifetime of the switching elements in the pulse generator module is proportional to the running time of the instrument. For this reason the prime power to the instrument should be turned off when the instrument is not in use. In the case of failure, the switching elements are easily replaced following the procedure described in the following section.

Fig. 2

FRONT PANEL CONTROLS



- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. With the PRF range switch in 1 position, PRF control will vary PRF from 20 Hz to about 200 Hz. With the PRF range switch in 2 position, varies PRF from about 200 Hz to about 2 KHz. With the PRF range switch in the 3 position, varies PRF from about 2 KHz to 20 KHz. The operating PRF should be set using a scope.
- (3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (7). This delay is variable as follows:

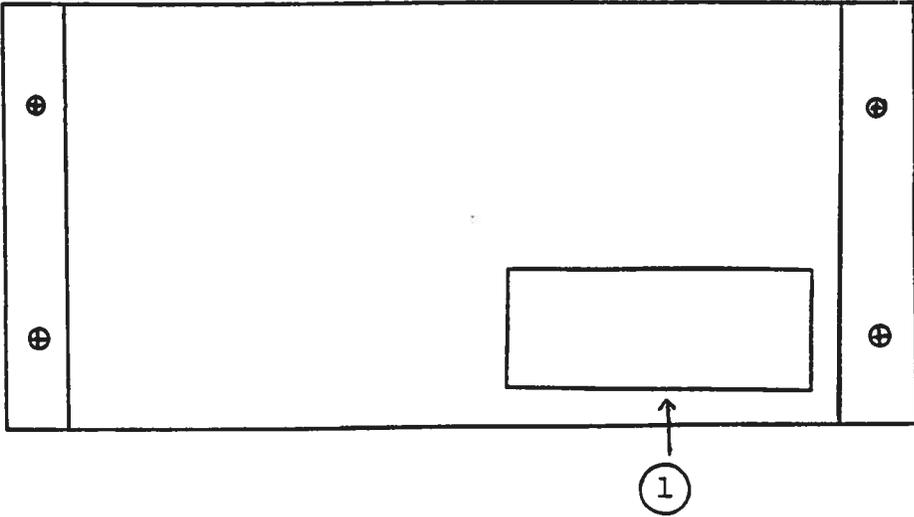
RANGE 1	0 to 1.3 us
RANGE 2	0.5 to 2.5 us
RANGE 3	1.3 to 4.2 us
RANGE 4	4.0 to 12 us

The TRIG output precedes the main output when the LEAD-LAG switch is in the LEAD position and lags when the switch is in the LAG position.

- (4) TRIG Output. This output precedes the main output (7) and is used to trigger the sampling scope time base. The output is a TTL level 100 nsec (approx.) pulse capable of driving a fifty ohm load. The external trigger signal is applied at this input when the EXT toggle switch is in the EXT position. The output pulse at (7) appears about 350 nsec after the application of the TRIG pulse.
- (5) PW Control. A one-turn control which varies the output pulse width from 0 to 100 nsec (to 400 nsec for -W option).
- (6) AMP Control. The output pulse amplitude is controlled by means of the one-turn potentiometer (AMP) and the HIGH-LOW switch adjacent to the AMP control. With the switch in the HIGH position, the output amplitude is variable over the range of 75 to 170 volts while in the LOW position the output amplitude is variable over the range of about 0 to 150 volts.
- (7) OUT Connector. N connector provides output to a fifty ohm load.
- (8) EXT-INT Control. With this toggle switch in the INT position, the PRF of the AVL unit is controlled via an internal clock which in turn is controlled by the PRF controls. With the toggle switch in the EXT position, the AVL unit requires a 0.2 usec TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.

Fig. 3

BACK PANEL CONTROLS



- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.25 A SB).

REPAIR PROCEDURE

- 1) **WARNING:** Before attempting any repairs, note that potentials as high as 400 volts are employed in the chassis structure.
- 2) The pulse generator is constructed from the following basic subsystems or modules:
 - a) Metal chassis
 - b) Pulse generator module (AVL-2A-PG)
 - c) Power supply module (AVL-2A-PS)
 - d) Clock module (AVL-2A-CL)
 - e) Power supply board

The three modules are interconnected as shown in Fig. 1.

- 3) If no output pulse is provided by the AVL-2A unit, turn off the prime power supply and remove the top cover panel by removing the four Phillips screws on the back side of the instrument. The top cover will then slide out to expose the interior. Apply a scope probe or voltmeter to pin 6 of the AVL-2A-PG unit. Turn on the prime power supply. A voltage of about 360 to 380 volts should be read at pin 1. If the voltage is zero or much less than 380 volts, then one of the switching transistors (Part No. SL3) in the AVL-2A-PG module has probably failed. **WARNING:** The cases of the transistors are at potentials as high as 380 volts. With the prime power supply off remove one of the transistors by removing the two 2-56 screws which secure the transistor in its socket. Pull the transistor out of the socket. With the unit un-triggered turn on the prime power supply and measure the voltage from the case of the remaining transistor to ground. If the voltage is about 360 to 380 volts then the transistor which was removed is defective and should be replaced. If the voltage which is measured is less than 360 volts then the transistor still in position is defective and should be replaced. Note that the two transistors are completely interchangeable (Order Avtech Part No. SL3). Note that with both transistors removed, the voltage at pin 1 on the AVL-2A-PG module should be in the range of 360 to 380 volts. If the voltage is less then the AVL-2A-PS module must be replaced.

The AVL-2A-CL module is functioning properly if:

- a) The TTL level output at pin 2 varies from 50 Hz to 5 KHz as the PRF control is varied.
- b) The relative delay between the output at pins 2 and 3 varies by about 100 to 200 nsec as the DELAY control is varied.

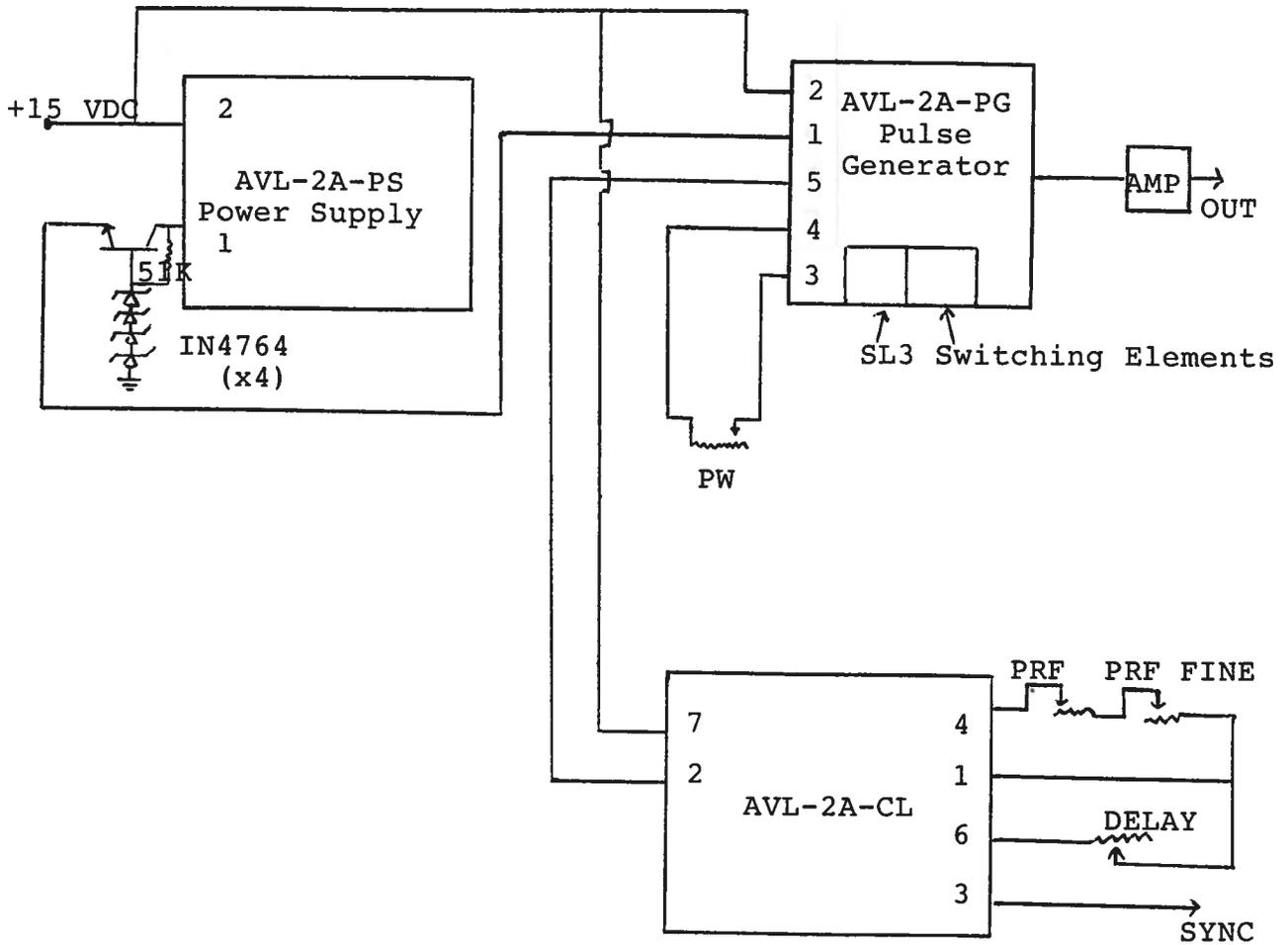


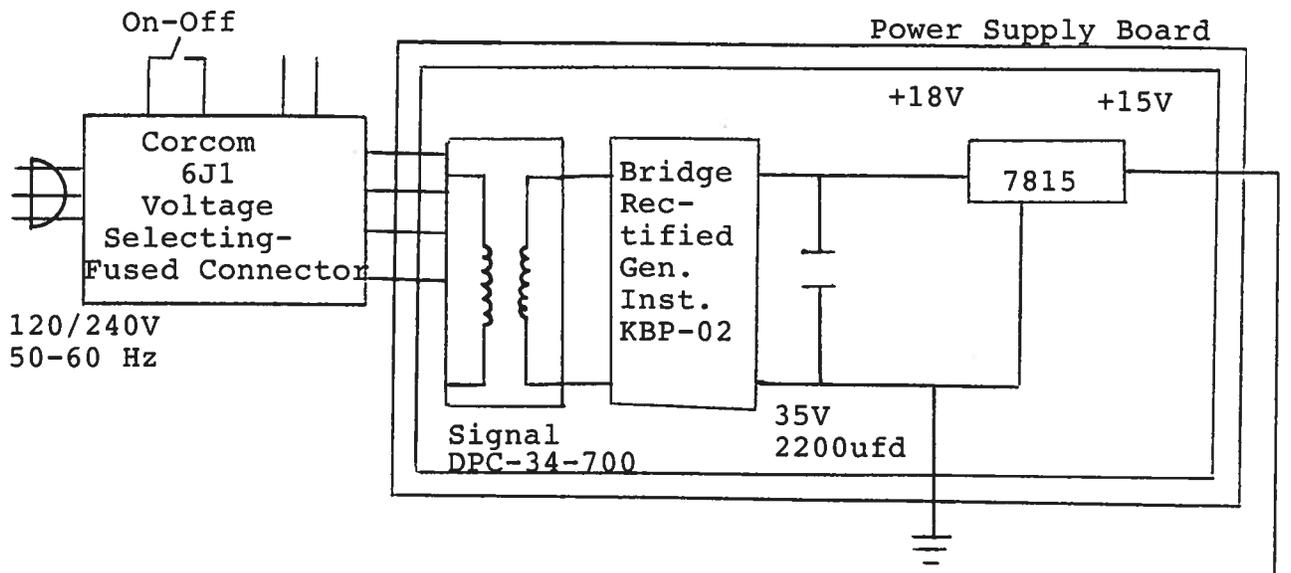
Fig. 1

Notes:

- 1) All module chassis are grounded to main chassis and to each other via separate ground lines.
- 2) **WARNING:** The line connecting pin 1 of AVL-2A-PS to pin 1 of AVL-2A-PG is a potential of 375 to 400 volts.

LINE POWERED AVL-2A SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVL-2A-C consists of the three standard modules and a power supply board which supplies +15 volts (600 mA max) to the pulse generator modules. In the event that the AVL-2A unit malfunctions, remove the instrument cover by removing the two screws on each side of the unit, thereby exposing the modules. Measure the voltage at the +15 V pin of the PG module. If this voltage is substantially less than +15 volts, unsolder the line connecting the power supply board and PG module and connect a 50 ohm 10 W load to the power supply output. The voltage across this load should be about 15 V DC. If this voltage is substantially less than 15 volts the power supply board is defective and should be repaired or replaced. If the voltage is near +15V then see instructions in preceding section.



To Fig. 1

Schroff

12.04.90

-W