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INSTRUCTIONS

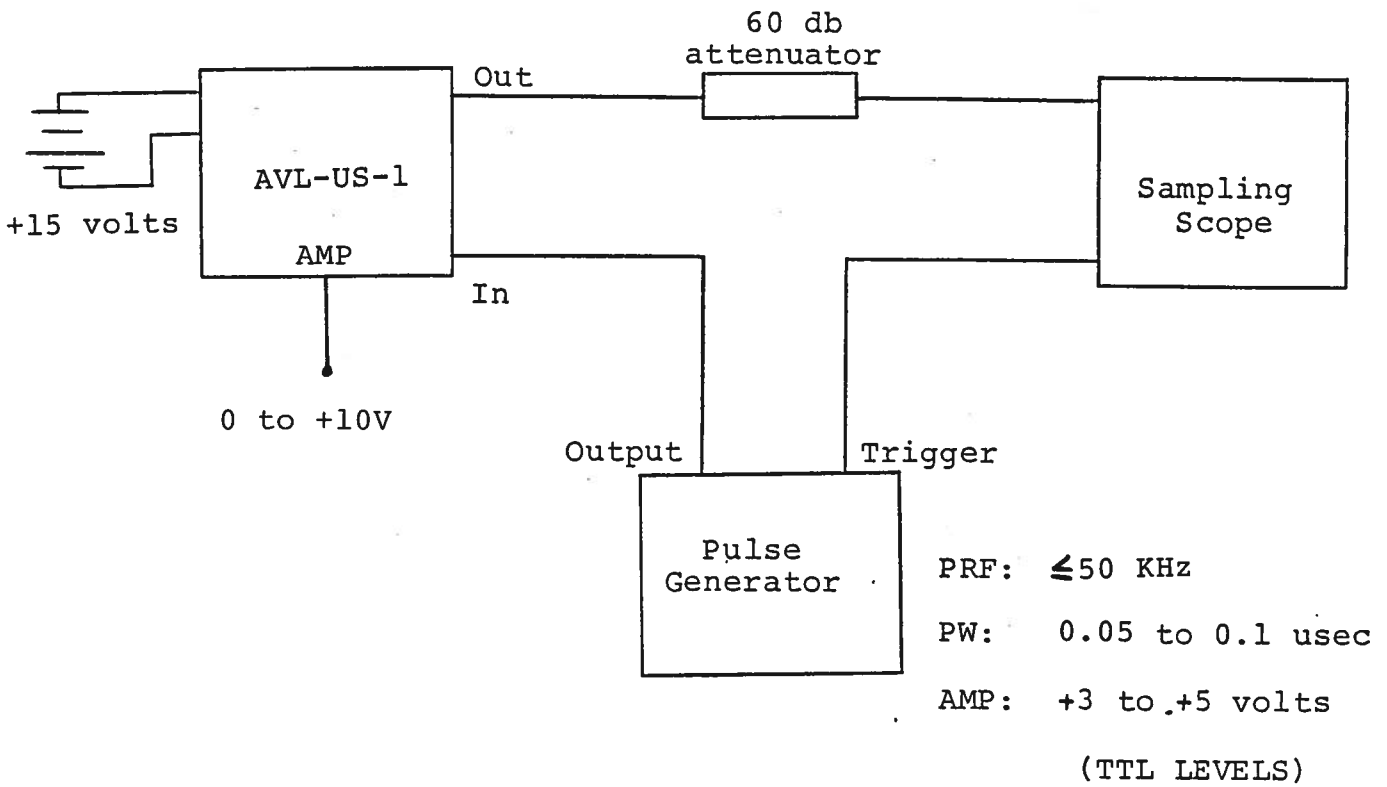
MODEL AVL-US-1 IMPULSE GENERATOR

S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVL-US-1 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the impulse generator output signal (attenuators, cables, connectors, etc.) should exceed several gigahertz.
- 2) The use of 60 db attenuators will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the pulse generator trigger delay control should be set in the 100 nsec range. Other settings should be as shown in the above diagram. The AVL pulse generator output is delayed with respect to the trigger input signal by about 50 nsec. (typically).
- 4) The pulse generator can withstand an infinite VSWR on the output port.
- 5) The output amplitude is controlled by 0 to +10 volts applied to the AMP solder terminal ($R_{IN} \gg 10K$).

-EA

-TP

Notes:

- 1) The bandwidth capability of components and instruments used to display the impulse generator output signal (attenuators, cables, connectors, etc.) should exceed several gigahertz.
- 2) The use of 60 db attenuators will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the pulse generator trigger delay control should be set in the 100 nsec range. Other settings should be as shown in the above diagram. The AVL pulse generator output is delayed with respect to the trigger input signal by about 20 nsec. (typically).
- 4) The pulse generator can withstand an infinite VSWR on the output port.
- 5) The output amplitude is controlled by 0 to +10 volts applied to the AMP solder terminal (Rin > 10K).