AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS ENGINEERING . MANUFACTURING



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INSTRUCTIONS

MODEL AVL-US-1 IMPULSE GENERATOR

S.N.:

MODEL AVL-US-1 PULSE GENERATOR TEST ARRANGEMENT

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Notes:

- The bandwidth capability of components and instruments used to display the impulse generator output signal (attenuators, cables, connectors, etc.) should exceed several gigahertz.
- The use of 60 db attenuators will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the pulse generator trigger delay control should be set in the 100 nsec range. Other settings should be as shown in the above diagram. The AVL pulse generator output is delayed with respect to the trigger input signal by about 50 nsec. (typically).
- 4) The pulse generator can withstand an infinite VSWR on the output port.
- 5) The output amplitude is controlled by 0 to +10 volts applied to the AMP solder terminal $(R_{IN} > 10K)$.

AVTECH ELECTROSYSTEMS LTD. Ref. 1778									
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	То:		Universidade de Sao Paulo,		Your Ref No:				
			Instituto de Fisica, Brasil		Our Ref:				
Attn: From:		1:	Dr. Marcos N. Martins Avtech Electrosystems Ltd.		Date:	e: March 11, 1991		L	
		a :			Receivers Fax No:	011 55	11 814-	-0503	
Subject:		ject:	Your fax March 11/91		No. pages faxed:	1			
The specs for the two models are as follows:									
	1)	Mode Outr PW: Rise Fall PRF: Prin Tric	el designation: put: e time: time: me power: gger required:	AVL-US1. 0 to +100 volts Fixed at 10 ns. \leq 1.0 ns. \leq 2 ns. 0 to 20 KHz. +15 volts. TTL 0.5 us.	to 50 ohms	5.			
	2) Model designation: Output amplitude: Output PW: Propagation delay: Prime power: IN, OUT connectors: Chassis: Trigger required:			AVL-US1-EA-TP. 0 to +100 volts to 50 ohms. Controlled by 0 to +10 volts control voltage. Fixed at 10 ns with 1 ns TR and 2 ns TF. ≤ 50 ns. Constant within ±100 ps over full amplitude range. +15 volts 350 mA max. SMA. Package Style A. TTL 0.5 us.					
	Thank you for your interest in our products. Please contact me again if you require any additional information.								
	Rgds								

Walter J. Chudobiak Chief Engineer

WJC:pr