



AVTECH ELECTROSYSTEMS LTD.  
NANOSECOND WAVEFORM ELECTRONICS

---

P.O. BOX 265  
OGDENSBURG, NY  
U.S.A. 13669-0265  
TEL: (315) 472-5270  
FAX: (613) 226-2802

BOX 5120 STN. F  
OTTAWA, ONTARIO  
CANADA K2C 3H4  
TEL: (613) 226-5772  
FAX: (613) 226-2802

INSTRUCTIONS

MODEL AVMH-4-F-BNWA PULSE GENERATOR

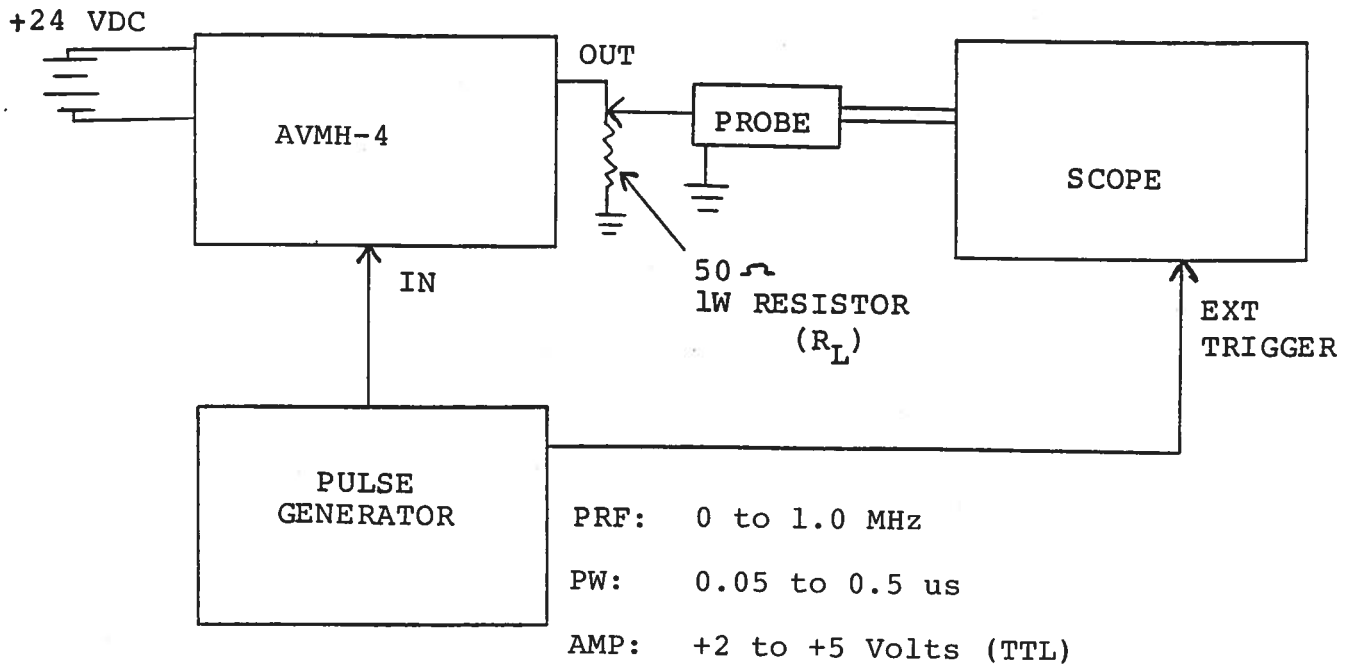
S.N. :

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

A.

TEST ARRANGEMENT



B.

GENERAL OPERATING INSTRUCTIONS

- 1) The equipment should be connected in the general fashion shown above. Since the AVMH unit provides an output pulse rise time as low as 1.0 ns a fast oscilloscope (at least 1 GHz) should be used to display the waveform. Note also that the unit requires a 50 ohm load.
- 2) The magnitude of the output pulse is controlled by the front panel one turn pot (AMP). Maximum clockwise rotation of the pot provides the maximum output. For units with the EA option, the output amplitude is controlled by 0 to +10 volt applied to the AMP solder terminal ( $R_{IN} \geq 10K$ ). Note that the amplitude and pulse width interact. For example, decreasing the amplitude causes the pulse width to increase. Therefore, it is normally most convenient to first set the desired amplitude and then set the desired pulse width.
- 3) The output pulse width is controlled by the one turn PW pot. For units with the EW option, the output pulse width is controlled by 0 to +10 volt applied to the PW solder terminal ( $R_{IN} \geq 10K$ ).
- 4) CAUTION: The unit may fail if an attempt is made to operate at a PRF exceeding 1.0 MHz.
- 5) A DC offset of 0 to  $\pm 50$  Volts ( $\pm 200$  mA max) may be applied to the output by applying the required DC voltage to the rear panel OS terminal.
- 6) If additional assistance is required, call (613) 226-5772 or Fax (613) 226-2802.

July 14/93

-EA

-EW