

**AVTECH ELECTROSYSTEMS LTD.**

**NANOSECOND WAVEFORM ELECTRONICS  
ENGINEERING . MANUFACTURING**

□ P.O. BOX 265  
OGDENSBURG  
NEW YORK  
13669  
(315) 472-5270

□ BOX 5120 STN. 'F'  
OTTAWA, ONTARIO  
CANADA K2C 3H4  
(613) 226-5772  
TELEX 053.4591

INSTRUCTIONS

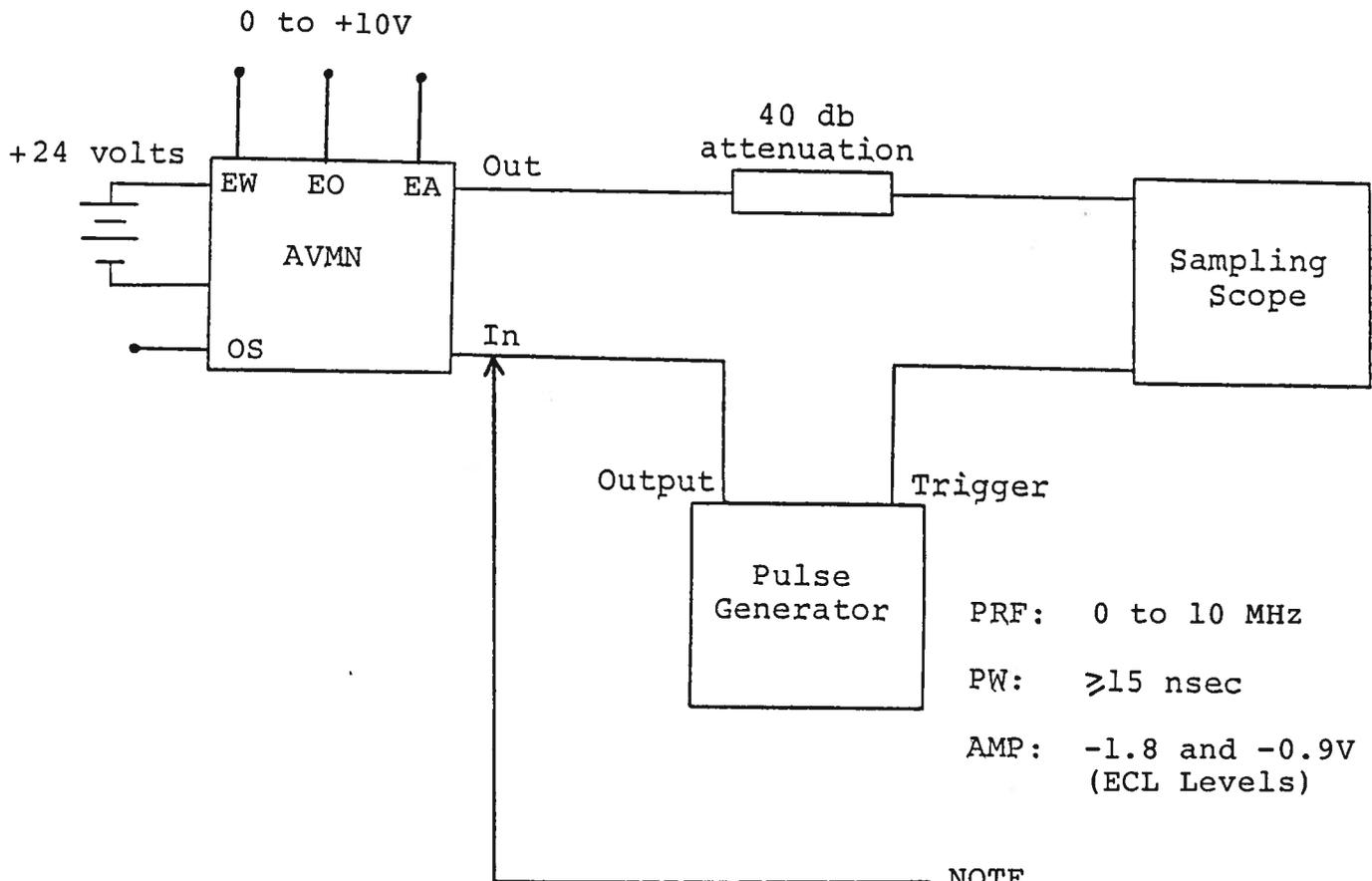
MODEL AVMN-3A-P-EA-EW-EO-M1 PULSE GENERATOR

S.N.:

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

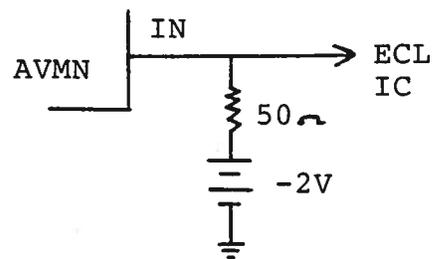
MODEL AVMN PULSE GENERATOR TEST ARRANGEMENT



PRF: 0 to 10 MHz  
 PW:  $\geq 15$  nsec  
 AMP: -1.8 and -0.9V  
 (ECL Levels)

NOTE

If triggering using ECL IC connect 50 ohm to -2 volt here:



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
- 2) The use of 40 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range.\*
- 4) The AVMN requires an ECL trigger signal (-0.9 and -1.8 volts). If triggered directly from an ECL IC, the input to the AVMN should be shunted by 50 ohms terminated in -2 volts.
- 5) The input trigger pulse width should be greater than 15 nsec and less than one half of the pulse repetition frequency period. The unit triggers on the leading edge of the input trigger signal.
- 6) WARNING: Model AVMN may fail if triggered at a PRF greater than 10.0 MHz.
- 7) The output pulse width and amplitude are each controlled by 0 to +10 volts applied to the EW and EA solder terminals ( $R_{IN} \gg 10K$ ).
- 8) The output DC offset is controlled by 0 to +10 volts applied to the EO solder terminal when the EO switch is in the ON position. The output offset is -5 volts when 0V is applied and increases to +5 volts when +10 volts is applied. When the EO switch is in the OFF position, the internally generated offset is inactive but a DC offset can be applied by connecting a desired potential to the rear panel OS solder terminal.

11 The Government's policy of...  
 12 ...the Government's policy of...  
 13 ...the Government's policy of...  
 14 ...the Government's policy of...  
 15 ...the Government's policy of...  
 16 ...the Government's policy of...  
 17 ...the Government's policy of...  
 18 ...the Government's policy of...  
 19 ...the Government's policy of...  
 20 ...the Government's policy of...  
 21 ...the Government's policy of...  
 22 ...the Government's policy of...  
 23 ...the Government's policy of...  
 24 ...the Government's policy of...  
 25 ...the Government's policy of...  
 26 ...the Government's policy of...  
 27 ...the Government's policy of...  
 28 ...the Government's policy of...  
 29 ...the Government's policy of...  
 30 ...the Government's policy of...  
 31 ...the Government's policy of...  
 32 ...the Government's policy of...  
 33 ...the Government's policy of...  
 34 ...the Government's policy of...  
 35 ...the Government's policy of...  
 36 ...the Government's policy of...  
 37 ...the Government's policy of...  
 38 ...the Government's policy of...  
 39 ...the Government's policy of...  
 40 ...the Government's policy of...  
 41 ...the Government's policy of...  
 42 ...the Government's policy of...  
 43 ...the Government's policy of...  
 44 ...the Government's policy of...  
 45 ...the Government's policy of...  
 46 ...the Government's policy of...  
 47 ...the Government's policy of...  
 48 ...the Government's policy of...  
 49 ...the Government's policy of...  
 50 ...the Government's policy of...