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INSTRUCTIONS

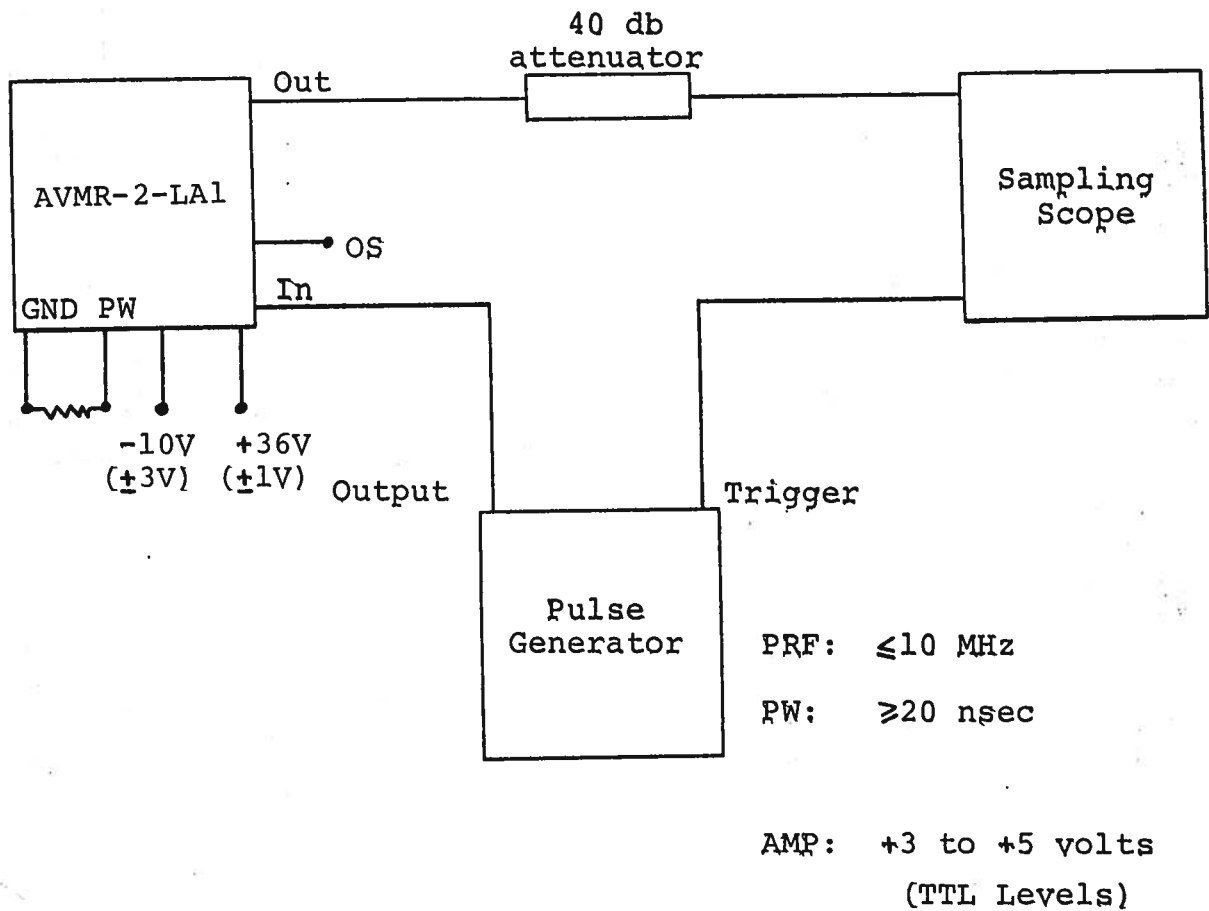
MODEL AVMR-2-P-LA1 PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVMR-2 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
- 2) The use of 40 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) When triggering the AVMR-2 from a high speed lab pulse generator it may be necessary to shunt the input to the AVMR-2 by a 50 ohm resistor to eliminate reflection which may interfere with the operation of the lab pulse generator.
- 4) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.
- 5) **WARNING:** Model AVMR-2 may fail if triggered at a PRF greater than 10 MHz or if the duty cycle exceeds 15% or if the PW exceeds 40 nsec.
- 6) The output amplitude is fixed at +20V to 50 ohms.
- 7) The output pulse width is controlled by the resistance value connected between the PW solder terminal and ground. 330 ohms yields a PW of approximately 15 nsec while 1800 ohms provides an output PW of approximately 40 nsec. Do not solder to the terminals while the prime power is on.
- 8) The required output DC offset voltage is applied to the OS solder terminals (± 50 volts, 100 mA max).
- 9) The module should be attached to a heat sink using the two 2-56 screw holes on the bottom of the module. The 2-56 screws should not protrude more than 0.3" into the module.

The first part of the document discusses the importance of maintaining accurate records of all transactions and activities. It emphasizes the need for transparency and accountability in financial reporting.

In the second section, the author outlines the various methods used to collect and analyze data. This includes both qualitative and quantitative approaches, ensuring a comprehensive understanding of the subject matter.

The third part of the document focuses on the results of the research. It details the findings from the data analysis, highlighting key trends and patterns that emerged during the study.

Finally, the document concludes with a summary of the main points and offers recommendations for future research. It suggests areas where further investigation would be beneficial and provides practical advice based on the findings.

The overall goal of this document is to provide a clear and concise overview of the research process and its outcomes. It aims to inform stakeholders and contribute to the broader field of study.

For more information or to request a copy of the full report, please contact the author at the address provided below.

The author is currently available for consultation on a limited basis. Please reach out as soon as possible to discuss your requirements and schedule a meeting. Your input is highly valued and will help shape the final output.

Thank you for your interest in this work. We look forward to the opportunity to collaborate and explore new ideas together.

Best regards,
[Name]
[Title]
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