

AVTECH ELECTROSYSTEMS LTD.

**NANOSECOND WAVEFORM ELECTRONICS
ENGINEERING . MANUFACTURING**

□ P.O. BOX 265
OGDENSBURG
NEW YORK
13669
(315) 472-5270

☒ BOX 5120 STN. "F"
OTTAWA, ONTARIO
CANADA K2C 3H4
(613) 226-5772
TELEX 053.4591

INSTRUCTIONS

Model AVMR-2-PW Pulse Generator

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

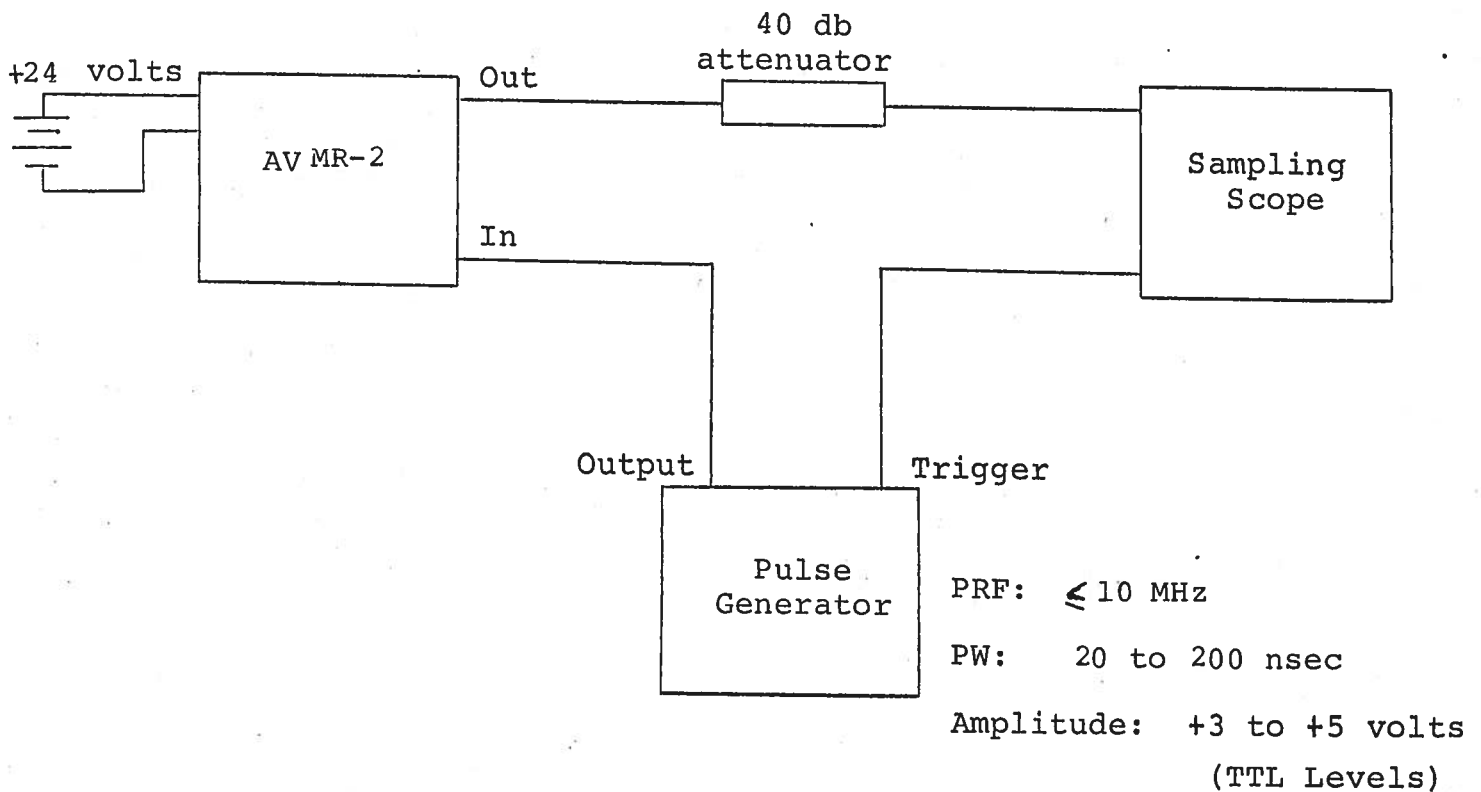
AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
ENGINEERING · MANUFACTURING

P.O. BOX 265
OGDENSBURG
NEW YORK
13869
(315) 472-5270

BOX 5611 STN. "F"
OTTAWA, ONTARIO
CANADA K2C 3M1
(613) 226-5772
TELEX 053-4591

MODEL AVMR-2 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one GHz.
- 2) The use of a 40 db attenuator will insure a peak input signal to the sampling scope of less than one volt.

- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.
 - 4) WARNING: Model AVMR-2 may fail if triggered at a PRF greater than 10 MHz or if the duty cycle exceeds 20% or if the PW exceeds 200 nsec.
 - 5) The output amplitude is controlled by means of the one turn potentiometer (AMP).
 - 6) The output pulse width is approximately equal to the input pulse width.
 - 7) When triggering the AVMR-2 from a high speed lab pulse generator it may be necessary to shunt the input to the AVMR-2 by a 50 Ω resistor to eliminate reflection which may interfere with the operation of the lab pulse generator.
-
- 4) To DC offset the output, connect a DC power supply set to the desired offset values to the rear panel OS terminals (± 50 volts).