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INSTRUCTIONS

MODEL AVO-5C-C-PN PULSE GENERATOR

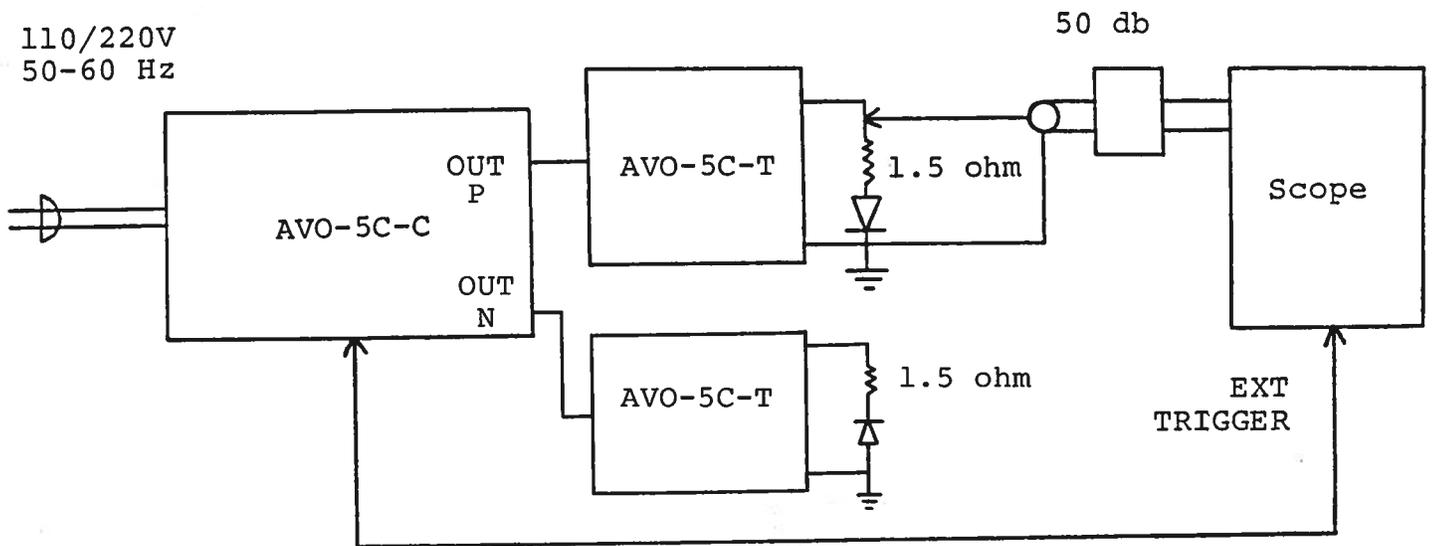
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WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

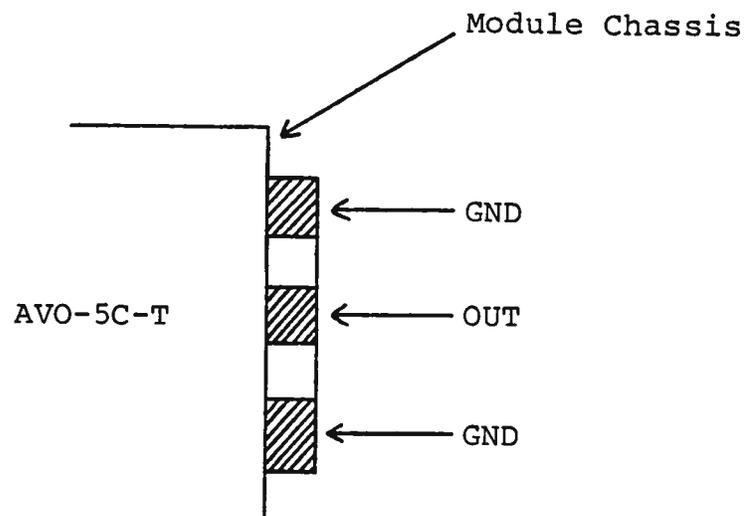
Fig. 1

PULSE GENERATOR TEST ARRANGEMENT

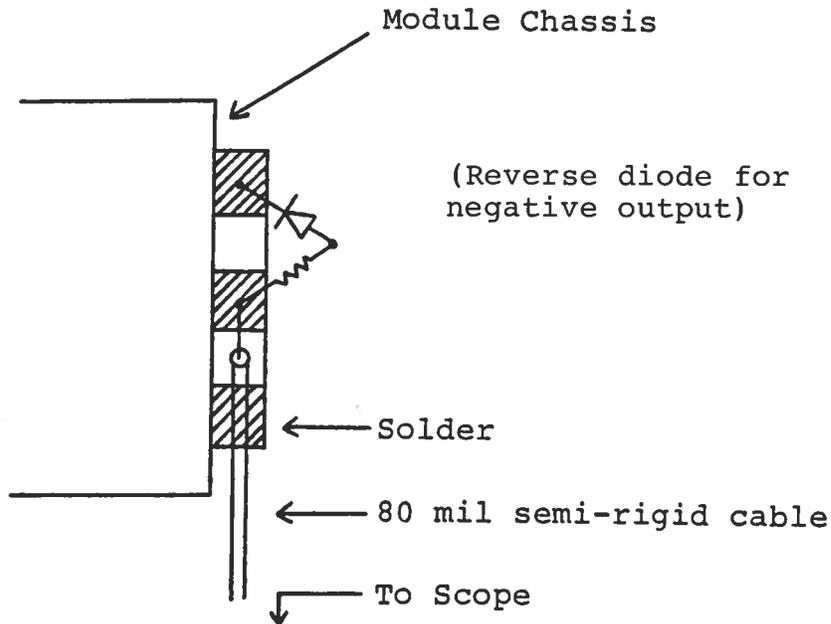


Notes:

- 1) The equipment should be connected in the general fashion shown above. Since the AVO unit provides an output pulse rise time as low as 5 nsec a fast oscilloscope (at least 50 MHz and preferably 200 MHz) should be used to display the waveform. If a sampling scope is used, a 50 db (or greater) attenuator should be used to insure a peak input to the scope of less than 0.5 volts.
- 2) The sync output channel provides a +0.5 volt 100 nsec pulse.
- 3) To obtain a stable output display the PRF controls on the front panel should be set mid-range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY control and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF controls. The main output is delayed with respect to the SYNC output by about 0 to 200 nsec depending on the DELAY control setting.
- 4) The desired output polarity is selected by means of the front panel POLARITY switch. With the POLARITY switch in the P position, the negative output pulse generator is rendered inactive. Likewise, with the POLARITY switch in the N position, the positive pulse generator is rendered inactive.
- 5) The AVO-5C-T transformer module transforms the 50 volt output of the AVO-5C mainframe to 12 volts to 1.5 ohm. The AVO-5C-T module connects to the mainframe via two parallel 50 ohm miniature coaxial cables approx. 2 feet in length. The output terminals of the transformer module consists of a short length of microstrip transmission line protruding from the module chassis. The OUT terminal is the center conductor which is bounded on both sides by the ground plane (see below):



The diode load and series resistor (1.0 to 2.0 ohm 1/4 W carbon composition resistor) should be connected between the OUT and GND terminals using very short leads ($\ll 0.2$ cm). The voltage across the resistor-diode load may be monitored by connecting a length of 80 mil semi-rigid 50 ohm cable as shown below:

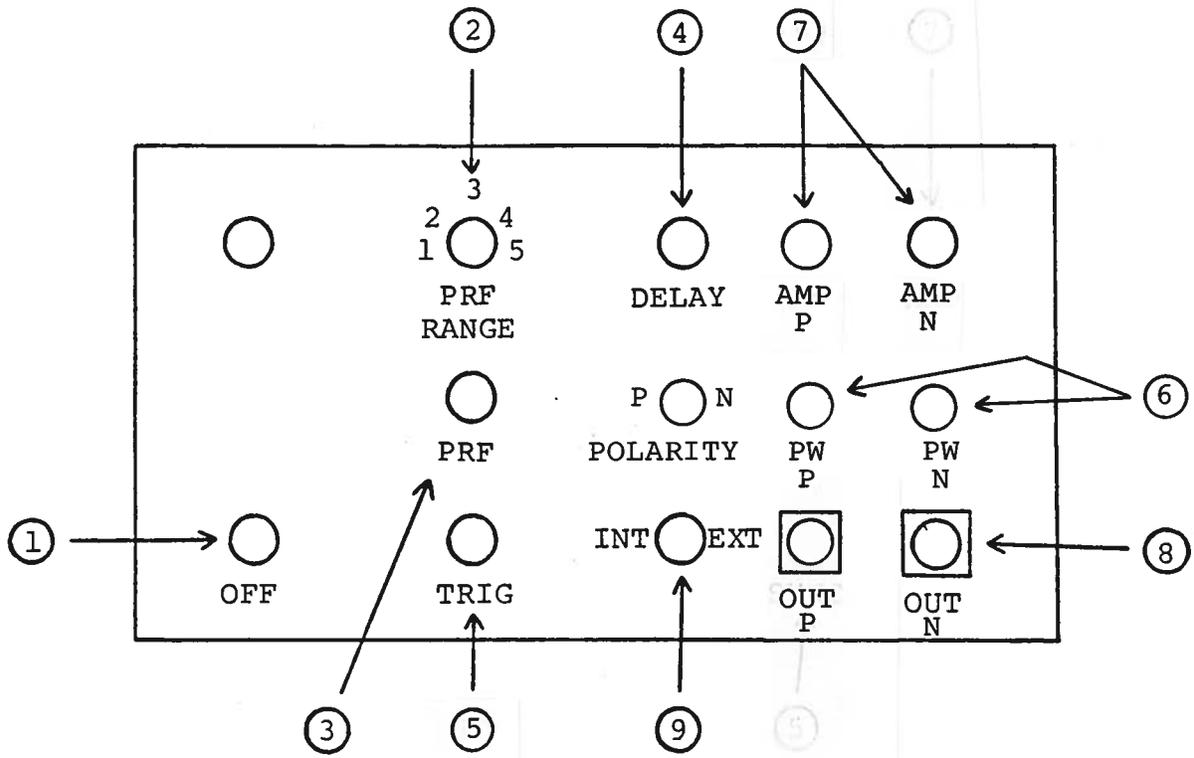


Take care to insure that during soldering the OUT conductor is not shorted to the chassis. Also, use minimal heat when soldering.

- 6) To voltage control the output pulse width, remove the jumper wire between banana plugs A and B on the back panel and apply 0 to +10V to connector B ($R_{IN} \gg 10K$). (option).
- 7) To voltage control the output amplitude, remove the jumper wire between banana plugs A and B on the back panel and apply 0 to +10V to connector B ($R_{IN} \gg 10K$). (option).
- 8) **CAUTION:** The maximum output duty cycle should not exceed 10%. For example, at 5 MHz the output pulse width should not exceed 20 nsec.

Fig. 2

FRONT PANEL CONTROLS



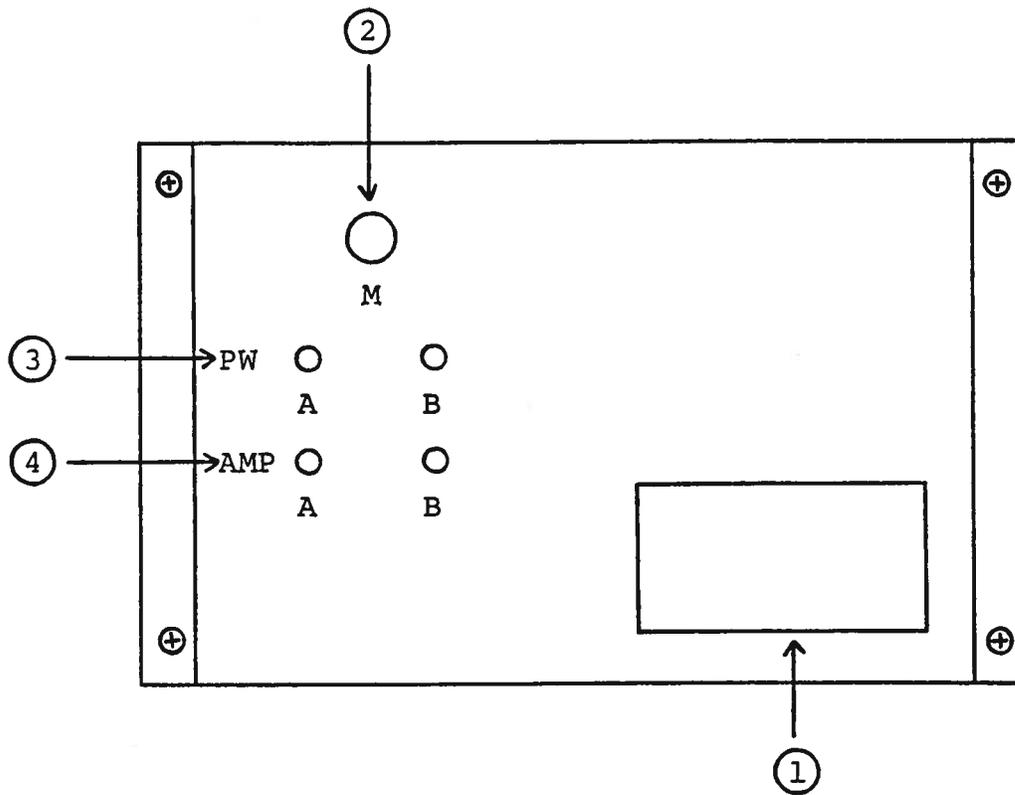
- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. Varies PRF from about 50 Hz to 5 MHz. The
- (3) operating PRF should be set using a scope.

	PRF MIN	PRF MAX
RANGE 1	1.6 KHz	8 KHz
RANGE 2	8 KHz	40 KHz
RANGE 3	40 KHz	200 KHz
RANGE 4	200 KHz	1.0 MHz
RANGE 5	1.0 MHz	5.0 MHz

- (4) DELAY Control. Controls the relative delay between the reference output pulse provided at the SYNC output (5) and the main output (8). This delay is variable over the range of about 0 to 200 nsec.
- (5) SYNC Output. This output precedes the main output (8) and is used to trigger the sampling scope time base. The output is a TTL level 100 nsec (approx) pulse capable of driving a fifty ohm load.
- (6) PW Control. A one turn control which varies the output pulse width from 5 to 50 nsec. Note that the output duty cycle should not exceed 10%.
- (7) AMP Control. The output pulse amplitude is controlled by means of the one turn potentiometer (AMP).
- (8) OUT Connectors. Two SMA connectors for two miniature coaxial cables connected to the AVO-5C-T module.
- (9) EXT-INT Control. With this toggle switch in the INT position, the PRF of the AVO unit is controlled via an internal clock which in turn is controlled by the PRF controls. With the toggle switch in the EXT position, the AVO unit requires a 20 nsec TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.
- (10) TRIG. The external trigger signal is applied at this input when the EXT-INT toggle switch is in the EXT position. The output pulse at (8) appears about 30 nsec after the application of the TRIG pulse. The SYNC OUT pulse is provided at this point when the INT-EXT switch is in the INT position.

Fig. 3

BACK PANEL CONTROLS



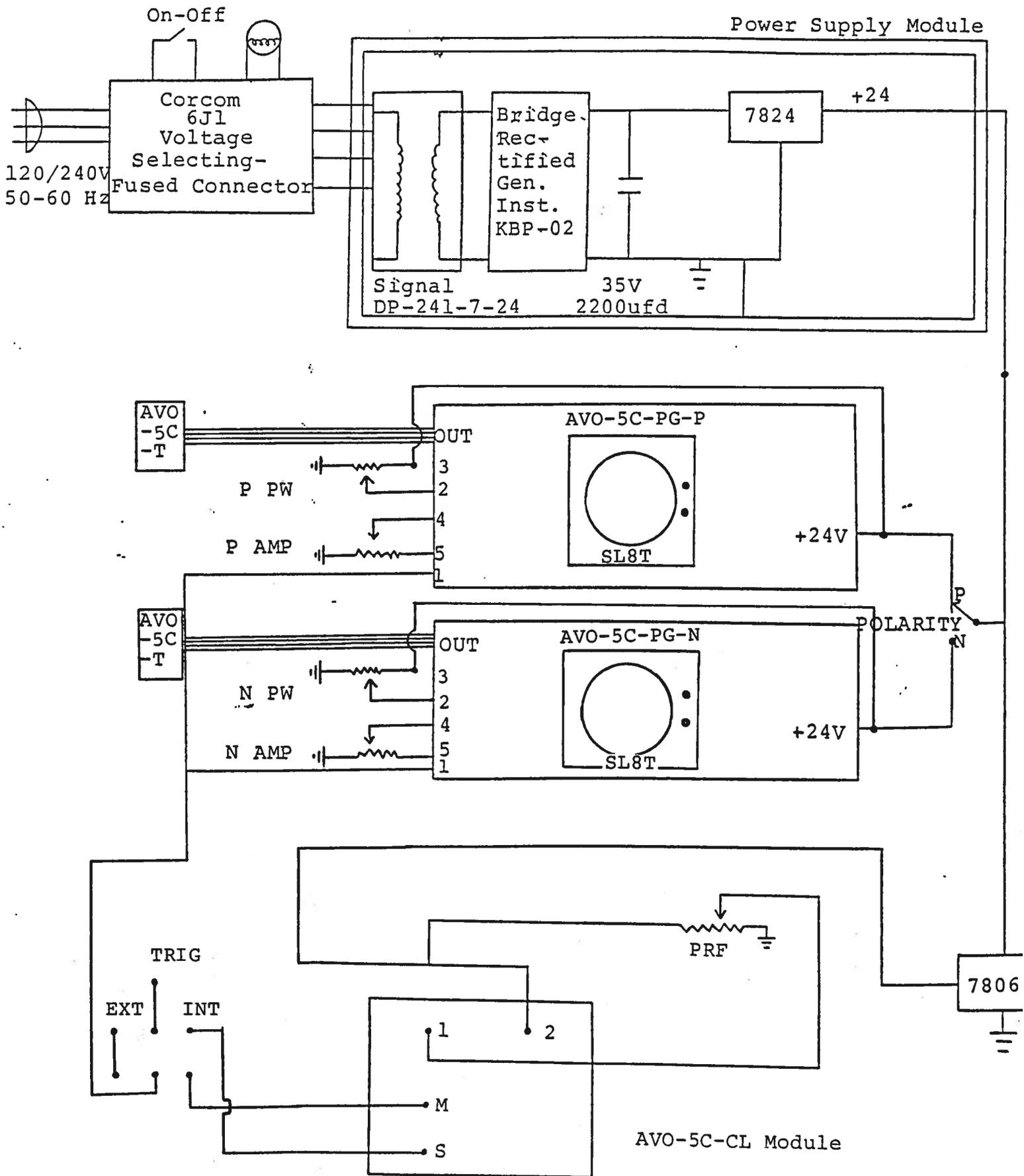
- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse.
- (2) MONITOR Output (M). Output voltage (to fifty ohms) at this point (V_M) can be related to AVO-5A-T output current (I_{OUT}) as follows (option):

$$I_{OUT} = 0.16 V_M \quad (\text{Volts and Amps})$$

- (3) To voltage control the output pulse width, remove the jumper wire between banana plugs A and B and apply 0 to +10V to connector B ($R_{IN} \geq 10K$). (option).
- (4) To voltage control the output amplitude, remove the jumper wire between banana plugs A and B and apply 0 to +10V to connector B ($R_{IN} \geq 10K$). (option).

Fig. 4

SYSTEM BLOCK DIAGRAM



SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVO-5C-C-PN unit consists of the following basic modules:

- 1) AVO-5C-PG pulse generator module (positive and negative)
- 2) AVO-5C-CL clock module
- 3) +24V power supply board

The modules are interconnected as shown in Fig. 4.

The clock module controls the output PRF and the relative delay between the main output and the SYNC outputs. The PG pulse generator modules generate the output pulse. In the event of an instrument malfunction, it is most likely that some of the output switching elements (SLBT) may have failed due to an output short circuit condition or to a high duty cycle condition. The switching elements may be accessed by removing the cover plate on the bottom side of the chassis. **NOTE:** First turn off the prime power. **CAUTION:** Briefly ground the SLBT tabs to discharge the 60 volts power supply potential. The elements may be removed from their sockets by means of a needle nosed pliers after removing the four counter sunk 2-56 Phillips screws which attach the small aluminum heat sinks to the body of the chassis. The SLBT is a selected VMOS power transistor in a TO 220 package and may be checked on a curve tracer. If defective, replacement units should be ordered directly from Avtech. When replacing the SLBT switching elements, take care to insure that the short lead (of the three leads) is adjacent to the black dot on the chassis. The SLBT elements are electrically isolated from the small aluminum heat sinks but are bonded to the heat sinks using WAKEFIELD TYPE 155 HEAT SINK ADHESIVE. If the switching elements are not defective, then the four Phillips screws on the back panel should be removed. The top cover may then be slid off and the operation of the clock and power supply modules checked. The clock module is functioning properly if:

- a) 20 nsec outputs are observed at pins 2 and 3.
- b) The PRF of the outputs can be varied over the range of 0.1 KHz to 5 MHz using the PRF controls.
- c) The relative delay between the pin 2 and 3 outputs can be varied by at least 500 nsec by the DELAY control.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed. The power supply board generates +24V DC to power the other modules. If the voltage is less than +24V, turn off the prime power and unsolder the lead from the 7824 regulator chip on the power supply board. Solder a 100 ohm 5 watt resistor to the 7824 output to ground and turn on the prime power. A voltage of +24 volts should be read. If the voltage is less then the power supply board is defective and should be repaired or replaced.

Schroff

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