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INSTRUCTIONS

MODEL AVO-7A-C PULSE GENERATOR

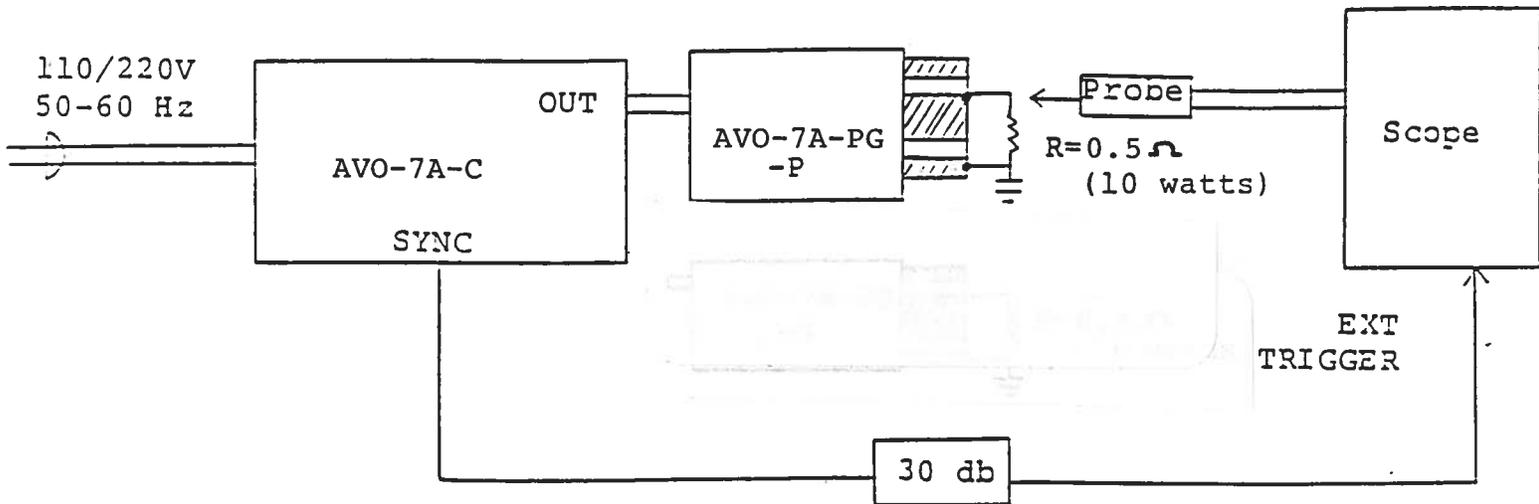
S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

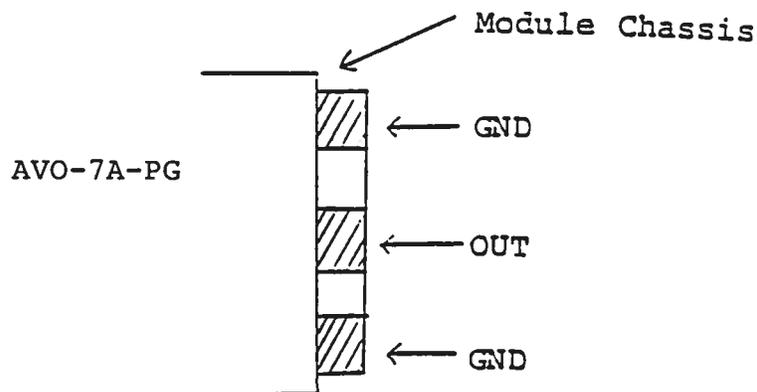
Fig. 1

PULSE GENERATOR TEST ARRANGEMENT

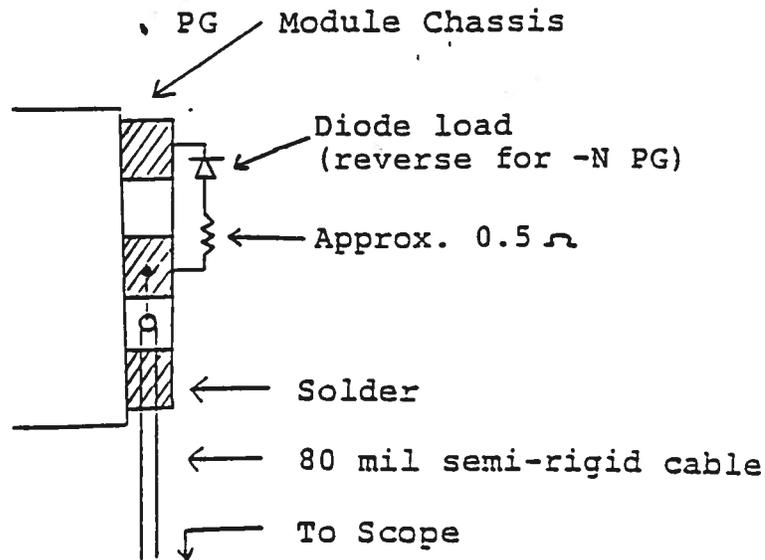


GENERAL OPERATING INSTRUCTIONS

- 1) The equipment should be connected in the general fashion shown above. Since the AVO unit provides an output pulse rise time as low as 100 nsec a fast oscilloscope (at least 50 MHz and preferably 200 MHz) should be used to display the waveform.
- 2) The sync output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some sampling scopes, a 30 db attenuator should be placed at the input to the sampling scope trigger channel.
- 3) To obtain a stable output display the PRF controls on the front panel should be set mid-range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY control and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF and PRF FINE controls. The main output is delayed with respect to the SYNC output by about 0 to 1 usec depending on the DELAY control setting.
- 4) The output terminals of the pulse generator module consists of a short length of microstrip transmission line protruding from the module chassis. The OUT terminal is the center conductor which is bounded on both sides by the ground plane (see below):



The load should be connected between the OUT and GND terminals using very short leads ($\ll 0.5$ cm). The voltage across the load may be monitored by connecting a length of 80 mil semi-rigid 50 ohm cable as shown below (or by means of a high impedance scope probe):



Take care to insure that during soldering the OUT conductor is not shorted to the chassis. Also, use minimal heat when soldering.

- 5) To voltage control the output pulse width, set the rear panel switch in the EXT position and apply 0 to +10V between terminal A and ground ($R_{IN} \gg 10K$). (option).
- 6) To voltage control the output amplitude, set the rear panel switch in the EXT position and apply 0 to +10V between terminal A and ground ($R_{IN} \gg 10K$). (option).
- 7) An external clock may be used to control the output PRF of the unit by setting the front panel TRIG toggle switch in the EXT position and applying a 0.2 usec (approx) TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the SYNC output.

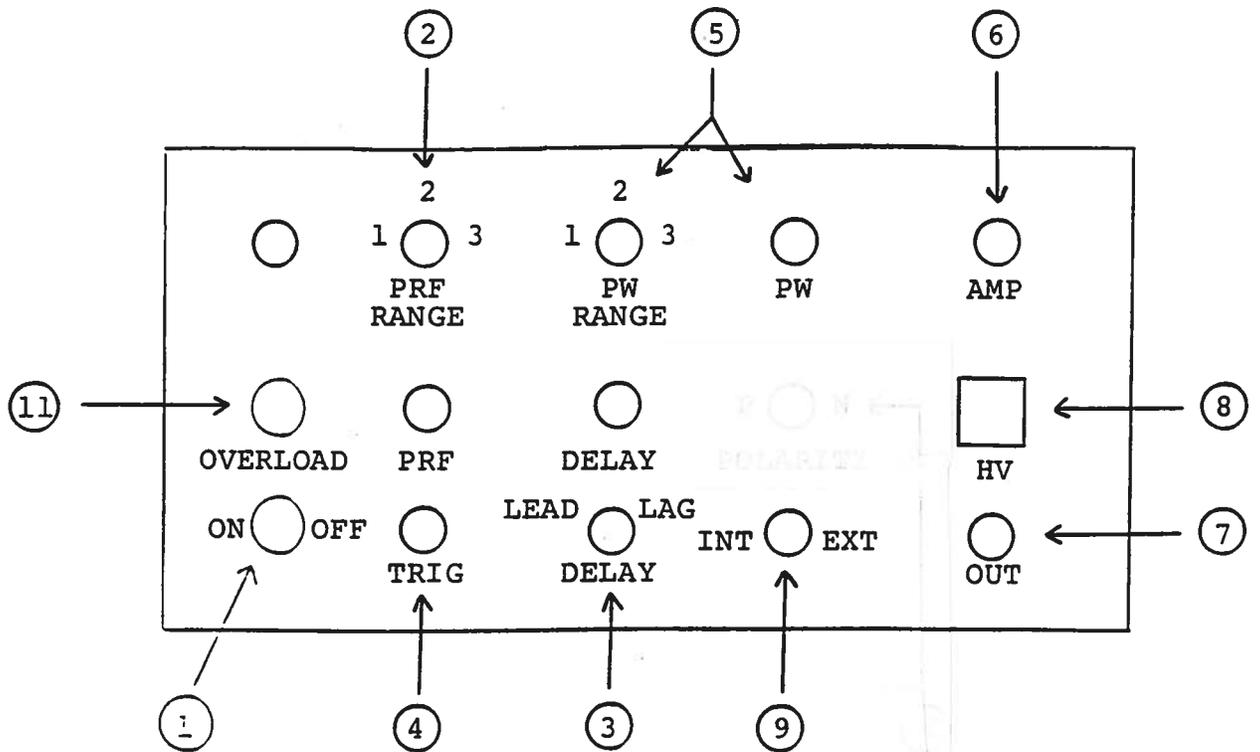
- 8) MONITOR Option. The M output connector on the PG module provides a voltage pulse (to 50 ohm or higher) where amplitude is proportional to the output peak load current. An output current of 40 amperes provides a monitor voltage of 0.5 volts.
- 9) The AVO-7-C is designed to supply up to 40 amperes to a maximum load voltage of 20 volts. Factory tests are conducted with a 0.5 ohm load capable of dissipating at least 10 watts. Higher load resistance values may be used but the output voltage must be limited to 20 volts or less.
- 10) CAUTION: The AVO-7-C is designed to operate with a maximum duty cycle of 1%. For example at PRF up to 50 Hz, the full 200 usec PW may be employed. At 1 KHz PRF, the maximum allowable PW is reduced to 10 usec.
- 11) CAUTION: The output stage is protected against overload condition by a 1.0 A slow blow fuse on the main frame back panel. However, the output switching elements (SL17T) may fail if the duty cycle rating is exceeded. Heating and subsequent likely failure of the output stage is reduced if the following action is taken where possible:
- a) PRF is kept to a minimum, ie. operate in the LOW PRF range when possible rather than in the HIGH range.
 - b) Keep the output PW to a minimum.
 - c) Keep the load resistance as high as possible.
- 12) AVO-7-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:
- 1) Reducing PRF (i.e. switch to a lower range)
 - 2) Reducing pulse width (i.e. switch to a lower range)
 - 3) Removing output load short circuit (if any)

- 13) The unit can be converted from 110 to 220V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector cable connector assembly.
- 14) Units with a serial number higher than 6400 have a solder terminal on the side of the -PG module. For -P units this terminal is at a potential as high as +30 volts while for -N units the terminal is at a potential as high as -30 volts. Electrolytic capacitors in the range of 5000 to 10000 ufd may be connected between this terminal and the ground on the output terminal in order to reduce the droop on the output pulse at very wide pulse widths (eg. 200 us). CAUTION: Insure that the polarity of the capacitor is correct.
- 15) For additional assistance:

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Fig. 2

FRONT PANEL CONTROLS



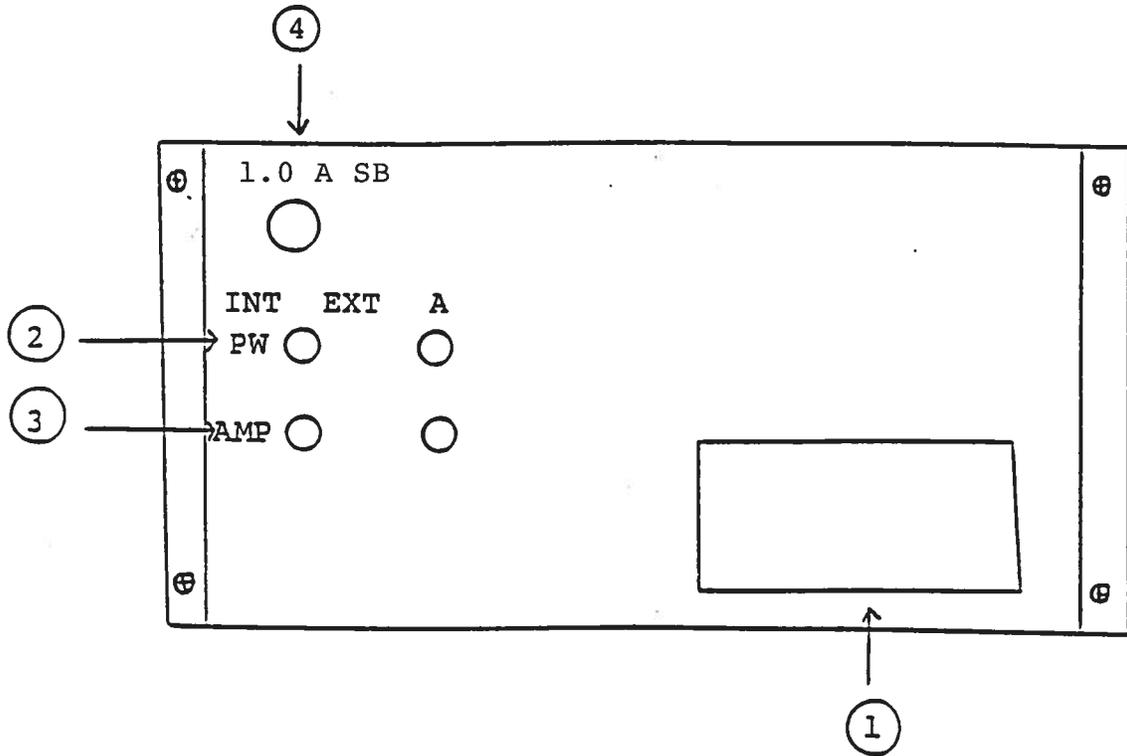
- (1) ON-OFF Switch. Applies basic prime power to all stages.
- (2) PRF Control. Varies PRF as follows:
 - Range 1: 5 Hz to 50 Hz
 - Range 2: 10 Hz to 100 Hz
 - Range 3: 100 Hz to 1 KHz
- (3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (7). This delay is variable over the range of 0 to about 10.0 usec. The TRIG output precedes the main output when the LEAD-LAG switch is in the LEAD position and lags when the switch is in the LAG position.
- (4) TRIG Output. This output is used to trigger the scope time base. The output is a TTL level 100 nsec (approx.) pulse capable of driving a fifty ohm load.
- (5) PW Control. A one turn control and 3 position range switch that varies PW as follows:
 - RANGE 1: 0.2 to 2.5 usec
 - RANGE 2: 1.7 to 30 usec
 - RANGE 3: 17 to 200 usec
- (6) AMP Control. The output pulse amplitude is controlled by means of the one turn potentiometer (AMP).
- (7) OUT Connector. A multi pin connector which attaches the 2 foot cable from the pulse generator module to the main frame.
- (8) HV Connector. Two foot long RG124 cable from -PG module connects to this SMA connector. Supplies 30 volts from mainframe to -PG module.
- (9) EXT-INT Control. With this toggle switch in the INT position, the PRF of the AVO unit is controlled via an internal clock which in turn is controlled by the PRF controls. With the toggle switch in the EXT position, the AVO unit requires a 0.2 usec TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.
- (10) AVO units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The

light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:

- 1) Reducing PRF (i.e. switch to a lower range)
- 2) Reducing pulse width (i.e. switch to a lower range)
- 3) Removing output load short circuit (if any)

Fig. 3

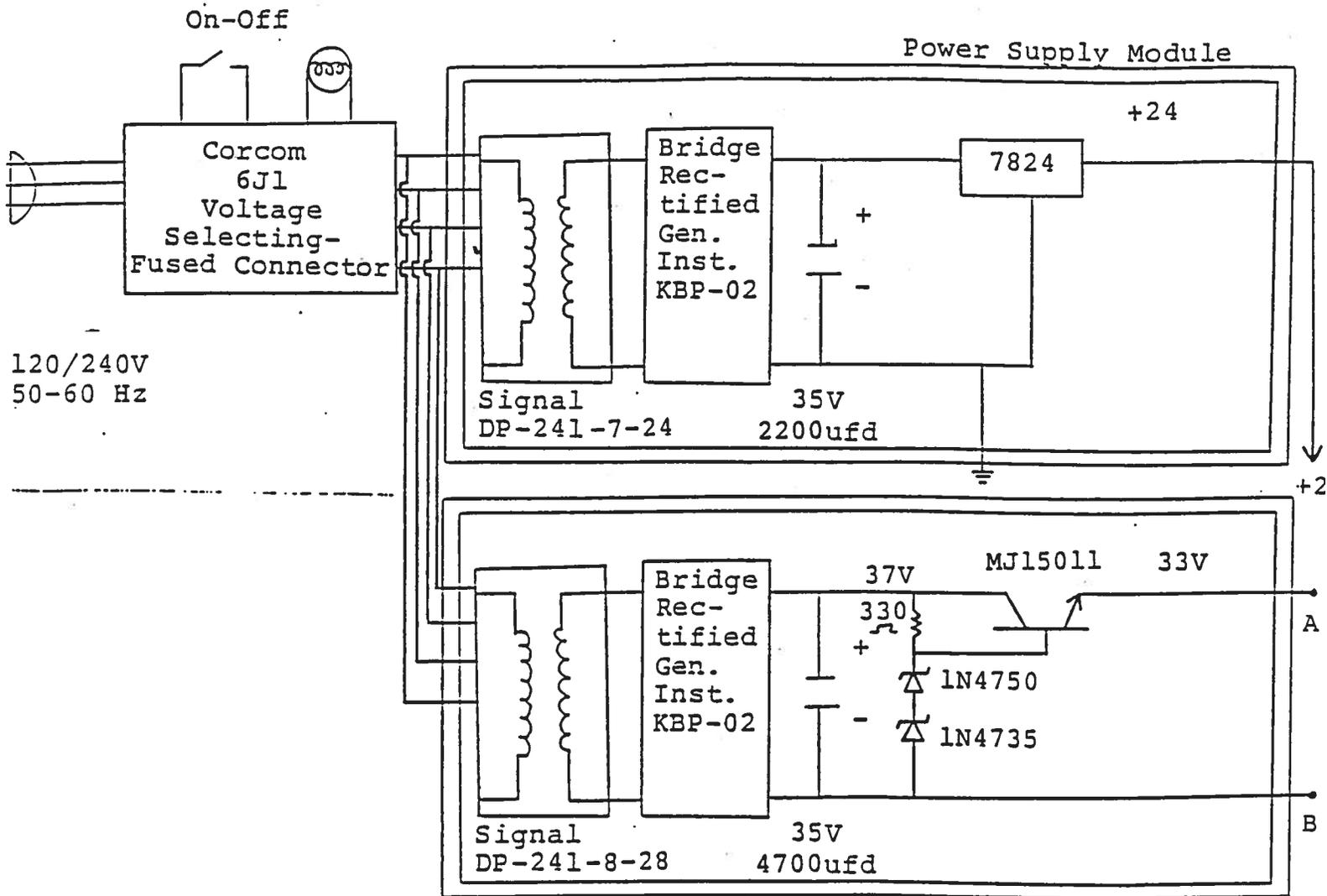
BACK PANEL CONTROLS



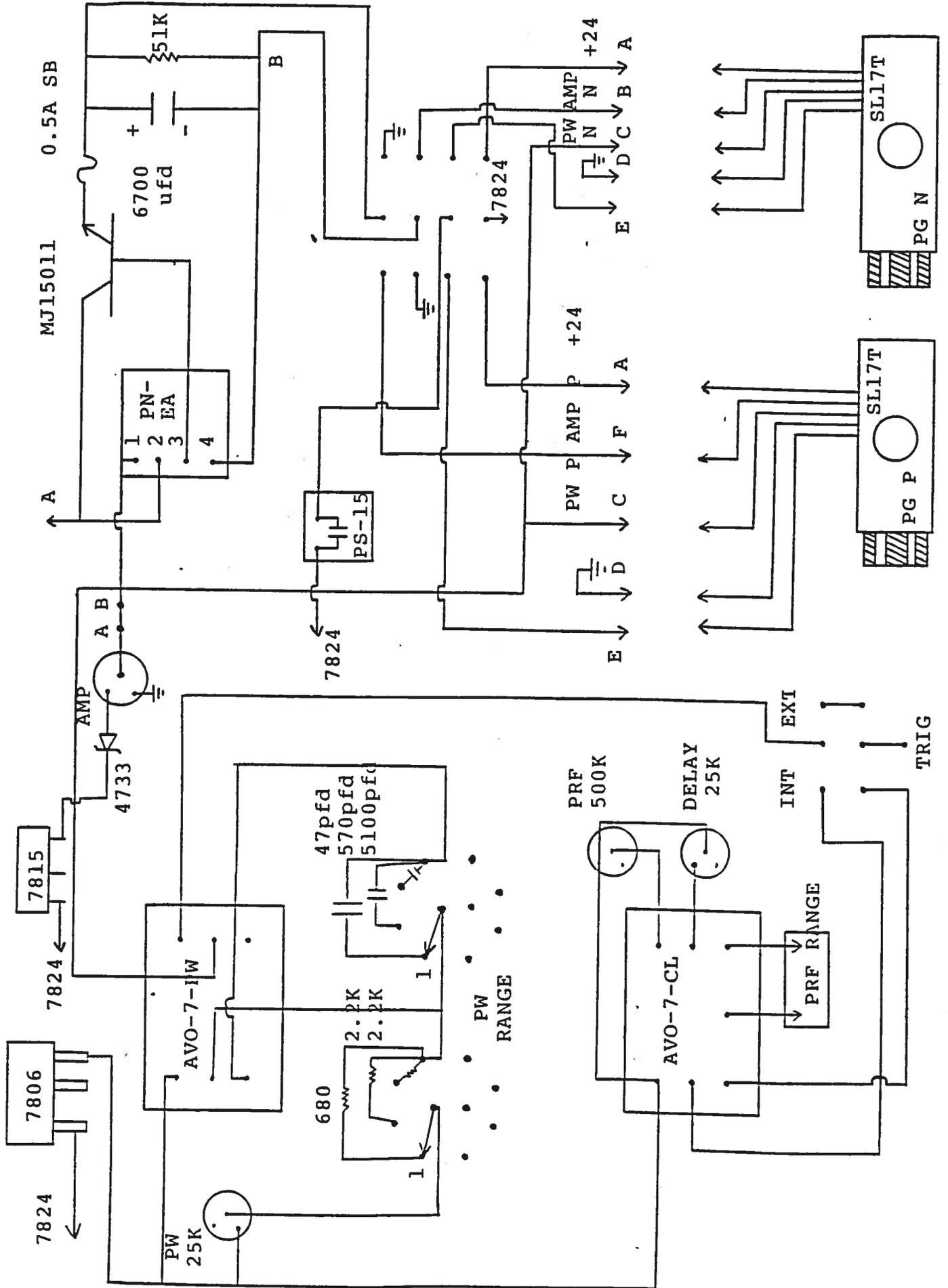
- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.5 A).
- (2) To voltage control the output pulse width, set the rear panel switch in the EXT position and apply 0 to +10V between terminal A and ground ($R_{IN} \geq 10K$). (option).
- (3) To voltage control the output amplitude, set the rear panel switch in the EXT position and apply 0 to +10V between terminal A and ground ($R_{IN} \geq 10K$). (option).
- (4) 1.0 A SB FUSE. Protects output stage against overload conditions.

Fig. 4

SYSTEM BLOCK DIAGRAM



AVO-7-C-PN-EA BLOCK DIAGRAM



SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVD-7-C-PN unit consists of the following basic modules:

- 1) AVD-7-PG pulse generator module
- 2) AVD-7-CL clock module
- 3) +24V and +33V power supply board
- 4) AVD-7-PW pulse width module

The modules are interconnected as shown in Fig. 4.

In the event of an instrument malfunction, it is most likely that the 1.0A slow blow fuse or the main power fuse on the rear panel has blown. Replace if necessary. If the unit still does not function, it is most likely that some of the output switching elements (SL17T) may have failed due to an output short circuit condition or to a high duty cycle condition. The switching elements may be accessed by removing the cover plate on the bottom side of the -PG module. The cover plate is removed by removing the four counter sunk 6-32 Phillips screws. **NOTE:** First turn off the prime power. **CAUTION:** Briefly ground the SL17T tabs to discharge the 33 volts power supply potential. The elements may be removed from their sockets by means of a needle nosed pliers after removing the four counter sunk 2-56 Phillips screws which attach the small aluminum heat sinks to the body of the AVD-7-PG module. The SL17T is a selected VMOS power transistor in a TO 220 package and may be checked on a curve tracer. If defective, replacement units should be ordered directly from Avtech. When replacing the SL17T switching elements, take care to insure that the short lead (of the three leads) is adjacent to the back of the chassis. (See following Fig.). The SL17T elements are electrically isolated from the small aluminum heat sinks but are bonded to the heat sinks using WAKEFIELD TYPE 155 HEAT SINK ADHESIVE. If the switching elements are not defective, then the four Phillips screws on the back panel should be removed. The top cover may then be slid off and the operation of the clock and power supply modules checked. The clock module is functioning properly if:

- a) 0.1 usec TTL level outputs are observed at pins 2 and 3.
- b) The PRF of the outputs can be varied over the range of 2 Hz to 1 KHz using the PRF controls.
- c) The relative delay between the pin 2 and 3 outputs can be varied by at least 1 usec by the DELAY control.

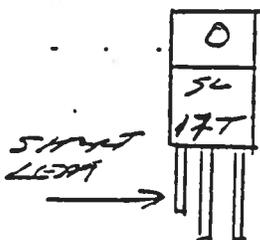
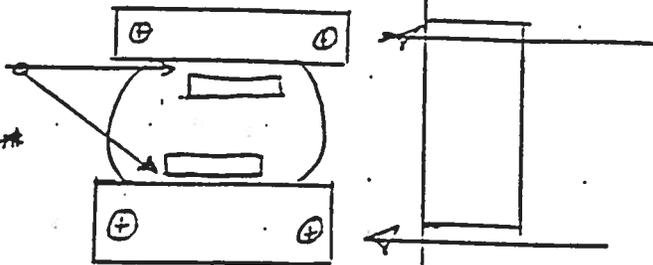
The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed.

BACK

-P-P6

FRONT

155
HEAT SINK
ADHESIVE*

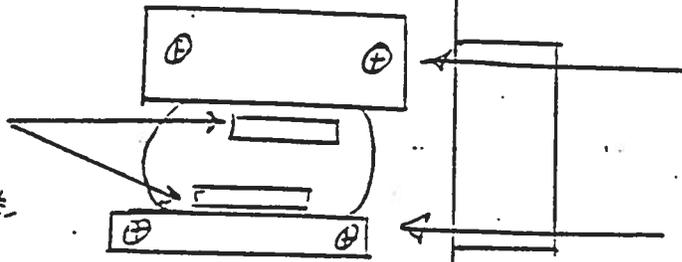


BACK

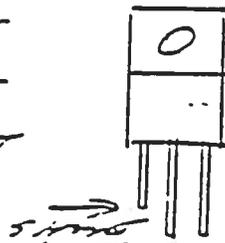
-N-P6

FRONT

155 HEAT
SINK
ADHESIVE*



*
CAUTION INSURE THAT
TABS OF SLIT
DO NOT CONTACT
H.S. TABS.



Schroff

08.05.92

-EW

-EA

-M