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INSTRUCTIONS

MODEL AVO-9G-C-OP1 PULSE GENERATOR

MODEL AVX-S1-MI BIAS INSERTION UNIT

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

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EC Declaration of Conformity

We

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declare that the AVO-9G-C pulse generator meets the intent of Directive 89/336/EEC for Electromagnetic Compatibility. Compliance pertains to the following specifications as listed in the official Journal of the European Communities:

EN 50081-1 Emission

EN 50082-1 Immunity

CE

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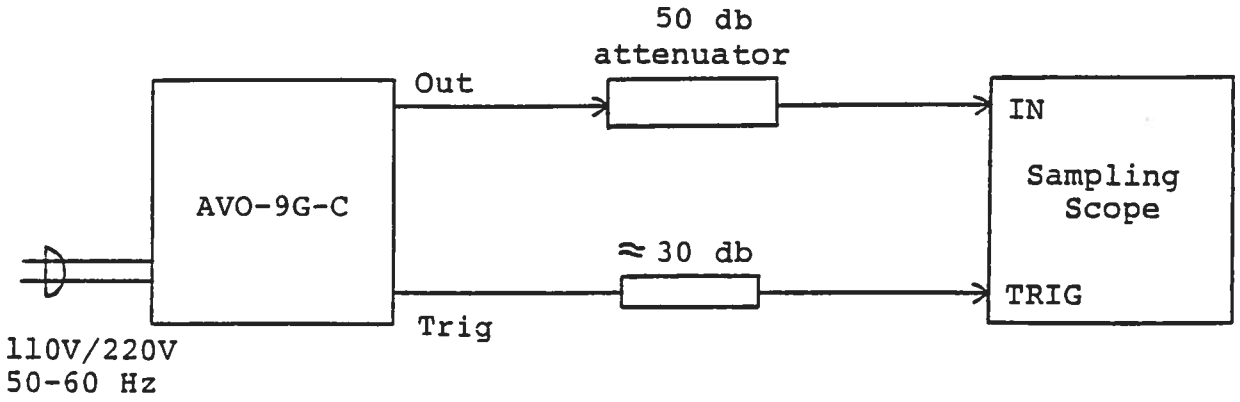
Fig. 1 PULSE GENERATOR TEST ARRANGEMENT

(AVX-S1 MODULE DISCONNECTED)



OPERATING INSTRUCTIONS

1) For front panel manual control of the output parameters, the rear panel LOCAL-REMOTE switch must be in the LOCAL position. For remote control using a personal computer, the switch should be in the REMOTE position. See the AN-101-9G section (at the end of the manual) for the instructions of this mode of operation.

2) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed 2 GHz.

3) The use of 50 dB attenuator at the scope vertical input channel will insure a peak input signal to the scope of less than one Volt (necessary only if sampling scope used). If a high impedance real time scope is used, the pulse generator should be terminated using a shunt 50 Ohm resistor.

4) The TRIG output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some scopes, a 30 dB attenuator should be placed at the input to the scope trigger channel. The TRIG output precedes the main output when the front panel ADVANCE-DELAY switch is in the ADVANCE position. The TRIG output lags the main output when the switch is in the DELAY position.

5) To obtain a stable output display the PW and PRF controls on the front panel should be set mid range. The front panel TRIG toggle switch should be in the INT position. The DELAY controls and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF controls.

6) The output pulse width is controlled by means of the front panel one turn PW control (ten turn control for units with -PWT option).

7) The output pulse amplitude is controlled by means of the front panel one turn AMP control (ten turn control for units with the -AT option).

8) An external clock may be used to control the output PRF of the AVO unit by setting the front panel TRIG toggle switch in the EXT position and applying a 50 ns (or wider) TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the TRIG output.

9) AVO units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:

   1) Reducing PRF (i.e. switch to a lower range)
   2) Reducing pulse width (i.e. switch to a lower range)
   3) Removing output load short circuit (if any)

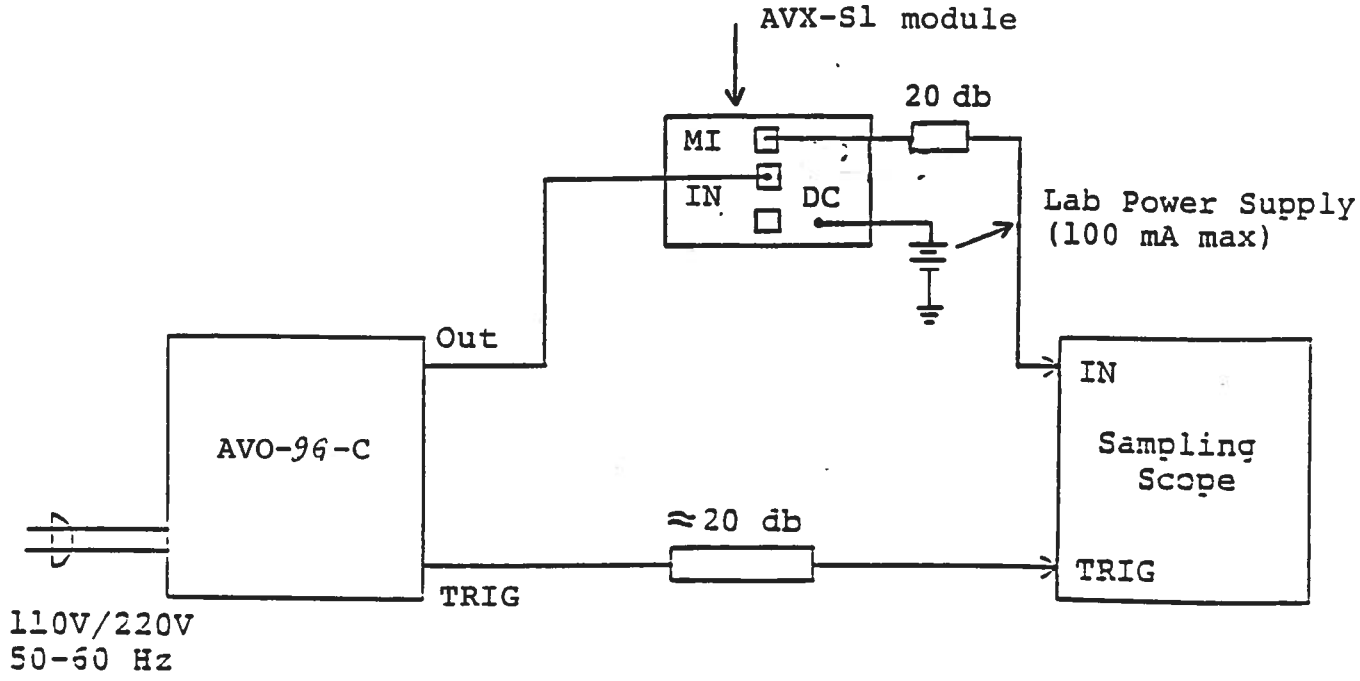
10) The unit can be converted from 120 to 240V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.

11) For additional assistance:

Tel: (613) 226-5772
Fax: (613) 226-2802

Fig. 2 PULSE GENERATOR TEST ARRANGEMENT

(AVX-S1 MODULE CONNECTED)



AVX-S1

1) A general description of the AVX-S1 module is given in the enclosed data sheet.

2) The AVX-S1 module should be connected to the AVO-9G-C mainframe via the supplied 24" RG174 cable. The diode current may be monitored by connecting the MI and MV output ports to the sampling scope via 20 dB attenuators. The output amplitude (V_{MI}, Volts) and diode current (I_D, Amps) are related as follows:

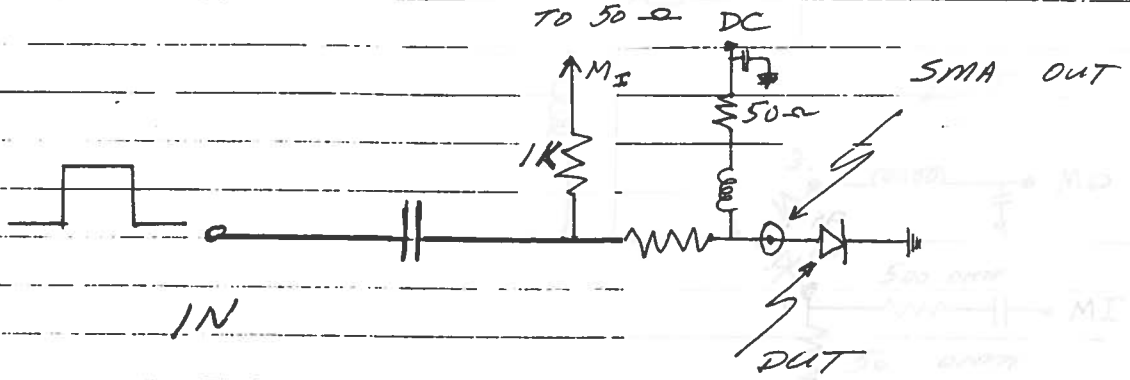
$$I_D = 0.4\ V_{MI}$$

3) The laser diode anode must connect directly to the centre pin of the SMA connector outside of the AVX-S1 module (lead lengths should not exceed 1 cm).

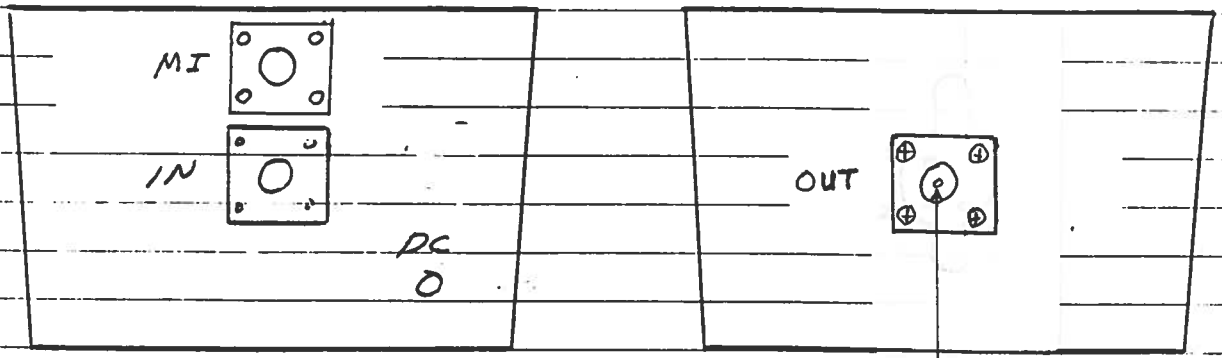
4) A forward DC bias may be applied to the laser diode by connecting a DC potential of 0 to ±5 Volts to the DC solder terminal. The application of a small forward bias often yields a more ideal diode current waveform (as observed on the MI port). Note that the DC port must be shorted to ground if a bias is not applied.

PR-51

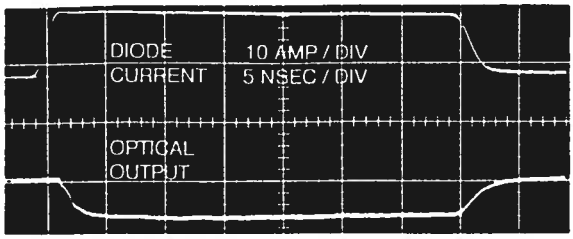
SN 8203



FUNCTIONAL EQUIV. CKT.



PACKAGE



- Socket mounting of laser diodes
- Peak currents from 100 mA to 48 Amps
- Pulse widths from 0.4 to 200 ns
- Rise times from 0.2 to 2.0 ns
- Pulse or CW RF
- Diode current and voltage monitor options

The AVX-S series of bias insertion units is designed for applying pulse or RF CW signals and DC bias to laser diodes which insert into a high quality socket included on the mount. The bias insertion module includes the necessary networks to match the laser diode to the pulse or RF source as well as networks for applying DC bias to the diode. Optional outputs allow for monitoring of the laser diode current, voltage and a photo detector diode output. Readily available socket configurations (TO-18, TO-5, TO-3, OP-3) are shown on the following page. Note that the laser diodes are not supplied with the AVX-S series.

The AVX-S series includes 3 basic models namely the AVX-S1, AVX-S2 and the AVX-S3. The basic functional equivalent circuit for the three models are shown below. Model AVX-S1 is specifically designed for ultra high-speed, low current applications (rise times as low as 200 ps, bandwidths to 1 GHz, I <1.0 ampere). Model AVX-S1 is employed in the AVO-9-C series of diode drivers. Model AVX-S2 is intended for application with rise times greater than 2 ns and currents above 1 ampere. Model AVX-S3 is specifically designed for use with the AVO-2 and AVO-5 series pulse generators (which provide currents in the range of 5 to 50 amperes).

The input series blocking capacitor in Models AVX-S1 and AVX-S2 presents a low impedance to RF CW signals and to baseband pulses while the shunt indicator presents a high impedance to RF (or pulse) signals but an extremely low impedance to the DC bias. The resistor in series with the laser diode is selected to insure that the impedance at the IN port is 50 ohms. Normally a laser diode resistance of 3 ohms is assumed.

The optional diode current monitor (M_D) provides an output waveform (to 50 ohms) which is an attenuated replica of the laser diode current. The output amplitude (V_{MI}, volts) and diode current (I_D, Amps) are related as follows:

-S1: $I_D = 0.2V_{MI}$ -S2: $I_D = V_{MI}$

The optional diode voltage monitor (MV) provides an output waveform that may be related to the voltage across the laser diode (V_D, volts) as follows:

-S1: $V_D = 10\,(V_{MV} - V_{MI})$ - S2: $V_D = 10\,V_{MV}$

Model AVX-S3 is available in four different versions (AVX-S3A, AVX-S3B, AVX-S3C and AVX-S3D) all of which include a matching transformer which effectively boosts the laser diode current beyond that provided by the pulse source.

Model AVX-S3A is designed to match 50 ohm pulse generators such as Model AVO-2-C to 12 ohm loads with peak currents of 5 amperes. Consequently, the resistor R_S in the equivalent circuit for this model is 10 ohm. This resistor is accessible in all AVX-S3 models and may be changed by the user (by desoldering). The series resistance of the laser diode and the resistor R_S must equal the pulse generator source impedance divided by N^2. Consequently, if the series resistance of the laser diode is relatively high, it then may be necessary to reduce the value of R_S. Model AVX-S3B is designed to match 50 ohm pulse generators such as Model AVO-5-C to 3 ohms and will provide peak diode currents up to 28 amperes. Model AVX-S3C is designed to match Models AVO-2W-C and AVO-2-C (25 ohm source impedance) to load resistance of about 5 ohms and will provide peak diode currents as high as 10 amperes. Model AVX-S3D is designed for use with Model AVO-5B-C and will provide up to 48 amperes of diode current.

Two optional SMA output connectors provide attenuated coincident replicas of the diode current (-MI option) and diode voltage (-MV option) as per the following relationships (Amps, Volts):

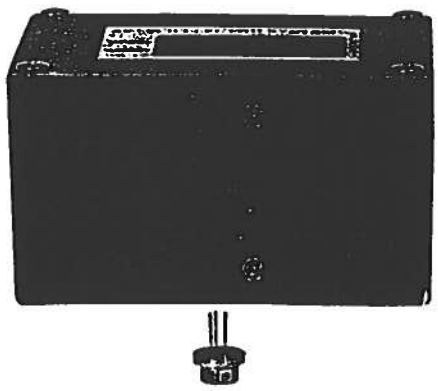
$$I_D = \frac{10\,V_{MI}}{R_S} \qquad V_D = 10\,(V_{MV} - V_{MI})$$

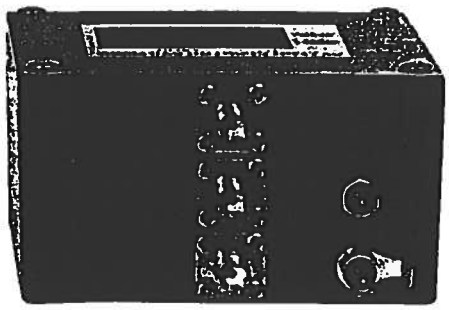
All AVX-S3 units include two foot long input cables with SMA male connectors.

When ordering members of the AVX-S family, the customer must specify the basic model number (eg. AVX-S1) and the following additional information.

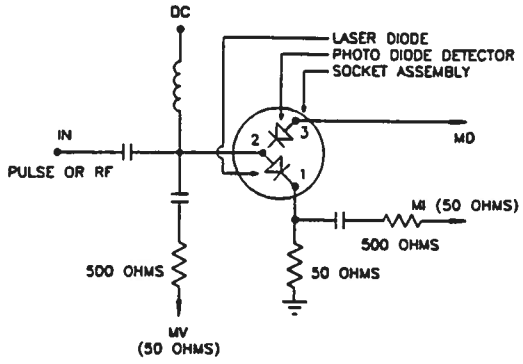
a) Diode package type (eg. TO-18) and the required pin connections (eg. anode, cathode, ground etc). See the following page for readily available package mounting. Contact Avtech for special or different packages.
b) Desired options (eg. -MI, -MV, -MD).

Contact Avtech for your special requirements.

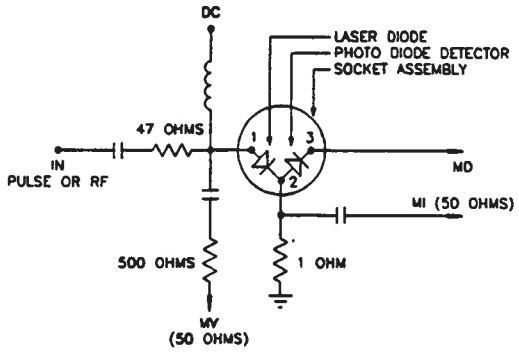




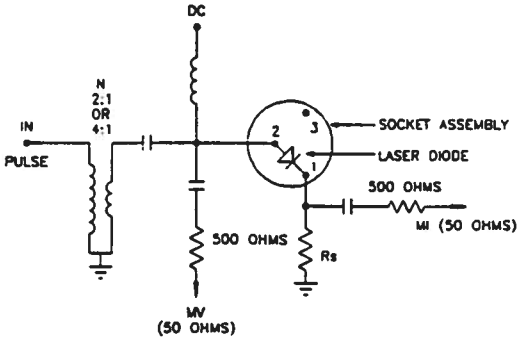
Model:	AVX-S1	AVX-S2	AVX-S3A	AVX-S3B	AVX-S3C	AVX-S3D
Peak diode current:	400 mA	2 Amps	5 Amps	28 Amps	10 Amps	48 Amps
Max. input amplitude:	20 volts	100 volts	150 volts	350 volts	150 volts	150 volts
Pulse width (ns):	0.4 - 200	1 - 1000	2 - 100	2 - 100	2 - 100	5 - 500
Rise time (ns):	0.2	0.5	0.5	1.0	0.5	2.0
Pulse PRF range:	DC - 0.5 GHz	DC - 100 MHz	DC - 10 MHz	DC-10 MHz	DC - 10 MHz	DC - 1 MHz
CW frequency range:	10 MHz - 1.0 GHz	1 - 200 MHz	-	-	-	-
Max. bias current:	100 mA	100 mA	100 mA	100 mA	100 mA	100 mA
Max. bias voltage:	50 volts	50 volts	50 volts	50 volts	50 volts	50 volts
Input impedance:	50 ohms	50 ohms	50 ohms	50 ohms	25 ohms	12 ohms
N:	-	-	2	4	2	4
R_S (ohms):	-	-	10	3	5	0.7
IN connector:	SMA					
Monitor connector:	SMA					
Bias connector:	Solder pin					
Dimensions (H x W x D):	41 mm x 66 mm x 76 mm (1.6 " x 2.6 " x 3.0")					
Material:	Cast aluminum, blue enamel					
Mounting:	Any					



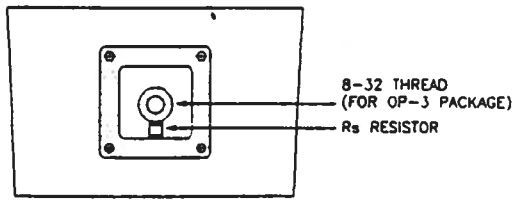
AVX-S1 FUNCTIONAL EQUIVALENT CIRCUIT



AVX-S2 FUNCTIONAL EQUIVALENT CIRCUIT

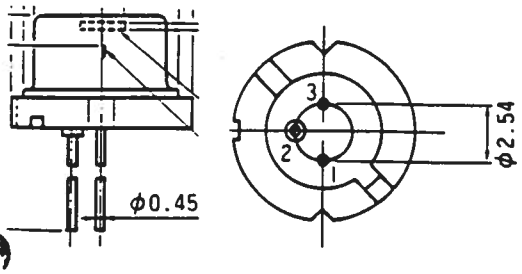


AVX-S3 FUNCTIONAL EQUIVALENT CIRCUIT

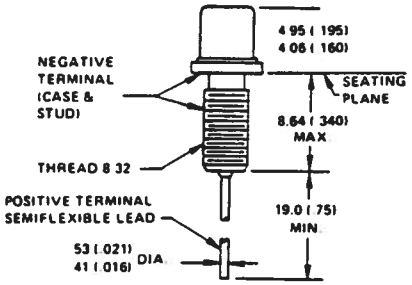


AVX-S3 INPUT ASSEMBLY (FOR OP-3 PACKAGE)

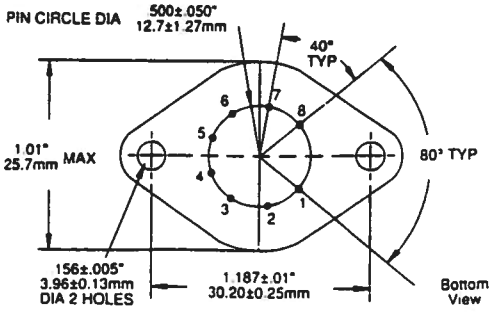
TO-18



OP-3



TO-3 8 PIN



TYPICAL PACKAGES

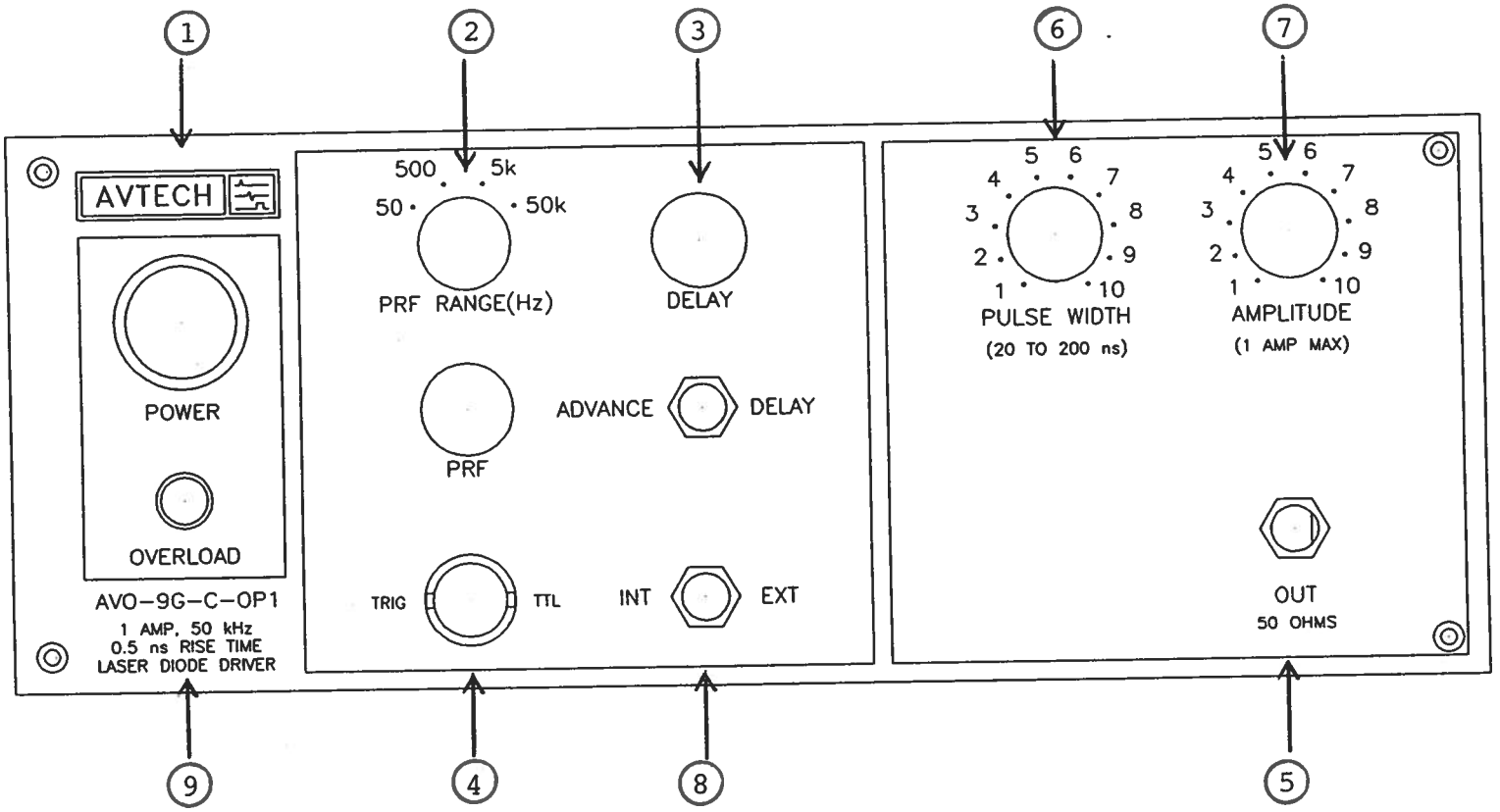


Fig. 3 FRONT PANEL CONTROLS

(1) ON-OFF Switch. Applies basic prime power to all stages.

(2) PRF Control. Varies PRF as follows:

	MIN	MAX
Range 1	5 Hz	50 Hz
Range 2	50 Hz	500 Hz
Range 3	500 Hz	5 kHz
Range 4	5 kHz	50 kHz

The operating PRF should be set using a scope.

(3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (5). This delay is variable over the range of 0 to about 0.2 us. The TRIG output precedes the main output when the ADVANCE-DELAY switch is in the ADVANCE position and lags when the switch is in the DELAY position.

(4) TRIG Output. This output is used to trigger the scope time base. The output is a TTL level 100 ns (approx.) pulse capable of driving a fifty Ohm load.

(5) OUT Connector. SMA connector provides output to a fifty Ohm load.

(6) PW Control. A one turn control which varies the output pulse width from 20 ns to 200 ns.

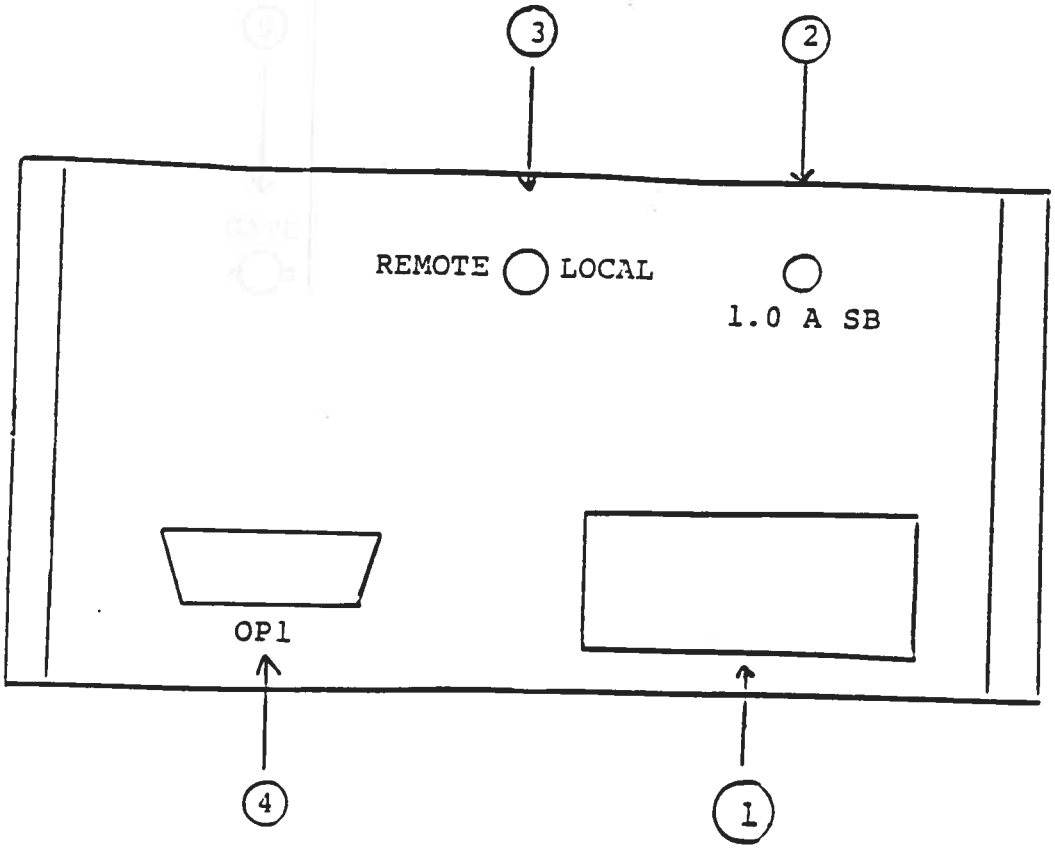
(7) AMP Control. A one turn control which varies the output pulse amplitude to a fifty Ohm load.

(8) EXT-INT Control. With this toggle switch in the INT position, the PRF of the AVO unit is controlled via an internal clock which in turn is controlled by the PRF controls. With the toggle switch in the EXT position, the AVO unit requires a 50 ns (or wider) TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.

(9) OVERLOAD. AVO units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:

1) Reducing PRF (i.e. switch to a lower range)
2) Reducing pulse width (i.e. switch to a lower range)
3) Removing output load short circuit (if any)

FIG 5: BACK PANEL CONTROLS



BACK PANEL CONTROLS

(1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.5A SB).

(2) 1.0A SB. Fuse which protects the output stage if the output duty cycle rating is exceeded.

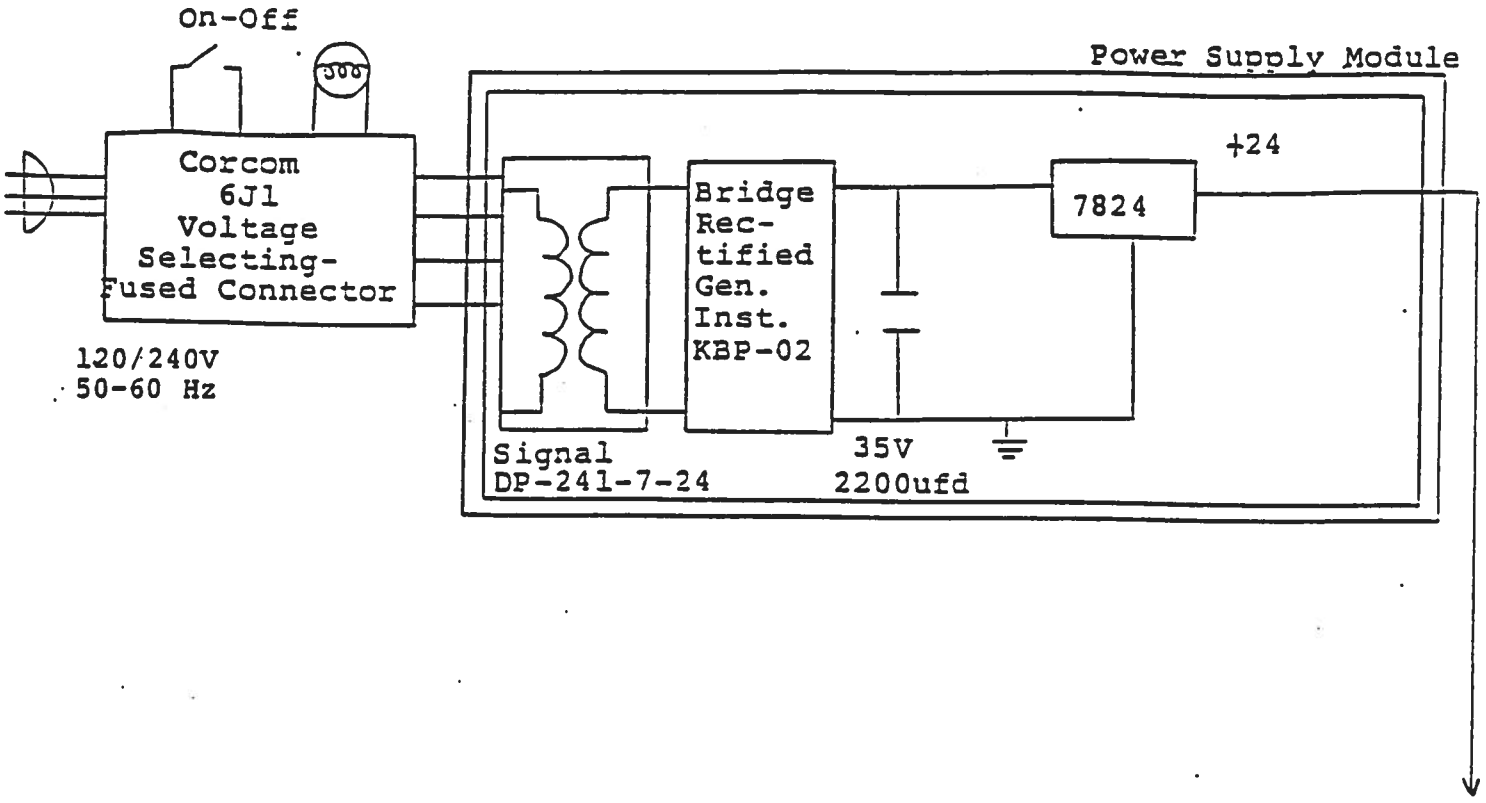
(3) LOCAL REMOTE SWITCH. This two-position switch must be in the LOCAL position to operate this instrument from the front panel controls. To control the instrument using your personal computer, the switch must be in the REMOTE position.

(4) OP1 CONNECTOR. GPIB cable (supplied) connects between this connector and your personal computer.

TOP COVER REMOVAL AND RACK MOUNTING

(1) The interior of the instrument may be accessed by removing the four Phillips screws on the top panel. With the four screws removed, the top cover may be slid back (and off).

Fig. 5 SYSTEM BLOCK DIAGRAM



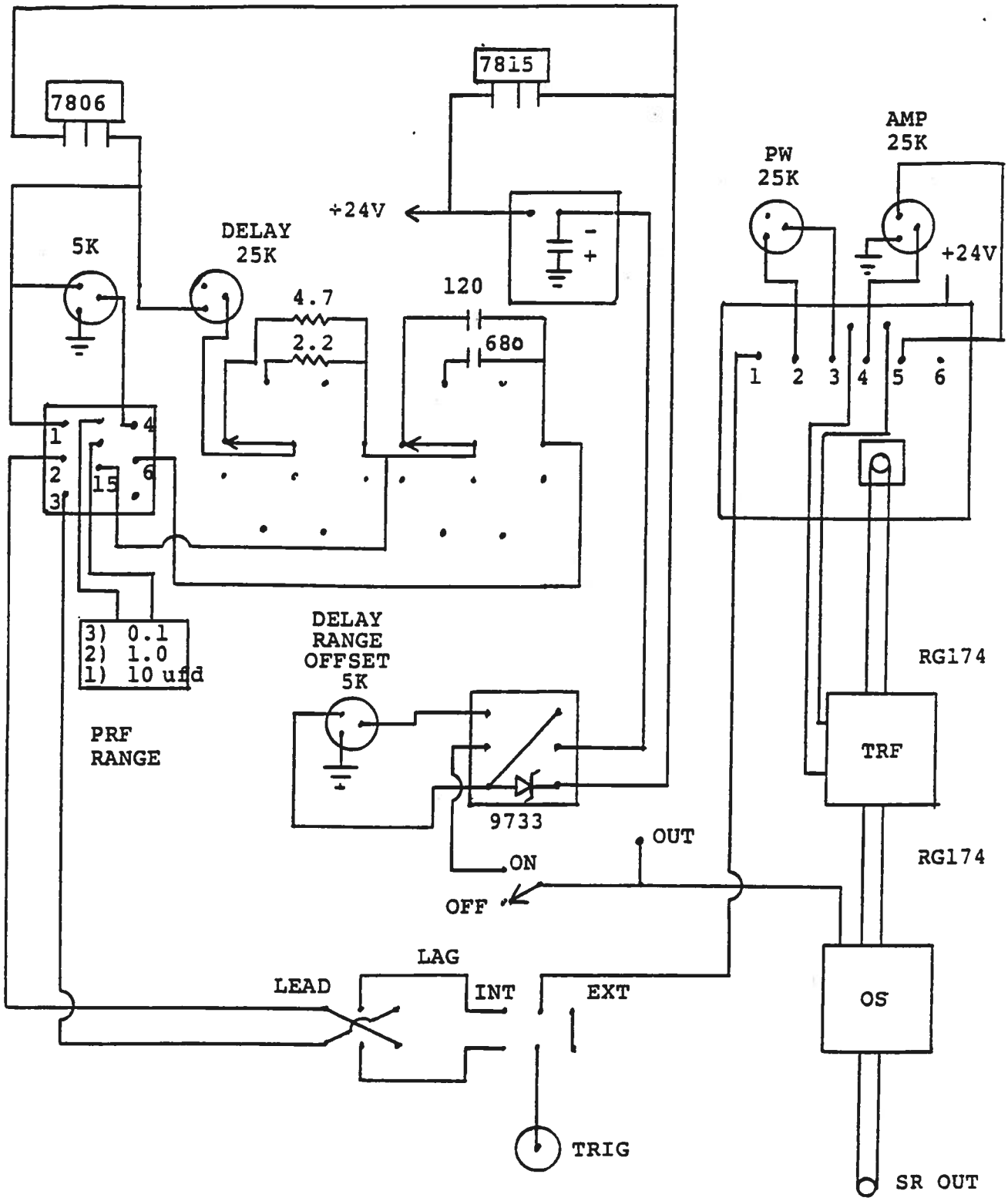


Fig. 6 AVR-E1-W-C-P-OT (GGB MOD)

SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVO-9G-C consists of the following basic modules:

1) AVO-9G-PG pulse generator module
2) AVO-9G-CL clock module
3) +24V power supply board
4) Overload module

The modules are interconnected as shown in Fig. 4.

The clock module controls the output PRF and the relative delay between the main output and the TRIG output. The PG pulse generator modules generate the output pulse. In the event of an instrument malfunction, it is most likely that the rear panel 1.0A SB fuse may have failed due to an output short circuit condition or to a high duty cycle condition. If the fuse is not blown, then the four Phillips screws on the back panel should be removed. The top cover may then be slid off and operation of the clock and power supply modules should be checked. The clock module is functioning properly if:

a) 0.1 us TTL level outputs are observed at Pins 2 and 3.
b) The PRF of the outputs can be varied over the range of 50 Hz to 50 kHz using the PRF controls.
c) The relative delay between the Pin 2 and 3 outputs can be varied by at least 5 us by the DELAY controls.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed. The power supply board generates +24V DC to power the other modules. If the voltage is less than +24V, turn off the prime power and unsolder the lead from the 7824 regulator chip on the power supply board. Solder a 100 Ohm 5 Watt resistor to the 7824 output to ground and turn on the prime power. A voltage of +24 Volts should be read. If the voltage is less then the power supply board is defective and should be repaired or replaced.

1.0 Introduction

This section describes how to use the OP-1 GPIB Bus Listener interface for remote computer control of the Avtech pulse generator, by means of the IEEE 488 General Purpose Interface Bus (GPIB).

The available commands and their structure, a typical command sequence and a sample program are included.

In addition, possible methods of incorporating remote duty cycle limit checking and instructions on how to change the GPIB address are provided

2.0 Interface to the GPIB

The IEEE 488 compatible Bus functions available to the user for GPIB control are as follows: The listed functions define a Bus Listener capability:

- SH0, AH1, T0, TE0, L2, LE0, SR0, RL0, PP0, DC1, DT0, C0.

2.1 Available Commands

The OP-1 GPIB user interface is designed to be used to remotely program the Avtech pulse generator to control the pulse repetition rate, pulse width, pulse amplitude delayed (or advanced) trigger output.

The available command acronyms, outputs, units and range of acceptable values for the AVO-9G-C-OP1 generator are defined in the table below:

Acronym	Output	Units	Range	Decades
I	Peak amplitude	Amps	0 to 1	
R	Repetition rate	Hertz	5 to 50000	4
W	Width of pulse	nano-sec	20-200	1
D	Delay (trigger)	nano-sec	20-200	1
A	Advance (trigger)	nano-sec	20-200	1

For example, for a GPIB controller from National Instruments, the following set of commands would be sent:

```
ibwrt "r=100"
ibwrt "i=0.5"
ibwrt "a=50"
ibwrt "w=50"
```

This command sequence will cause the generator to produce a positive output pulse of width 50 nano-sec and an amplitude of 0.5 amps peak, repeated at a rate of 100 pulses per second. An oscilloscope attached to the generator output will confirm the result. If the generator output trigger port is used, it will be noted that each output pulse will occur 50 nano-sec after the trigger pulse occurs.

2.4 Sample Program

To illustrate the remote control process by means of the GPIB, a sample program written in BASIC is provided. While this example is prepared for use with the B&C MicroSystems PC488 circuit card, the general principles of control apply to any IEEE 488 GPIB Controller.

```
'TEST of Pulser Controller
OPEN "PC488" FOR OUTPUT AS #1
PRINT #1, "ABORT"
PRINT #1, "CLEAR"
PRINT #1, "OUTPUT 8;I", 0.5
PRINT #1, "OUTPUT 8;R", 100
PRINT #1, "OUTPUT 8;A",50
PRINT #1, "OUTPUT 8;W",50
END
```

3.0 Duty Cycle Limits

Typically, Avtech pulse generators are limited to a maximum duty cycle because of thermal constraints, where duty cycle is the ratio of Pulse Width to the reciprocal of the Repetition Rate (i.e.; R times W). Although the generator contains automatic protection against an excessive duty cycle, whenever this protection is activated, the output is inhibited. Therefore, it may be desirable to have the control computer calculate the duty ratio, then generate a warning message to the operator whenever the limits are exceeded (preferably prior to actually sending the command sequence).

This message could caution the user either to reduce the repetition rate or the pulse width, to avoid thermal overload. While this calculation is not mandatory, it could avoid the annoyance of automatic inhibiting of the generator output.

4.0 Changing the Unit GPIB Address

Since the GPIB data bus address for the pulse generator has been preset to "8" in the factory, commands are required to be sent to this address. However, the user may wish to change the address to any address in the allowed range of 0 to 30. This address may be easily changed by re-setting the GPIB address switch on the GPIB Interface board located inside the pulse generator chassis.

The address is set by means of a five position "Dipswitch " located on the top of a small circuit card located inside the enclosure near the top rear. The switch may observed to be set to the default address by noting that the Dipswitch position 4 is set in the OFF position, defining a binary address of 8.

The switch setting is calculated as the sum of the switch weights in the OFF position, calculated as follows: (a switch in the ON position it has a weight of zero):

Switch Number	OFF Weight
1	1
2	2
3	4
4	8
5	16

For example, a switch with positions 1, 4 and 5 set to OFF will result in an address setting of 25 (16 plus 8 plus 1 = 25).

5.0 Trouble-Shooting Aid

In the event that difficulties are encountered communicating via the GPIB interface, two auxiliary communications status indicators have been included on the GPIB interface circuit card. These status indicators are small LED lamps, one which flashes briefly whenever a properly addressed command is received. The second LED will light whenever an out-of-range value or invalid command is received, and will remain lit until a valid command with a valid in-range value is subsequently received.

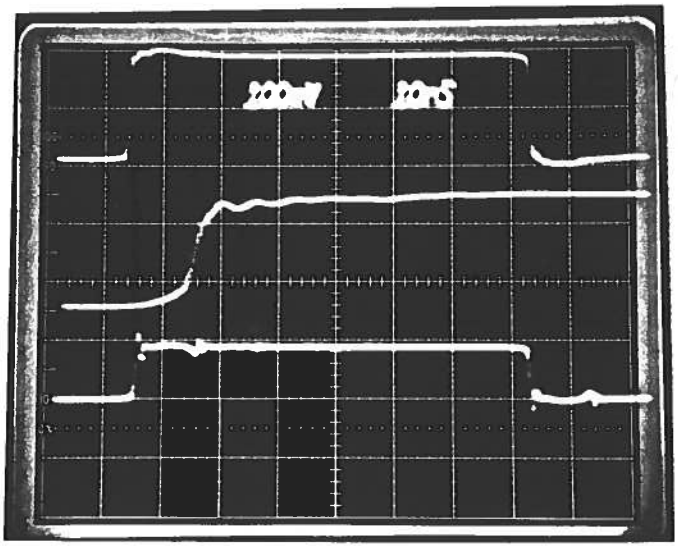
PULSE GENERATOR

PERFORMANCE CHECK

Model: AVO-9G-C-P-OP1.

S.N.: 8203

Date: NOV 4 1997



a) Output signal amplitude: 0 TO +/-0 VOLTS

b) Pulse width: 20 TO 200NS

c) Rise time: ≤ 0.5NS

d) Fall time: ≤ 0.5NS

e) PRF: 0 TO 50 KHZ

f) Jitter, stability: OK

g) Prime power: 120/240V 50-60 HZ.

TOP MAINFRAME OUT TO 50 dB, 10 NS/DIV.

MID AS TOP BUT 1 NS/DIV (RISE TIME)

BOT MI OUT TO 30 dB (1N5819 DIODE LOAD). PRF = 50 KHZ 10 NS/DIV.

November 1997

File: c:\WPINSTR\AN09\9GCOPI.INS