AVTECH ELECTROSYSTEMS LTD.
NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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INSTRUCTIONS

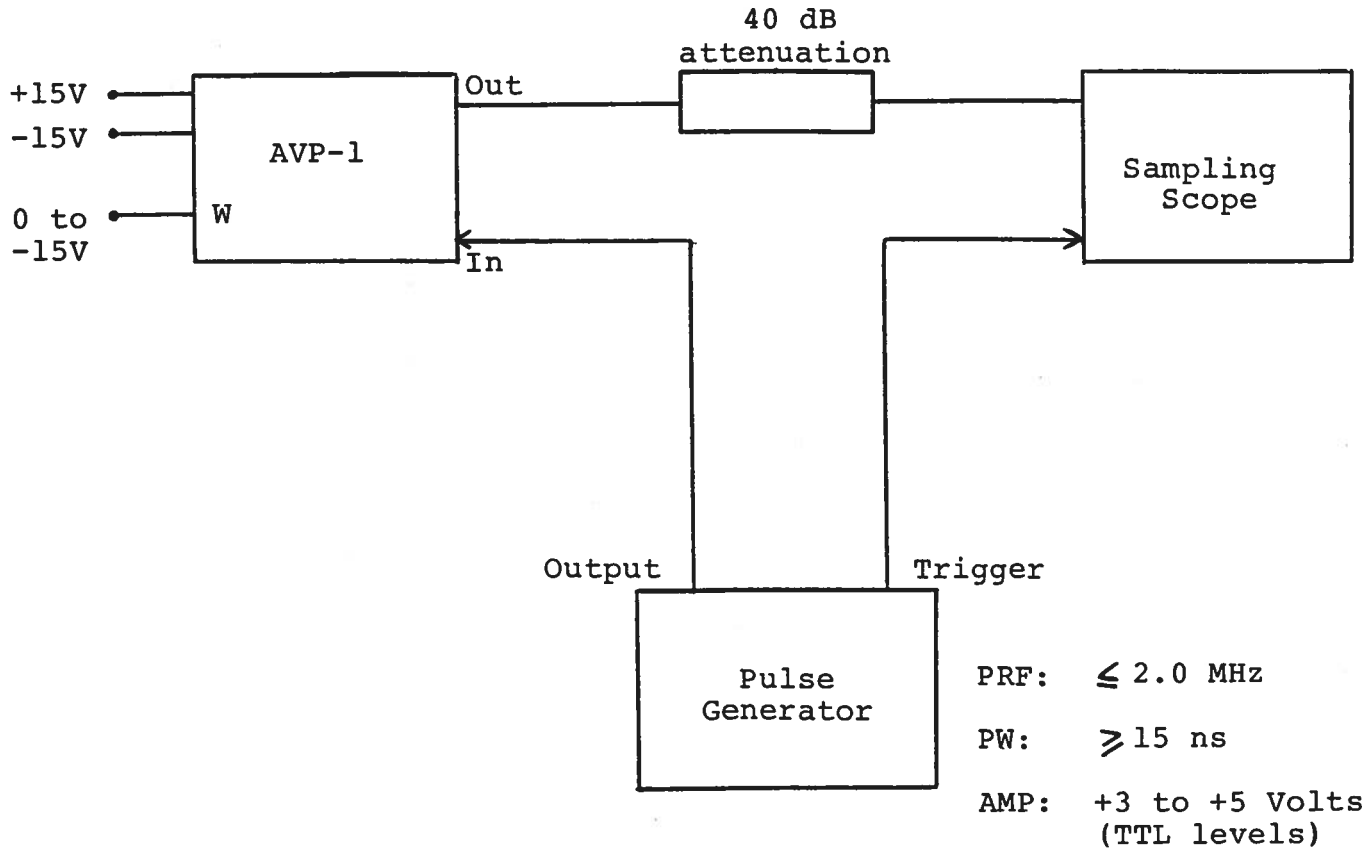
MODEL AVP-1-P-DSRCA1 PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

PULSE GENERATOR TEST ARRANGEMENT



Notes:

1) The bandwidth capability of components and instruments used to display the impulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.

2) The use of 40 dB attenuation will insure a peak input signal to the sampling scope of less than one Volt.

3) In general, the pulse generator trigger delay control should be set in the 100 ns range. Other settings should be as shown in the above diagram. The impulse generator output is delayed with respect to the trigger input signal by about 30 ns. (typically).

4) The output amplitude is fixed at +10V.

5) The output pulse width is controlled by the 0 to -15 VDC applied to the W solder terminal (R_{IN} > 500 Ohms).

6) The PRF is controlled by the input trigger PRF. CAUTION: The unit may be damaged if the PRF exceeds 2.0 MHz.

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Fax Ref No: 10810

From: Avtech Electrosystems Ltd.

To: David Sarnoff

Our Fax No: (613) 226-2802

Research Ctr.

Date: May 10, 1995

Attn: Arye Rosen
Tel: 609-734-2927

Receivers Fax No: 609-734-2050

Subject: Quotation

No. of pages: 2

Following our telephone conversation of May 9th, I am pleased to offer the following revised price and delivery quotation:

Model designation:	AVP-1-P-DSRCA1
Output amplitude:	≥ 10 Volts (fixed)
Pulse width:	0.25 to 1.0 ns. Controlled by applying 0 to -15 Volts to a solder terminal (R_{IN} ≈ 500 Ohms).
Rise time:	≤ 100 ps
Fall time:	≤ 150 ps
PRF:	2 MHz. Controlled by input trigger PRF
Input trigger:	TTL, 15 ns ≤ PW ≤ 200 ns
Jitter:	No higher than for AVM-1-C unit originally supplied to DSRC
Prime power:	a) +15 VDC, 100 mA b) -15 VDC, 20 mA
Chassis size:	1.1" x 1.3" x 2.3" (Avtech style B, see page 96, Cat. No. 9)

Chassis material:	Cast aluminum, blue enamel
Connectors:	Trigger: SMA Output: SMA Power: Solder terminals
Price:	$998.00 US each for a quantity of 16, FOB destination ($1,498.00 US for a quantity of one)
Delivery:	≤ 60 days

Rgds

Dr. Walter Chudobiak
Chief Engineer

WC:pr

June 7/95

Disk: AVP-AV-1

Name: 1PDSRCA1.INS