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INSTRUCTIONS

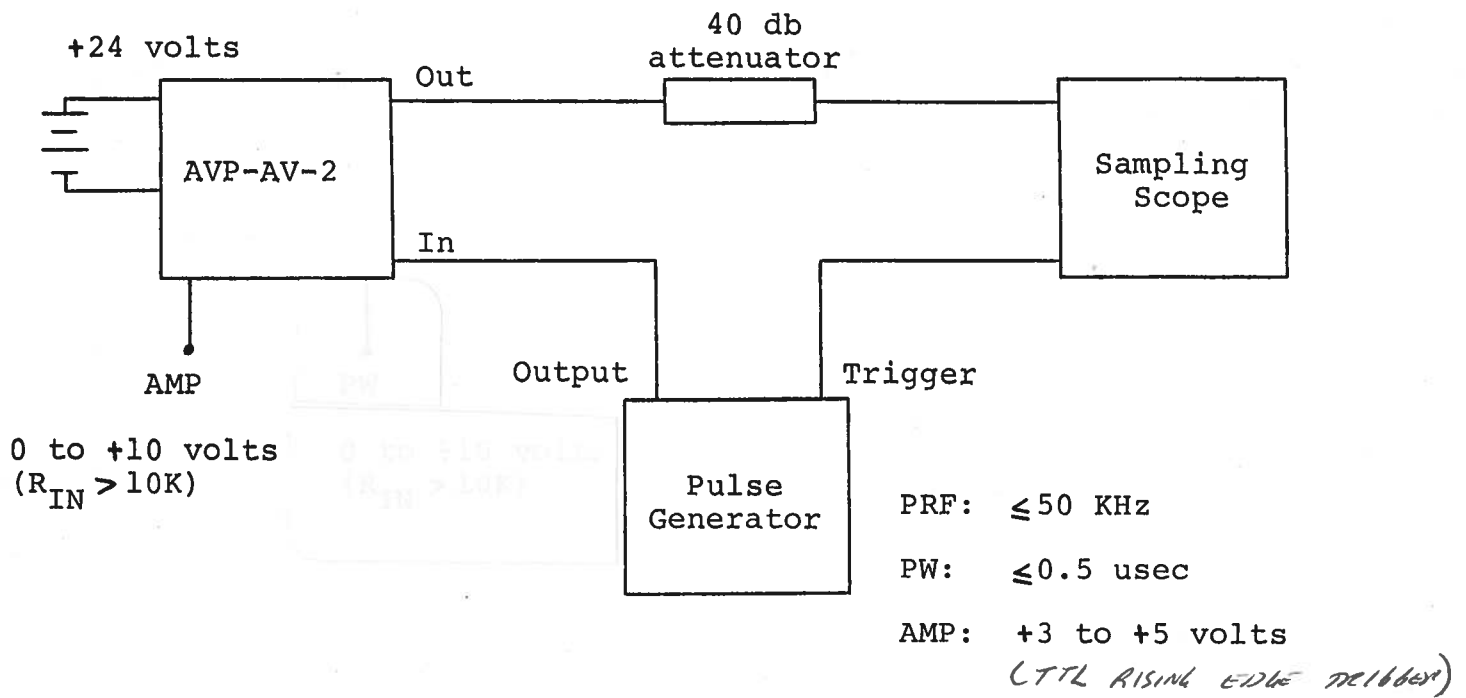
MODEL AVP-AV-2-EA-W-OS-M PULSE GENERATOR

S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVP-AV-2 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 40 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.
- 4) The Model AVP-AV pulse generator can withstand an infinite VSWR on the output port.
- 5) WARNING: Model AVP-AV may fail if triggered at a PRF greater than 50 KHz.
- 6) The output pulse width is controlled by means of a one turn pot control (PW).
- 7) The output pulse amplitude is controlled by means of a 0 to +10 volt DC signal applied to the AMP solder terminal ($R_{IN} \geq 10K$).
- 8) Some properties of the output pulse may change as a function of the amplitude pot setting. For some demanding applications, it may be desirable to use a combination of external attenuators and the amplitude pot to achieve the desired output amplitude.
- 9) To DC offset the output pulse connect a DC power supply set to required DC offset value to the terminals marked O.S. The maximum attainable DC offset voltage is ± 50 volts. (option).
- 10) The monitor output (-M) provides a 20 db attenuated coincident replica of the main output. (option).

- 1) The bandwidth capabilities of components and their limits must be checked. The power generated output signal transformers, cables, connectors, etc. should be checked in advance.
- 2) The use of 40 Ohm attenuator will cause a peak input signal to the scope of 75V, then the unit.
- 3) In general, the source pulse generator trigger delay control should be set to 0.1 to 1.0 use range. Trigger timing should be as shown in the above diagram.
- 4) The Model 207-AV pulse generator can withstand an output VSWR on the output port.
- 5) WARNING: Model 207-AV may fail if subjected to a PRF greater than 50 kHz.
- 6) The output pulse width is controlled by means of a pulse rate control (PRC).
- 7) The output pulse amplitude is controlled by means of a 0 to 10 volt DC signal applied to the AM solder terminal (pin 2 10V).
- 8) Some properties of the output pulse may change as a function of the amplitude and setting. For some demanding applications, it may be desirable to use a combination of external attenuators and the amplitude control to achieve the desired output amplitude.
- 9) To test the output pulse against a DC power supply, set to required DC output value in the terminal marked D.C. The maximum available DC output voltage is 250 volts (option).
- 10) The monitor output (M) provides a 20 db attenuated convenient replica of the main output (output).