AVTECH ELECTROSYSTEMS LTD.

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INSTRUCTIONS

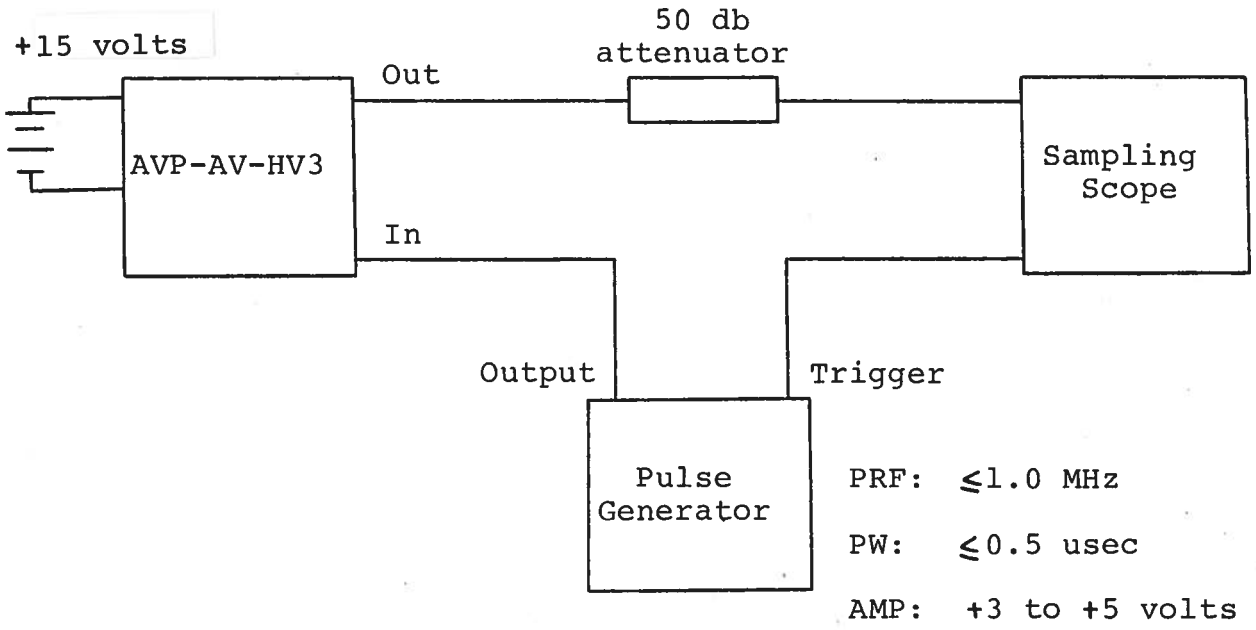
MODEL AVP-AV-HV3-EA-EW PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVP-AV-HV3 PULSE GENERATOR TEST ARRANGEMENT



Notes:

1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.

2) The use of 50 db attenuator will insure a peak input signal to the sampling scope of less than one volt.

3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.

4) The Model AVP-AV pulse generator can withstand an infinite VSWR on the output port.

5) WARNING: Model AVP-AV may fail if triggered at a PRF greater than 1.0 MHz.

6) The output pulse amplitude and pulse width are controlled by DC voltages (0 to +10V, $R_{IN} \geq 10K$) applied to the AMP and PW solder terminals.

7) To DC offset the output pulse connect a DC power supply set to required DC offset value to the terminals marked O.S. The maximum attainable DC offset voltage is ± 50 volts. (option).

8) The monitor output port (M) provides a coincident attenuated (x10) replica of the main output to a 50 ohm load. (option).

09.13.85

-OS

-M