

# AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS  
ENGINEERING . MANUFACTURING

□ P.O. BOX 265  
OGDENSBURG  
NEW YORK  
13669  
(315) 472.5270

BOX 5120 STN. "F"  
OTTAWA, ONTARIO  
CANADA K2C 3H4  
(613) 226-5772  
TELEX 053.4591

## INSTRUCTIONS

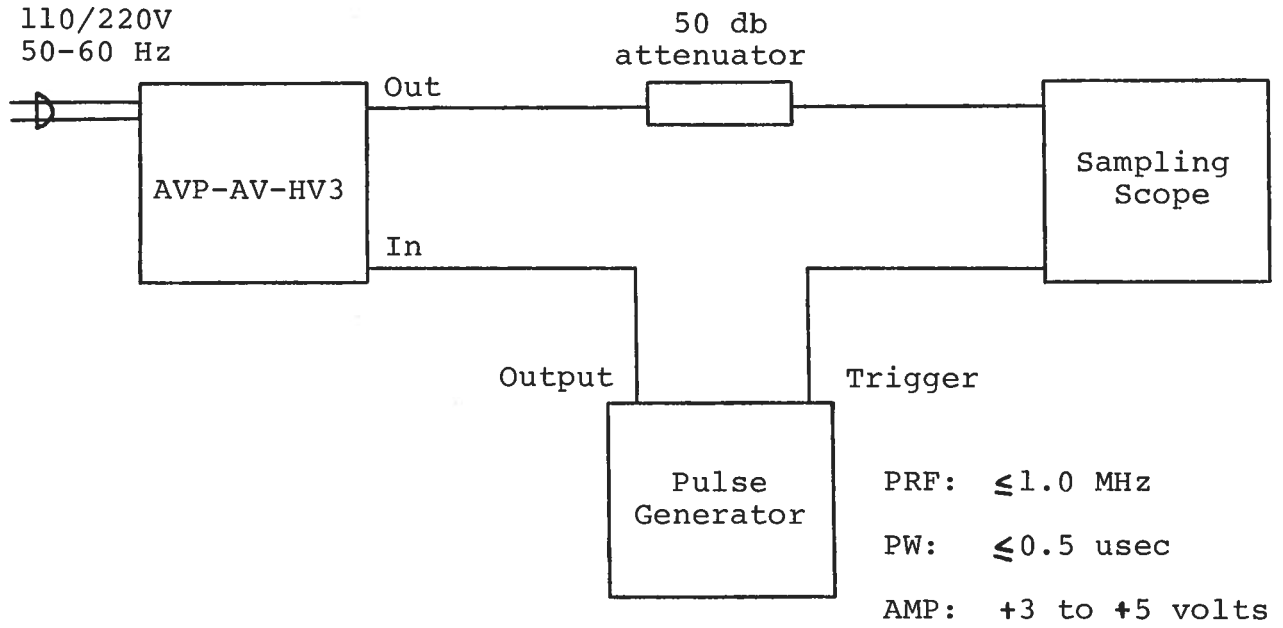
MODEL AVP-AV-HV3-PS PULSE GENERATOR

S.N.:

## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVP-AV-HV3-PS PULSE GENERATOR TEST ARRANGEMENT



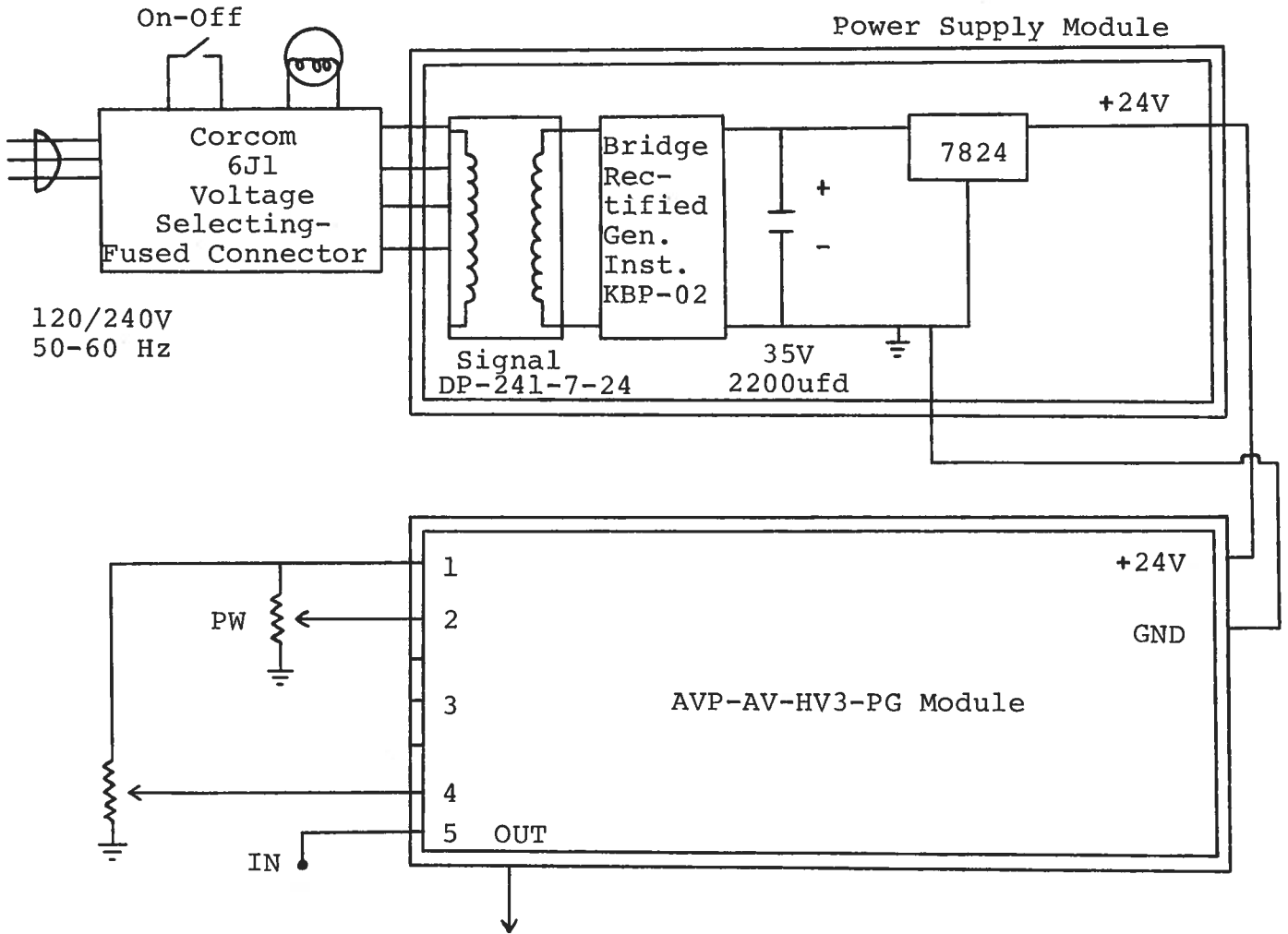
Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 50 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.
- 4) The Model AVP-AV pulse generator can withstand an infinite VSWR on the output port.
- 5) WARNING: Model AVP-AV may fail if triggered at a PRF greater than 1.0 MHz.
- 6) The output pulse width is controlled by means of the one turn potentiometer (PW). The pot should initially be set maximum clockwise and the pulse width adjusted using an oscilloscope.
- 7) The output pulse amplitude is controlled by means of the one turn potentiometer (AMP). The pulse width may change by several nanoseconds as the output amplitude is reduced from maximum to minimum. Therefore it is convenient to first set the desired amplitude and then set the desired pulse width. Rotation of the PW pot causes the position of the falling edge of the pulse to change.
- 8) Some properties of the output pulse may change as a function of the amplitude pot setting. For some demanding applications, it may be desirable to use a combination of external attenuators and the amplitude pot to achieve the desired output amplitude.
- 9) To DC offset the output pulse connect a DC power supply set to required DC offset value to the terminals marked O.S. The maximum attainable DC offset voltage is 50 volts. (option).
- 10) The unit can be converted from 110 to 220V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector cable connector assembly.
- 11) The P OUT port provides a positive output pulse. To obtain a negative output pulse, connect the P OUT port to the N IN port using a short length of 50 ohm semi-rigid cable. A negative pulse is then obtained at the N OUT port. (PN option).

## SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVP-AV-HV3-PS consists of a pulse generator module (AVP-AV-HV3-PG) and a power supply board which supplies +24 volts (600 mA max) to the pulse generator module. In the event that the unit malfunctions, remove the instrument cover by removing the four Phillips screws on the back of the unit. The top lid may then be slid off. Measure the voltage at the +24V pin of the PG module. If this voltage is substantially less than +24 volts, unsolder the line connecting the power supply and PG modules and connect 50 ohm 10 W load to the PS output. The voltage across this load should be about +24V DC. If this voltage is substantially less than 24 volts the PS module is defective and should be repaired or replaced. If the voltage across the resistor is near 24 volts, then the PG module should be replaced or repaired. The sealed PG module must be returned to Avtech for repair (or replacement).

SYSTEM BLOCK DIAGRAM



Schroff

07.27.84

-PN

-OS

