



AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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INSTRUCTIONS

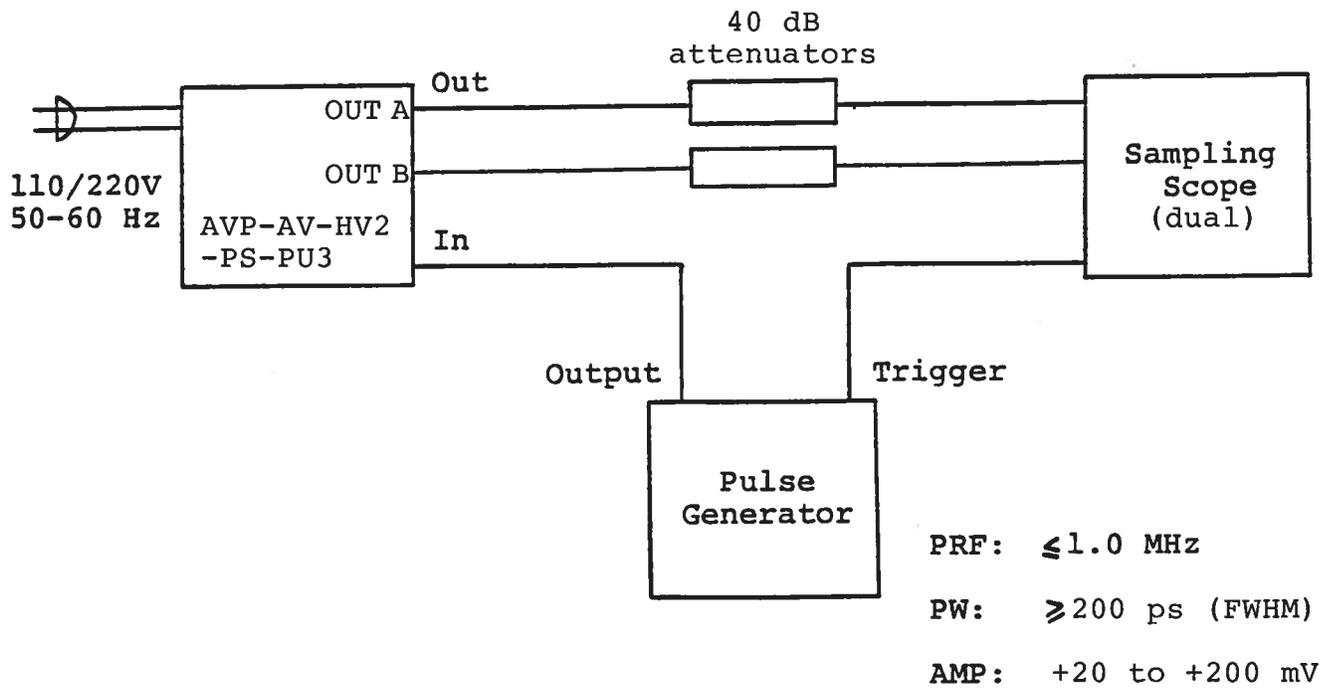
MODEL AVP-AV-HV2-PS-P-PU3 PULSE GENERATOR

S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVP-AV-HV2-PS-P-PU3 PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 40 dB attenuator will insure a peak input signal to the sampling scope of less than one Volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 us range. Other settings should be as shown in the above diagram.
- 4) WARNING: Model AVP-AV-HV2-PS-PU3 may fail if triggered at a PRF greater than 1.0 MHz.
- 5) The output pulse width for each channel is controlled by means of the front panel one turn PW control.
- 6) The output pulse amplitude for each channel is controlled by means of the front panel one turn AMP control. Some properties of the output pulse may change as a function of the amplitude pot setting. For some demanding applications, it may be desirable to use a combination of external attenuators and the amplitude pot to achieve the desired output amplitude.
- 7) The delay of output B, with respect to output A, may be varied from 0 to 2 ns by means of the one turn DELAY control.
- 8) The AVP unit can be converted from 110 to 220V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
- 9) For additional assistance:

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SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVP-AV-HV2-PS-PU3 consists of two pulse generator modules (-PGA and -PGB), a -15V DC module (PS-15), a preamp-limiter module (-AMP) and a power supply board which supplies +15 Volts and +24 Volts to the pulse generator modules. In the event that the unit malfunctions, first check the line fuse and then remove the instrument cover by removing the four Phillips screws on the top of the unit. The top lid may then be slid off. Measure the voltages at the +15V and +24V pins of the PS PCB. If the voltages are less than +15V or +24V, the unit should be returned to AVTECH for repair.

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Fax Ref No: 168 From: Avtech Electrosystems Ltd.

To: Princeton University Our Fax No: (613) 226-2802

Date: January 25/96

Attn: June-koo Rhee
Tel 609-258-6366 Receivers Fax No: 609-258-6746

Subject: Quotation No. of pages: 2

Following our telephone conversation of January 25th, I am pleased to offer the following revised price and delivery quotation:

Model designation: AVP-AV-HV2-P-PS-PU3.
Provides two outputs, each with its own amplitude control and a relative delay control (one turn, 2 ns).

Output amplitude: 0 to +10 Volts (one turn control, unit will operate to +12 Volts but rise time may increase).
(to 50 Ohms)

Rise time: ≤ 60 ps (fixed).

Fall time: ≥ 1 ns.

Pulse width: ≈ 1 ns.

Input trigger: Amplitude: 20 mV to 200 mV
Pulse width: 200 ps FWHM.

PRF: 0 to 1 MHz.

Propagation delay: ≈ 40 ns.

Jitter: ± 20 ps.

Other: See standard Model AVP-AV-HV2-PS.

Price:

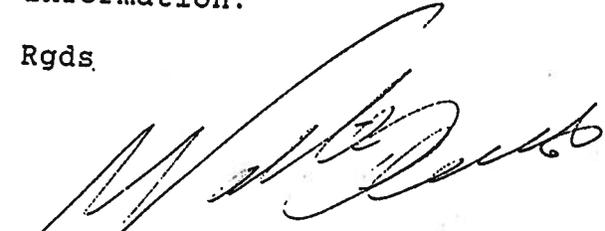
\$4,396.00 US each, FOB destination.
Please note that this price includes our
standard 5% academic discount.

Delivery:

60 - 90 days ARO.

Thank you for your continuing interest in our products. Please
call me (1-800-265-6681) if you require any additional
information.

Rgds



Dr. Walter Chudobiak
Chief Engineer

WC:mhd

May 30/96

Disk: AVP-AV-HV

Name: HV2PSPU3.INS