



# AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS  
SINCE 1975

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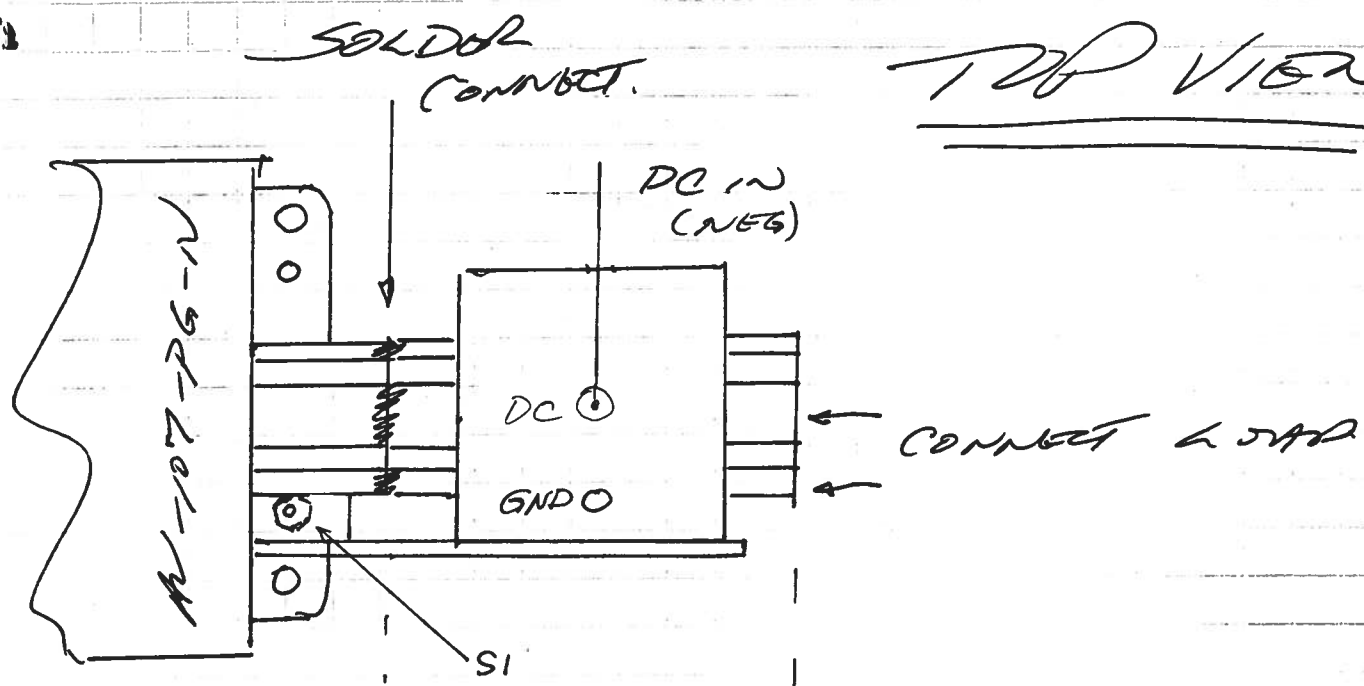
## MODEL AVX-T-N-NLXA BIAS TEE

### INSTRUCTIONS

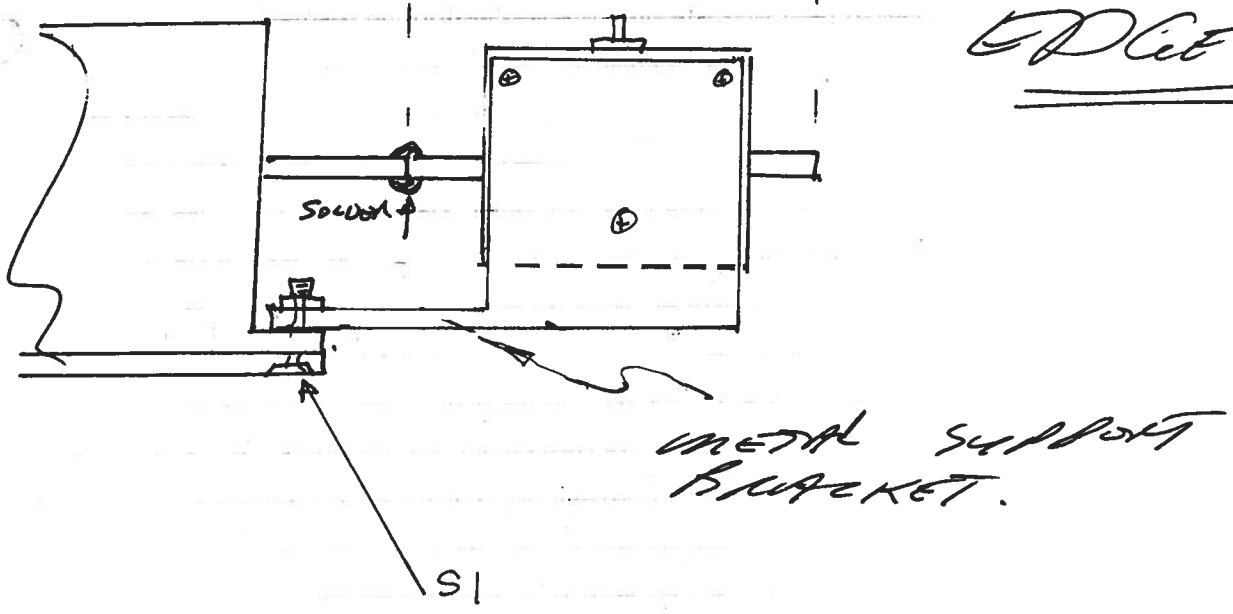
- 1) Remove original counter sunk 3/8" 6-32 machine screw SI. (see attached drawing).
- 2) Install new 3/4" 6-32 screw in this position.
- 3) Align and mate AV-107C-PG-N output PCB and AVX-T-N-NLXA input PCB while mating new SI screw through hole in the AVX-T-N-NLXA mounting bracket.
- 4) Securely tighten the nut on the new SI screw.
- 5) Solder the two PCB together as shown in attached drawing.
- 6) Solder connect the load.
- 7) Apply a trial DC current and check for expected results.
- 8) Reduce DC current to zero and apply a pulse and check for the desired result.
- 9) With the pulse still applied, increase the DC current from 0 to the desired value and check for the expected result.
- 10) **CAUTION:** The driver may be damaged if the DC bias is applied when the load is open-circuited or if the DC load voltage exceeds -60 Volts.
- 11) For additional assistance:

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TOP VIEW



EDGE VIEW



AUX-T-N-NLXA

DEC 99