#### AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS ENGINEERING - MANUFACTURING

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INSTRUCTIONS

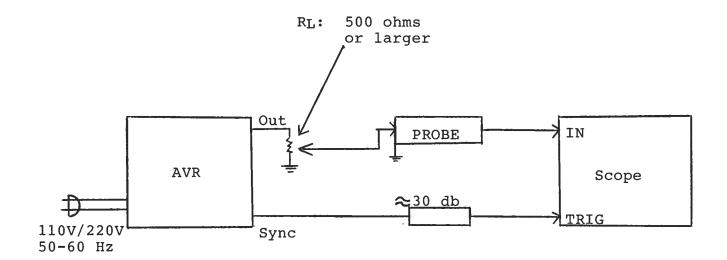
MODEL AVR-1-C-MSU1 PULSE GENERATOR

S.N.:

#### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.





#### Notes:

- The equipment should be connected in the general fashion shown above. Since the AVR unit provides an output pulse rise time as low as 5 nsec a fast oscilloscope (at least 50 MHz and preferably 200 MHz) should be used to display the waveform.
- 2) The sync output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some scopes, a 30 db attenuator should be placed at the input to the scope trigger channel.
- 3) To obtain a stable output display the PW and PRF controls on the front panel should be set maximum counter-clockwise while the front panel PRF RANGE switch may be in either range. The front panel TRIG toggle switch should be in the INT position. The DELAY controls and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF control and by means of the PRF RANGE switch.
- 4) The output pulse width is controlled by means of the front panel one turn PW control. The control should initially be set maximum counter-clockwise and the pulse width adjusted using an oscilloscope.
- 5) The output pulse amplitude is controlled by means of the front panel one turn AMP control.
- 6) An external clock may be used to control the output PRF of the AVR unit by setting the front panel TRIG toggle switch in the EXT position and applying a 0.2 usec (approx.) TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the SYNC output.
- 7) The AVR features a rise time of 5 nsec and an output impedance of the order of several ohms (rather than 50 ohms). The following consequences of this feature should be noted:
  - a) The output switching elements may fail if the unit is inadvertently operated into a short circuit. The switching elements are easily replaced in the field following the procedure outlined in the REPAIR Section.
  - b) The unit is designed to drive only high impedance loads (eg. > 500 ohms). Overshoot and ringing will increase dramatically as load capacitance and the length of 50 ohm output cable is increased. Load capacitance should be less than 50 pfd and the load cable less than a few feet.

### FRONT PANEL CONTROLS

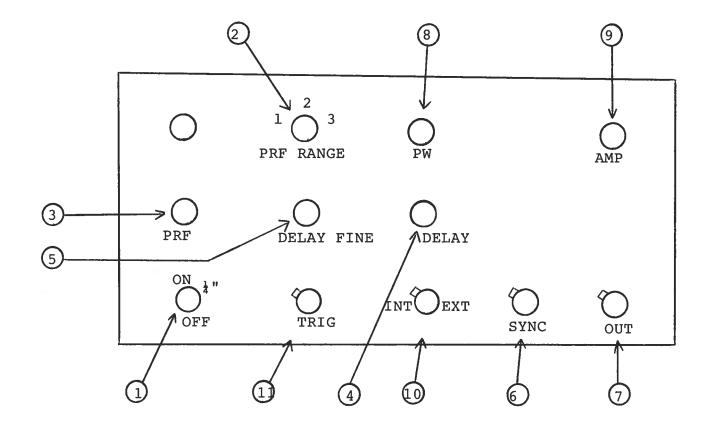


Fig. 2

(1) ON-OFF Switch. Applies basic prime power to all stages.

(2) <u>PRF Control</u>. Varies PRF from 10 Hz to 10 KHz as (3) follows:

 Range 1
 10
 Hz
 to
 100
 Hz

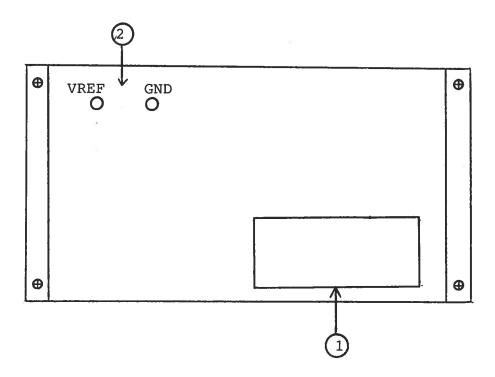
 Range 2
 10
 Hz
 to
 1
 KHz

 Range 3
 100
 Hz
 to
 10
 KHz

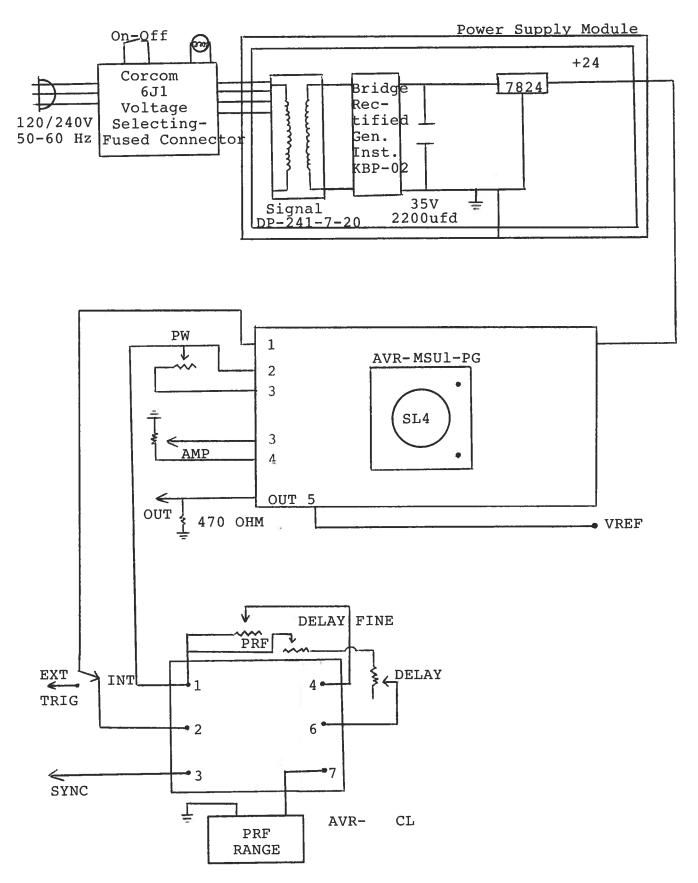
- (4) <u>DELAY Control</u>. Controls the relative delay between the reference output pulse provided at the SYNC output (6) and the main output (7). This delay is variable over the range of 0 to at least 500 nsec.
- (5) <u>DELAY FINE</u>. As DELAY (4) but about 5 times less sensitive.
- (6) <u>SYNC Output</u>. This output precedes the main output (7) and is used to trigger the scope time base. The output is a TTL level 100 nsec (approx.) pulse capable of driving a fifty ohm load. The relative delay between the SYNC output and the main output is variable from 0 to 500 nsec using the DELAY controls.
- (7) <u>OUT Connector</u>. BNC connector provides output to a high impedance load (> 500 ohms).
- (8) <u>PW Control</u>. A one turn control which varies the output pulse width from 0.1 usec to 2 usec.
- (9) <u>AMP Control</u>. A one turn control which varies the output pulse amplitude from 0 to 200V.
- (10) <u>EXT-INT Control</u>. With this toggle switch in the INT position, the PRF of the AVR unit is controlled via an internal clock which in turn is controlled by the PRF and PRF FINE controls. With the toggle switch in the EXT position, the AVR unit requires a 0.2 usec TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.
- (11) <u>TRIG Input</u>. The external trigger signal is applied at this input when the EXT-INT toggle switch is in the EXT position.



## BACK PANEL CONTROLS



- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse.
- (2) <u>VREF.</u> DC output voltage with magnitude equal to peak magnitude of output pulse. Minimum load impedance of 500K. <u>CAUTION</u>: DC voltages as high as +220V appear at this terminal.



The AVR consists of the following basic modules:

- 1) AVR- PG pulse generator module
- 2) AVR- CL clock module
- 3) +24V power supply board

The modules are interconnected as shown in Fig. 4. The clock module controls the output PRF and the relative delay between the main output and the SYNC outputs. The PW pulse generator module generates the output pulse. In the event of an instrument malfunction, it is most likely that some of the output switching elements (SL4) may have failed due to an output short circuit condition or to a high duty cycle condition. The switching elements may be accessed by removing the cover plate on the bottom side of the First turn off the prime power. instrument. NOTE: The elements may be removed from their sockets by means of a needle nosed pliers. The SL4 is a selected VMOS power transistor in a TO 220 packages and may be checked on a curve tracer. If defective, replacement units should be ordered directly from Avtech. When replacing the SL4 switching elements, take care to insure that the short lead (of the three leads) is adjacent to the black dot on the chassis. If the switching elements are not defective, then the four Phillips screws on the back panel should be removed. The top cover may then be slid off and operation of the clock and power supply modules should be checked. The clock module is functioning properly if:

- a) 0.1 usec TTL level outputs are observed at pins 2 and 3.
- b) The PRF of the outputs can be varied over the range of 0.1 KHz to 10 KHz using the PRF, PRF FINE and PRF RANGE controls.
- c) The relative delay between the pin 2 and 3 outputs can be varied by at least 500 nsec by the DELAY controls.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed. The power supply board generates +24V DC to power the other modules. If the voltage is less than +24V, turn off the prime power and unsolder the lead from the 7824 regulator chip on the power supply board. Solder a 100 ohm 5 watt resistor to the 7824 output to ground and turn on the prime power. A voltage of +24 volts should be read. If the voltage is less then the power supply board is defective and should be repaired or replaced.

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