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## INSTRUCTIONS

WAFRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled; modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

Fig. 1 PULSE GENERATOR TEST ARRANGEMENT


1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed 100 MHz .
2) The SYNC output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some scopes, a 30 db attenuator should be placed at the input to the scope trigger channel. The SYNC output precedes the main output when the front panel LEAD-LAG switch is in the LEAD position. The SYNC output lags the main output when the switch is in the LAG position.

The DELAY contral contrals the relative delay between the reference output pulse provided at the sYNC output and the main output. This delay is variable over the range of 0.1 us to $100 \mathrm{us}$.
MIN MAX

| Range 1 | 0.1 us | 1.0 us |
| :--- | :--- | :--- | :--- |
| Range 2 | 1.0 us | 10 us |
| Range 3 | 10 us | 100 us |

§) The output pulse width is controlled by means of the front panel one turn $P W$ contral and by the PW RANGE contral. The minimum and maximum FW for each range and the corresponding maximum PRF are as follows. Note that the unit may fail if operated at duty cycles exceeding the above.

| Range 1 | PW min | PW max |  |
| :---: | :---: | :---: | :---: |
|  | 0.145 | 1.0 us |  |
|  | PRF max 10 kHz | PRF max 10 | kHz |
| Range 2 | 1.045 | 10 us |  |
|  | PRF max 10 kHz | PRF max 1 | kHz |
| Range 3 | 10 usFRF max 1 kHz | 100 us |  |
|  |  | PRF max 100 | Hz |

To voltage control the output pulse width within each range, set the rear panel switch in the EXT position and apply 0 to +10 volts between terminal $A$ and ground (Rin $>10 k$ ). (option).
4) To obtain a stable output display the PRF control on the front panel should be set mid range. The front panel TRIG toggle switch should be in the INT position. The DELAY controls and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF and PRF FINE controls.
5) The output pulse amplitude is controlled by means of the front panel one turn AMP control. To voltage control the output amplitude, set the rear panel switch in the EXT position and apply 0 to +10 volts between terminal $A$ and ground (Rin > 1OK). (option).
6) The output polarity is controlled by the two position polarity switch. CAUTION: To avoid stressing of the output stage it is recommended that the amplitude be reduced to near zero before changing the output polarity.
7) An external clock may be used to control the output PRF of the AVR unit by setting the front panel TRIG toggle switch in the EXT position and applying a 0.2 us (approx.) TTL level pulse to the SYNC BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the SYNC output.
8) For single pulse manual operation, set the front panel INT-EXT-MAN switch in the MAN position and push the SINGLE FLLLSE button.
9) The AVR-5B-C features an output impedance of the order of several ohms (rather than 50 Ohms). The following consequences of this feature should be noted:
a) When used to switch some semiconductor devices (eg. bipolar and UMOS power transistors), the AVR unit will yield much faster switching times than those provided by 50 Ohm pulse generators.
b) The AVR unit will safely operate in to load impedances in the range of 50 Ohms to an open circuit. However, the fall time may degrade for load impedances higher than fifty Ohms.
c) The AVR unit may be effectively converted to a fifty Ohm output impedance generator by placing a fifty Ohm carbon composition resistor in series with the output of the unit and the load. The maximum available load voltage will then decrease to 250 volts (from 500 volts).
d) The output switching elements may fail if the unit is inadvertently operated into a short circuit. The switching elements are easily replaced in the field fallowing the procedure outlined in the REPAIF Section.
10) CAUTIQN: The qutput stage is protected against overload condition by a 2.0 A 51 ow blow fuse on the main frame back panel. However, the output switching elements may fail if the unit is triggered at a FFF exceeding 1 kHz or at duty cycles resulting in an average output power in excess of 50 watts. Heating and subsequent likely failure of the output stage is reduced if the following action is taken where possible:
a) PRF is kept to a minimum, i.e. operate in a low PRF range when possible rather than in a high PRF range. b) Keep the output PW to a minimuma
11) $\quad V E F L Q A D$ INDICATOR. AVR-5-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument $D F F$ and turn the indicator light $\square N$. The light will stay $\square N(i . e . \quad$ output $O F F$ ) for about 5 seconds after which the instrument will attempt to turn $\square N$ (i.e. light $O F F$ ) for about 1 second. If the overload condition persists, the instrument will turn OFF again 〈i.e. light ON) for another 5 secands. If the overload condition has been removed, the instrument will turn on and resume normal operation. Dverload conditions may be removed by:

1) Feducing FFF (i.e. switch to a lower range)
2) Reducing pulse width (i.e. switch to a lower range)
3) Removing output load short circuit (if any)
4) The unit can be converted from 110 ta $220 \mathrm{~V} 50-60 \mathrm{~Hz}$ operation by adjusting the voltage selector card in the rear panel fused voltage selector cable connector assembly.

(1) $\quad$ (N-OFF Switch. Applies basic prime power to all stages.
(2) FRF Control. Varies FRF from 0.1 Hz to 10 kHz as follows:

| Range 1 | 5 | Hz | 50 | Hz |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Range 2 | 20 Hz | 200 Hz |  |  |
| Range 3 | 100 Hz | 1 | kHz |  |
| Range 4 | 1 | kHz | 10 | kHz |

(3) DELAY Control. Controls the relative delay between the reference output pulse provided at the SVNC output (4) the main output (5). This delay is variable over the range of 0.1 to about 100 us. Delay LEADS or LAGS depending on the pasition of the LEAD-LAG switch.

MIN MAX

| Range 1 | 0.1 us | 1.0 us |
| :--- | :--- | :--- | :--- |
| Range 2 | 1.0 us | 10 us |
| Range 3 | 10 us | 100 us |

SYNC Dutput. This output is used to trigger the scape time base. The output is a TTL level 100 ns (approx.) pulse capable of driving a fifty Ohm load. This output precedes the output at (5) if the two position LEAD-LAG switch is in the LEAD position. This output follows the output at (5) if the switch is in the LAG position. The delay range is variable from 0.1 us to 100 us. The external trigger signal is applied at this input when the EXT-INT toggle switch is in the EXT position.
(5) DUT Connectors. $N$ connectors provide output to a 50 Ohm load.
(6) PW Control. A one turn control and 3 position range switch which varies the positive output pulse width from 0.1 us to 100 us. The minimum and maximum PW for each range and the corresponding maximum PRF are as follows. Note that the unit may fail if operated at duty cycles exceeding the above.

PW min

Range 1

Range 2

Riange 3
0.145

F'RF max 10 kHz
1.0 us

PRF max 10 kHz

10 us
FRF max 1 kHz

FW max
1.0 us
FRF max 10 kHz
10 us
PRF max 1
( kHz
PRF max 100 Hz
(日) AMP Control. A one turn control which varies the output pulse amplitude from 0 to 500 V .
(9) EXT-INT-MAN Control. With this toggle switch in the INT position, the PRF of the AVR unit is controlled via an internal clock which in turn is controlled by the PRF control. With the toggle switch in the EXT position, the AUR unit requires a 0.2 us TTL level pulse applied at the SYNC input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source. With the MODE A-B switch in the $A$ position the output pulse width is controlled by the FW controls. With the MODE A-B switch in the $B$ position, the output pulse width equals the input trigger pulse width. For single pulse operation, set the INT-EXT-MAN switch in the MAN position.
(10) SINGLE FULSE. For single pulse manual operation, set the front panel INT-EXT-MAN switch in the MAN position and push the SINGLE PULSE button.
(11) MODE $A-B$. For output pulse width control via the FW controls, the MODE switch should be in the A position. When triggering via an externally applied TTL level trigger pulse, the output pulse width equals the input trigger pulse width if the MODE switch is in the $B$ position.
(12) FOLARITY. The output pulse polarity is controlled by this two position switch. CAUTION: To avoid stressing the output stage, it is recommended that the output amplitude be reduced to near zero before changing the output polarity.
(13) DVERLDAD INDICATDR. AVR-5-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument GFF and turn the indicator light $O N$. The light will stay $O N$ (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn $O N$ (i.e. light $O F F$ ) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overlgad condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:

1) Feducing PRF (i.e. switch to a lower range)
2) Reduring pulse width (i.e. switch to a lower range)
3) Removing output load short circuit (if any)

Fig. 3 BACK PANEI CONTROLS

(1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse ( 1.0 A SB).
(2) 2.OA SB. Fuse which protects the output stage if the output duty cycle rating is exceeded.
(3) EA. To voltage control the output amplitude, set the switch in the EXT position and apply 0 to +10 volts between terminal $A$ and ground (Rin > 1OK). (option).
(4) EW. To voltage control the output pulse width, set the switch in the EXT position and apply 0 to +10 volts between terminal $A$ and ground (Rin > 1OK). (option).



## SYSTEM DESCRIPTIDN AND REPAIR PROCEDURE

CAUTIDN: Potentials as high as 550 volts DC are employed in the interior of this instrument so extreme caution must be exercised when attempting repairs. The following parts may be at high potential:
a) Fart No. SLRS-A-P and Part No. SLRS-A-N (and associated leads and capacitors).
b) Fins 1 and 2 on module AVF-SB-FS (and associated leads and capacitors).

The AVR-SB-P-C consists of the following basic moduless

1) AVR-5B-PG pulse generator modules ( P and $N$ )
2) AVR-5B-PS power supply module
3) AVR-5B-PW pulse width module
4) AVR-5B-CL clock module
5) $+36,+40,+24$ volt power supplies

The modules are interconnected as shown in Fig. 4.
In the event of an instrument malfunction, it is most likely that the 2.0 A slow blow fuse or the main power fuse on the rear panel has blown. Replace if necessary. If the unit still does not function, it is most likely that some of the output switching elements (IRFAGSO) may have failed due to an output short circuit condition or to a high duty cycle condition. The switching elements may be accessed by removing the top cover plate. The cover plate is removed by removing the 4 Phillips screws on the back panel. NOTE: First turn off the prime power. CAUTION: Thoroughly ground the IRFAG50 cases to discharge the 550 volts power supply potential. The IRFAGSO may be removed from the mounting bracket and checked on a curve tracer and replaced if necessary AVTECH Part No. SLFS-A consists of the two transistors mounted on the bracket with insulating washers, 1 K resistors and output cable.
schrobf 06.22 .92 edition B
-EW
-EA

