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## INSTRUCTIONS

MODEL AVR-A-1-PS PULSE GENERATOR
S.N.:

## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

Fig. 1 TEST ARRANGEMENT


1) The equipment should be connected in the general fashion shown above. Since the AVR unit provides an output pulse rise time as low as 10 ns a fast oscilloscope (at least 50 MHz and preferably 200 MHz ) should be used to display the waveform. Also, if a load of other than 50 Ohm is employed, the length of coaxial cable between the AVR unit and the load should not exceed about 5 feet or the output waveform may be degraded by the resulting reflections.
2) The output PRF is equal to the input trigger pulse PRF.
3) The output pulse width is equal to the input trigger pulse width. Care should be taken to not operate with an output pulse width greater than 1 us as prolonged operation in this mode may very well result in equipment failure. Also, the maximum PRF or duty cycle (0.5\%) must not be exceeded for the same reason (see Fig. 2). Under simultaneous conditions of wide pulse width, high PRF and high load current, the bias voltage applied to the output power stage decreases and as a result the attainable output peak voltage decreases to less than 200 Volts and the rear panel $0.5 A$ SB fuse may blow. Under conditions of severe loading the output stage may be damaged.
4) The output pulse amplitude is controlled by means of the front panel one turn AMP control. To voltage control the output, set the rear panel switch in the EXT position and apply 0 to +10 Volts to the BNC connector $A\left(R_{I N} \geq 10 K\right)$. The output pulse amplitude and this control voltage are related linearly. Note that with the INT-EXT switch placed in the EXT position, the driving source impedance at connector A should be less than 1 K in order to obtain 0 V OUT with 0 Volts IN. With the connector A open-circuited, the output voltage may be as high as 10 Volts.
5) This unit is protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:
6) Reducing PRF (i.e. switch to a lower range)
7) Reducing pulse width (i.e. switch to a lower range)

Note that the overload light may come on when the prime power is applied. The light will extinguish after a few seconds and the unit will then function normally.
6) If the peak output current exceeds 6 Amperes, the output stage stops triggering and thereby protects the unit.
7) The unit can be converted from 110 to $220 \mathrm{~V} 50-60 \mathrm{~Hz}$ operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
8) For additional assistance:

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Fig. 3
BACK PANEL CONTROLS

(1)
(1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse ( 0.25 A SB).
(2) 0.5A SB. Fuse which protects the output stage if the output duty cycle rating is exceeded.
(3) EA. To voltage control the output amplitude, set the switch in the EXT position and apply 0 to +10 Volts to the BNC connector $A\left(R_{I N} \geq 10 \mathrm{~K}\right)$. (option).

Fig. 4


1) The AVR unit consists of three basic components or modules:
a) Metal chassis
b) AVR-PS module (Power Supply)
c) AVR-PG module (Pulse Generator)
d) OL-471 overload module

The modules are interconnected as shown above.
2) If the unit malfunctions, first check the rear panel $0.5 A \mathrm{SB}$ fuse and then disconnect from the 60 Hz supply and the trigger source and remove the four Phillips screws on the back panel of the unit. With the screws removed, the top cover may be slid off.
3) Reconnect to the 60 Hz source and check the voltage on the line connecting the AVR-PS output to the +24 V pin of the AVR-PG module. A voltage of +24 Volts should be recorded. If the voltage is substantially less than +24 Volts, disconnect the 60 Hz source and disconnect the line from the +24 Volt pin. Connect a 50 Ohm 8 Watt resistance to the output of the AVR-PS module. Reconnect to the 60 Hz source and measure the voltage across this resistor. A voltage of +24 Volts should be indicated. If the voltage is substantially less than +24 Volts the AVR-PS module is defective and should be either repaired or replaced. If the measured voltage is equal to +24 Volts then the SL4 switching elements in the AVR-PG module have probably failed. The SL4 switching elements are easily replaced by removing the cover plate on the instrument bottom side and extracting the SL4 switching elements from their sockets using a pair of needle nose pliers. Before attempting this first insure that the prime power is off and also briefly ground the metal tabs on the SL4 elements to the chassis as the bypass capacitors may be charged to 225 Volts. Replacement SL4 units must be ordered directly from Avtech. When reinstalling the SL4 units in their sockets, insure that the shortest of the three terminals is adjacent to the black dot on the AVR-PG chassis.

Feb. $8 / 96$

- EA

Disk: $A V R-A-1$, $A V R-A-1-P S$
Tome = A1PS.MOD

Special for SDL

